

USER MANUAL

**SP-S154 / SP-S174
SP-S214**

Intel® 7th Gen. Core™ i7/i5/i3
Processor High Performance
and Low Power Panel PC

SP-SXX4 M3

SP-S154 / SP-S174 / SP-S214

High Performance Panel PC

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DISCLAIMER

This user's manual is meant to assist users in installing and setting up the system. The information contained in this document is subject to change without any notice.

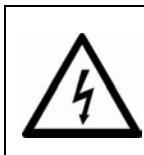
CE NOTICE

This is a class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

FCC NOTICE

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

You are cautioned that any change or modifications to the equipment not expressly approve by the party responsible for compliance could void your authority to operate such equipment.



CAUTION: Danger of explosion may occur when the battery is incorrectly replaced. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.



WARNING: Some internal parts of the system may have high electrical voltage. We strongly recommend that only qualified engineers are allowed to service and disassemble the system. If any damages should occur on the system and are caused by unauthorized servicing, it will not be covered by the product warranty.

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Revision History

The revision history of SP-S154 / SP-S174 / SP-S214 User Manual is described below:

Version No.	Revision History	Page No.	Date
M3	• Revised the Top View diagram of System Main Board BM-2502.	3-5	2021/03
	• Revised the Jumper Settings of BM-2502RB-**N.	3-6	
M2	In the Section 2.4 Safety Precautions, added the statement: The power cord with the plug shall be connected to a socket-outlet with earthing connections.	2-18	2019/12
M1	Initial Release	-	2019/06

1

Introduction

This chapter provides the introduction for the SP-S154 / SP-S174 / SP-S214 system as well as the framework of the user manual.

The following topic is included:

- About This Manual

1.1 About This Manual

Thank you for purchasing our SP-S154 / SP-S174 / SP-S214 system. The SP-S154 / SP-S174 / SP-S214 is an updated system designed to be comparable with the highest performance of IBM AT personal computers. The SP-S154 / SP-S174 / SP-S214 provides faster processing speed, greater expandability and can handle more tasks than before. This manual is designed to assist you how to install and set up the whole system. It contains 5 chapters and 2 appendixes. Users can configure the system according to their own needs. This user manual is intended for service personnel with strong hardware background. It is not intended for general users.

The following section describes the structure of this user manual.

Chapter 1 Introduction

This chapter introduces the framework of this user manual.

Chapter 2 Getting Started

This chapter describes the package contents and system specifications, and illustrates the physical appearances for the SP-S154 / SP-S174 / SP-S214 system. Read the safety reminders carefully on how to take care of your system properly.

Chapter 3 System Configuration

This chapter describes the locations and functions of the system motherboard & touch control board components. You will learn how to properly configure the connectors and system configuration jumpers on the motherboard and touch control board, and configure the system to meet your own needs.

Chapter 4 Software Utilities

This chapter introduces how to install Intel® Chipset Software Installation Utility, Intel® Management Engine Interface Driver Utility, Graphics Driver Utility, LAN Driver Utility, Sound Driver Utility, Intel® USB 3.0 Extensible Host Controller Driver Utility, eGalaxTouch Driver Utility and Intel® Rapid Storage Technology Installation Utility.

Chapter 5 BIOS Setup

This chapter provides BIOS setup information.

Appendix A System Diagrams

This appendix provides the exploded diagrams and part numbers of the SP-S154 / SP-S174 / SP-S214.

Appendix B Technical Summary

This appendix provides the information about the system block diagram, allocation maps for system resources, Watchdog Timer Configuration and Flash BIOS Update.

2

Getting Started

This chapter provides the information for the SP-S154 / SP-S174 / SP-S214 system. It describes how to set up the system quickly and outlines the system specifications.

The following topics are included:

- Package List
- System Overview
- System Specification
- Safety Precautions

Experienced users can go to Chapter 3 System Configuration on page 3-1 for a quick start.

2.1 Package List

If you discover any of the items listed below are damaged or lost, please contact your local distributor immediately.

SP-S154

Item	Q'ty
SP-S154	1
Manual / Driver DVD	1
Quick Guide	1
SATA Cable	2

SP-S174

Item	Q'ty
SP-S174	1
Manual / Driver DVD	1
Quick Guide	1
SATA Cable	2

SP-S214

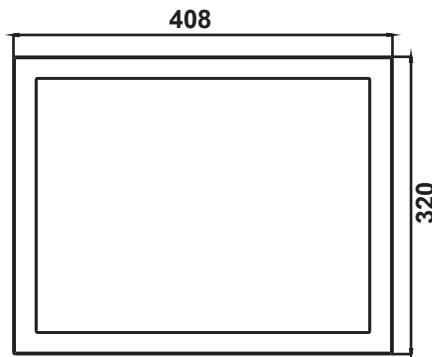
Item	Q'ty
SP-S214	1
Manual / Driver DVD	1
Quick Guide	1
SATA Cable	2

2.2 System Overview

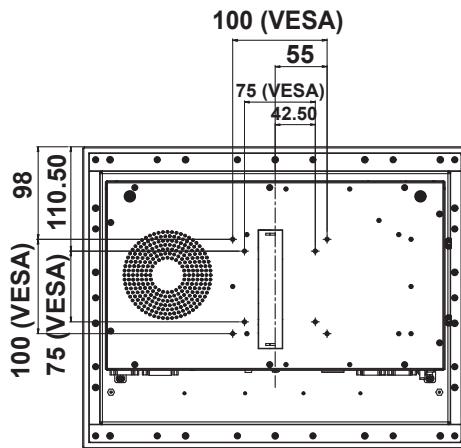
SP-S154

Unit: mm

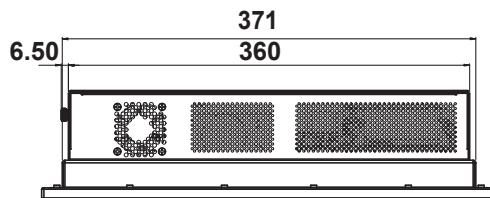
Front View



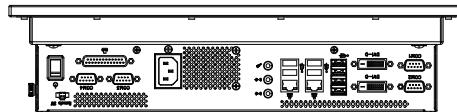
Rear View



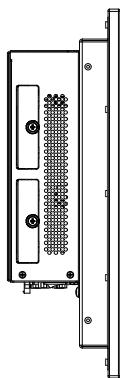
Top View



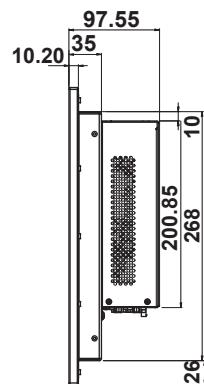
Bottom View



Left Side View

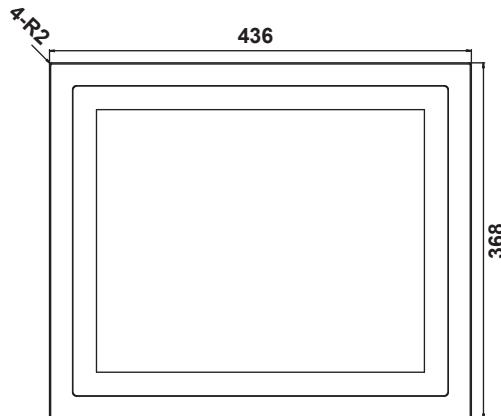


Right Side View

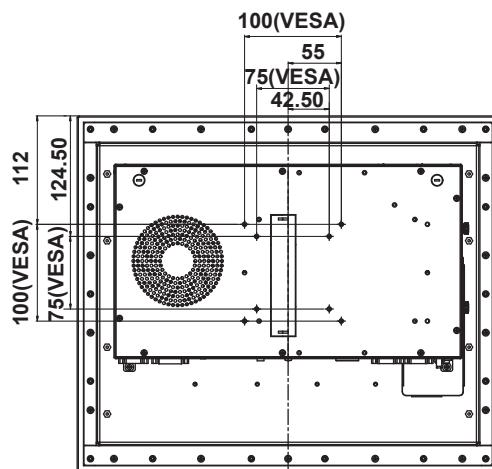


SP-S174

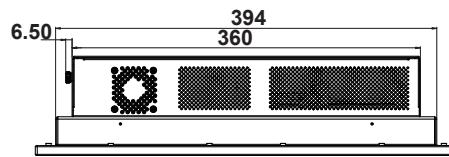
Front View



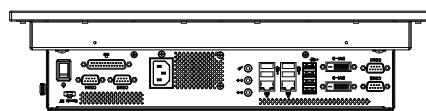
Rear View



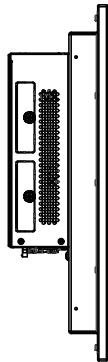
Top View



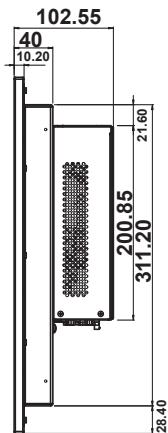
Bottom View



Left Side View

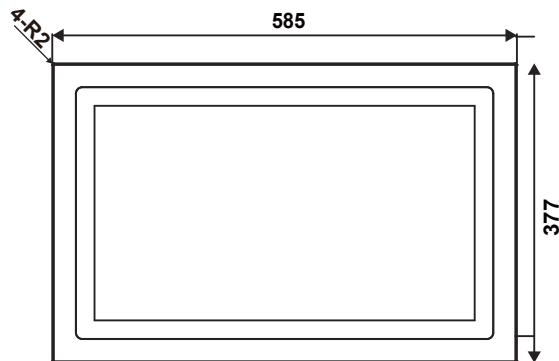


Right Side View

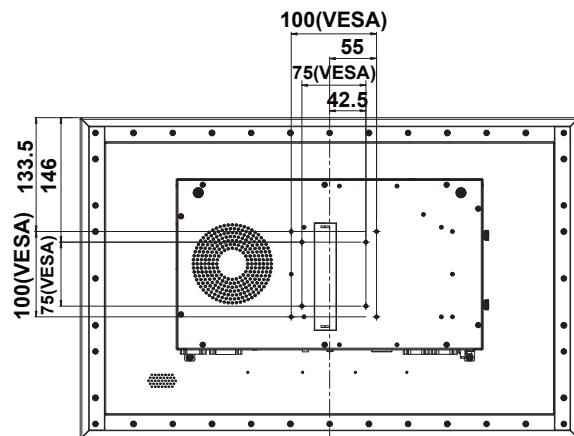


SP-S214

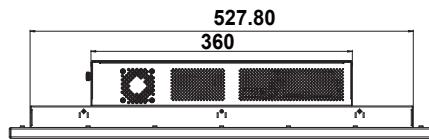
Front View



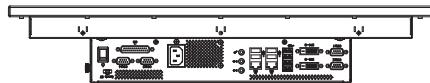
Rear View



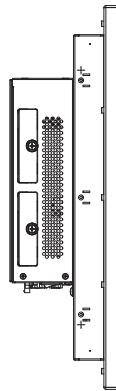
Top View



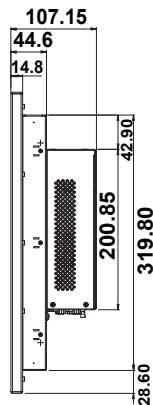
Bottom View



Left Side View



Right Side View



2.3 System Specifications

SP-S154

System	
CPU Support	Core™ i7-7700T, Quad Core, 2.9GHz, 8M cache Core™ i5-7500T, Quad Core, 2.7GHz, 6M cache Core™ i3-7101TE, Dual Core, 3.4GHz, 3M cache Celeron® G3930TE, Dual Core, 2.7GHz, 2M cache
Chipset	Intel® Q170/H110
Memory Support	2 x DDR4 2133MHz SO-DIMM sockets (up to 32GB)
Watchdog	1~255 seconds watchdog timer selectable
Storage	2 x 2.5" SATA III HDD/SSD (support RAID 0/1 in Q170 SKU only)
Front Bezel	Stainless steel
IP65	Yes (for front panel only)
Power Input	AC input ATX 250W
Operating System	Windows 10 IoT Enterprise (64bit)
Dimensions (W x H x D)	408 x 320 x 97.55mm
System Weight	8.1kg
Mounting Support	Wall mount / VESA 75,100 / Panel mount
Certifications	FCC / CE
I/O Ports	
Display	2 x DVI-D
USB	4 x USB 3.0 & 4 x USB 2.0
Audio	1 x Line In / 1 x Line Out / 1 x Mic In
LAN	2 x RJ45 ports LAN1: Intel® PHY1219LM (10/100/1000 Mbps) LAN2: Intel® LAN 210AT (10/100/1000 Mbps) Support Wake-On-LAN, PXE
Serial Ports	4 COM ports COM1 for RS-232/422/485 COM2/COM3/COM4 for RS-232 COM1/COM2/COM4 support +5V / +12V / RI selectable
Expansion Slots	1x PCIe (x16) 1x half-sized Mini PCIe
Antenna Hole	2 x antenna holes
Power On/Off	1 x power button switch

Chapter 2 Getting Started

Parallel Port	1 x 2-pin connector for remote power button 1 x Parallel Port (SPP/EPP/ECP)
Display	
LCD	15" TFT LCD(LED) resolution XGA (4:3) 1024x768
Touchscreen	
Environment	
Operating Temp.	HDD: 0°C~45°C (32°F~113°F) SSD: 0°C~50°C (32°F~122°F)
Storage Temp.	-20°C~80°C (-4°F~176°F)
Humidity	20%~90% (no condensation)

SP-S174

System	
CPU Support	Core™ i7-7700T, Quad Core, 2.9GHz, 8M cache Core™ i5-7500T, Quad Core, 2.7GHz, 6M cache Core™ i3-7101TE, Dual Core, 3.4GHz, 3M cache Celeron® G3930TE, Dual Core, 2.7GHz, 2M cache
Chipset	Intel® Q170/H110
Memory Support	2 x DDR4 2133MHz SO-DIMM sockets (up to 32GB)
Watchdog	1~255 seconds watchdog timer selectable
Storage	2 x 2.5" SATA III HDD/SSD (support RAID 0/1 in Q170 SKU only)
Front Bezel	Stainless steel
IP65	Yes (for front panel only)
Power Input	AC input ATX 250W
Operating System	Windows 10 IoT Enterprise (64bit)
Dimensions (W x H x D)	436 x 368 x 102.55mm
System Weight	9.1kg
Mounting Support	Wall mount / VESA 75,100 / Panel mount
Certifications	FCC / CE
I/O Ports	
Display	2 x DVI-D
USB	4 x USB 3.0 & 4 x USB 2.0
Audio	1 x Line In / 1 x Line Out / 1 x Mic In
LAN	2 x RJ45 ports LAN1: Intel® PHYI219LM (10/100/1000 Mbps) LAN2: Intel® LAN 210AT (10/100/1000 Mbps) Support Wake-On-LAN, PXE
Serial Ports	4 COM ports COM1 for RS-232/422/485 COM2/COM3/COM4 for RS-232 COM1/COM2/COM4 support +5V / +12V / RI selectable
Expansion Slots	1 x PCIe (x16) 1 x half size mini
Antenna Hole	2 x antenna holes
Power On/Off	1x power button switch 1x 2-pin connector for remote power button
Parallel Port	1 x Parallel Port (SPP/EPP/ECP)

Display	
LCD	17" TFT LCD(LED) resolution SXGA (5:4) 1280x1024
Touchscreen	17" P-CAP touch panel / 17" resistive touch panel
Environment	
Operating Temp.	HDD: 0°C~45°C (32°F~113°F) SSD: 0°C~50°C (32°F~122°F)
Storage Temp.	-20°C~80°C (-4°F~176°F)
Humidity	20%~90% (no condensation)

SP-S214

System	
CPU Support	Core™ i7-7700T, Quad Core, 2.9GHz, 8M cache Core™ i5-7500T, Quad Core, 2.7GHz, 6M cache Core™ i3-7101TE, Dual Core, 3.4GHz, 3M cache Celeron® G3930TE, Dual Core, 2.7GHz, 2M cache
Chipset	Intel® Q170/H110
Memory Support	2 x DDR4 2133MHz SO-DIMM sockets (up to 32GB)
Watchdog	1~255 seconds watchdog timer selectable
Storage	2 x 2.5" SATA III HDD/SSD (support RAID 0/1 in Q170 sku only)
Front Bezel	Stainless steel
IP65	Yes (for front panel only)
Power Input	AC input ATX 250W
Operating System	Windows 10 IoT Enterprise (64bit)
Dimensions (W x H x D)	585 x 377 x 107.15mm
System Weight	11.8kg
Mounting Support	Wall mount / VESA75,100 / Panel mount
Certifications	FCC / CE
I/O Ports	
Display	2 x DVI-D
USB	4 x USB 3.0 & 4 x USB 2.0
Audio	1 x Line In / 1 x Line Out / 1 x Mic In
LAN	2 x RJ45 ports LAN1: Intel® PHY1 219LM (10/100/1000 Mbps) LAN2: Intel® LAN 210AT (10/100/1000 Mbps) Support Wake-on-Lan, PXE
Serial Ports	4 COM ports COM1 for RS-232/422/485 COM2/COM3/COM4 for RS-232 COM1/COM2/COM4 support +5V / +12V / RI selectable
Expansion Slots	1x PCIe (x16) 1x half size mini PCIe
Antenna Hole	2 x antenna holes
Power On/Off	1x power button switch 1x 2-pin connector for remote power button
Parallel Port	1 x Parallel Port (SPP/EPP/ECP)

Display	
LCD	21.5" TFT LCD(LED) resolution FHD (16:9) 1920x1080
Touchscreen	21.5" P-CAP touch panel
Environment	
Operating Temp.	HDD: 0°C~45°C (32°F~113°F) SSD: 0°C~50°C (32°F~122°F)
Storage Temp.	-20°C~80°C(-4°F~176°F)
Humidity	20%~90% (no condensation)

2.4 Safety Precautions

Before operating this system, read the following information carefully to protect your systems from damages, and extend the life cycle of the system.

1. Check the Line Voltage
 - The operating voltage for the power supply should be within the range of 100V to 240V AC; otherwise, the system may be damaged.
2. Environmental Conditions
 - Place your SP-S154 / SP-S174 / SP-S214 on a sturdy, level surface. Be sure to allow enough space around the system to have easy access needs.
 - Avoid installing your SP-S154 / SP-S174 / SP-S214 system in extremely hot or cold places.
 - Avoid direct sunlight exposure for a long period of time (for example, in a closed car in summer time. Also avoid the system from any heating device.). Or do not use SP-S154 / SP-S174 / SP-S214 when it has been left outdoors in a cold winter day.
 - Avoid moving the system rapidly from a hot place to a cold place, and vice versa, because condensation may occur inside the system.
 - Protect your SP-S154 / SP-S174 / SP-S214 from strong vibrations which may cause hard disk failure.
 - Do not place the system too close to any radio-active device. Radio-active device may cause signal interference.
 - Always shut down the operating system before turning off the power.
3. Handling
 - Avoid placing heavy objects on the top of the system.
 - Do not turn the system upside down. This may cause the hard drive to malfunction.
 - Do not allow any objects to fall into this device.
 - If water or other liquid spills into the device, unplug the power cord immediately.
4. Good Care
 - When the outside case gets stained, remove the stains using neutral washing agent with a dry cloth.
 - Never use strong agents such as benzene and thinner to clean the surface of the case.
 - If heavy stains are present, moisten a cloth with diluted neutral washing agent or alcohol and then wipe thoroughly with a dry cloth.
 - If dust is accumulated on the case surface, remove it by using a special vacuum cleaner for computers.
 - The power cord with the plug shall be connected to a socket-outlet with earthing connections.

3

System Configuration

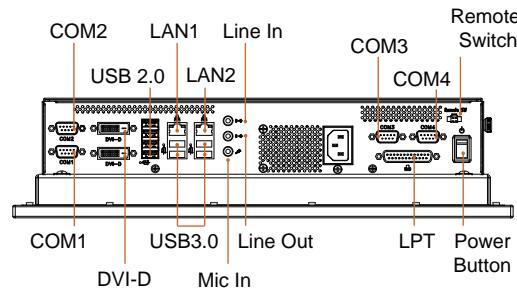
This chapter contains helpful information about the jumper & connector settings, and component locations for the main board and touch control board.

The following topics are included:

- Connector & Jumper Quick Reference Table
- System Main Board Component Locations
- How to Set Jumpers
- Setting Main Board Connectors and Jumpers
- Touch Control Board Component Locations
- Setting Touch Control Board Connectors and Jumpers

3.1 External I/O Ports Diagram

3.1.1 SP-S154 / SP-S174 / SP-S214 Rear I/O Ports Diagram



3.2 Jumper & Connector Quick Reference Table

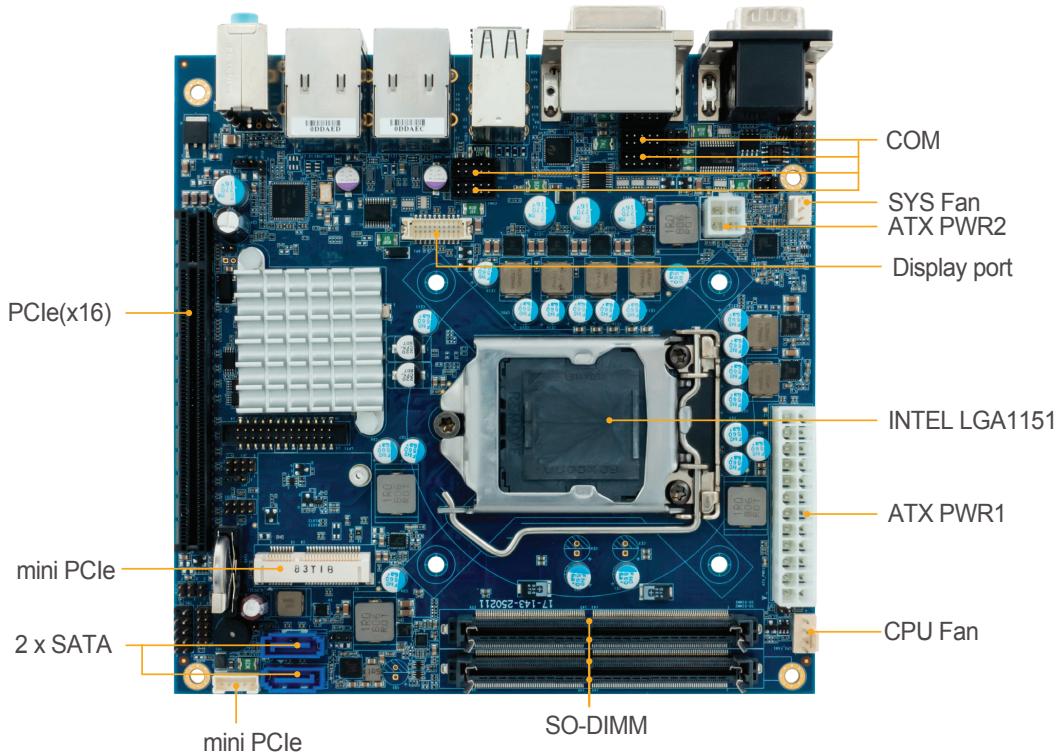
JUMPER Description	NAME
Clear CMOS Data Selection	JCMOS1
COM1 Pin9 RI/5V/12V Selection	JP_COM1
COM2 Pin9 RI/5V/12V Selection	JP_COM2
COM4 Pin9 RI/5V/12V Selection	JP_COM4
LAN2 Port Enable Selection	JP_LAN2
VCCIO Voltage Selection	JP1
AT / ATX Mode Selection	JP2 & JP3
Intruder Detection Selection	JP4
BIOS Recovery Mode Selection	JP5
COM1 RS-232/422/485 Selection	JP6
SPI Override Protection Selection	JP9
COM1 RS-485 Auto Flow Selection	JP11
Clear RTC Data Selection	JRTC1

CONNECTOR Description	NAME
COM Ports	COM1, COM2, COM3, COM4
COM3, COM4 Pin Headers	COM3, COM4
(Optional) COM5, COM6 Ports	COM5, COM6
(Optional) COM5, COM6 Pin Headers	COM5, COM6
2 x LAN Ports and 4 x USB 3.0 Ports	LAN1_USB1, LAN2_USB2
4 x 1 USB 2.0 Connectors	USB1
Internal USB 2.0 Connector	USB2
2 x DVI-D Connectors	DVI1
(Optional) Display Connector	DP1

CONNECTOR Description	NAME
HD Audio Connector	AUDIO1
(Optional) Printer Port (LPT) Connector	LPT1
Front Panel Connector	FP1
CPU Fan Connector	CPU_FAN1
System Fan Connector	SYS_FAN1
Half-Sized Mini PCI Express Slots	M_PCIE1
PCI Express Slots (PCIe (x16))	PCI_E1
SATA 3.0 Connectors	SATA1, SATA2
ATX Power Input Connectors	ATX_PWR1, ATX_PWR2
SPI Connector	JP7
Case Open Detection Connector	JP10
Low Pin Count (LPC) Connector	JP12
DDR4 SO-DIMM memory socket	SO-DIMM1
DDR4 SO-DIMM memory socket	SO-DIMM2

3.3 Component Locations of System Main Board

3.3.1 Top View of System Main Board BM-2502



3.3.2 Jumper Settings of BM-2502RB-**N

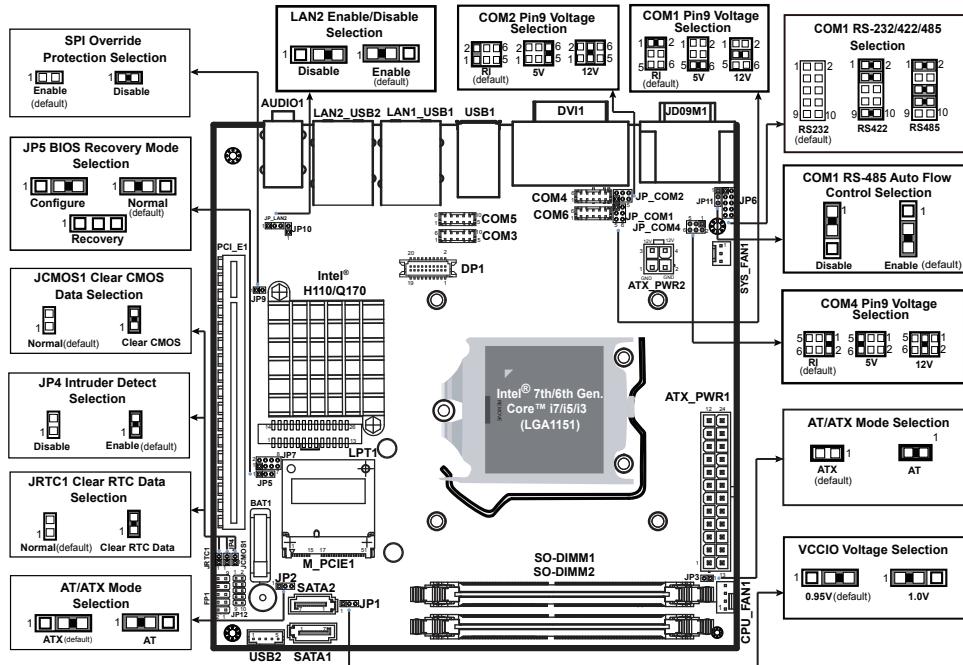


Figure 3-1. BM-2502 Component Location (Top View)

	WARNING: Always disconnect the power cord when you are working with connectors and jumpers on the main board. Make sure both the system and peripheral devices are turned OFF as sudden surge of power could damage sensitive components. Make sure BM-2502 is properly grounded.
	CAUTION: Observe precautions while handling electrostatic sensitive components. Make sure to ground yourself to prevent static charge while you are working on the connectors and jumpers. Use a grounding wrist strap and place all electronic components in any static-shielded devices.
	CAUTION: Always touch the motherboard components by the edges. Never touch components such as a processor by its pins. Take special care while you are holding electronic circuit boards by the edges only. Do not touch the main board components.

3.3.3 Bottom View of BM-2502RB-**N

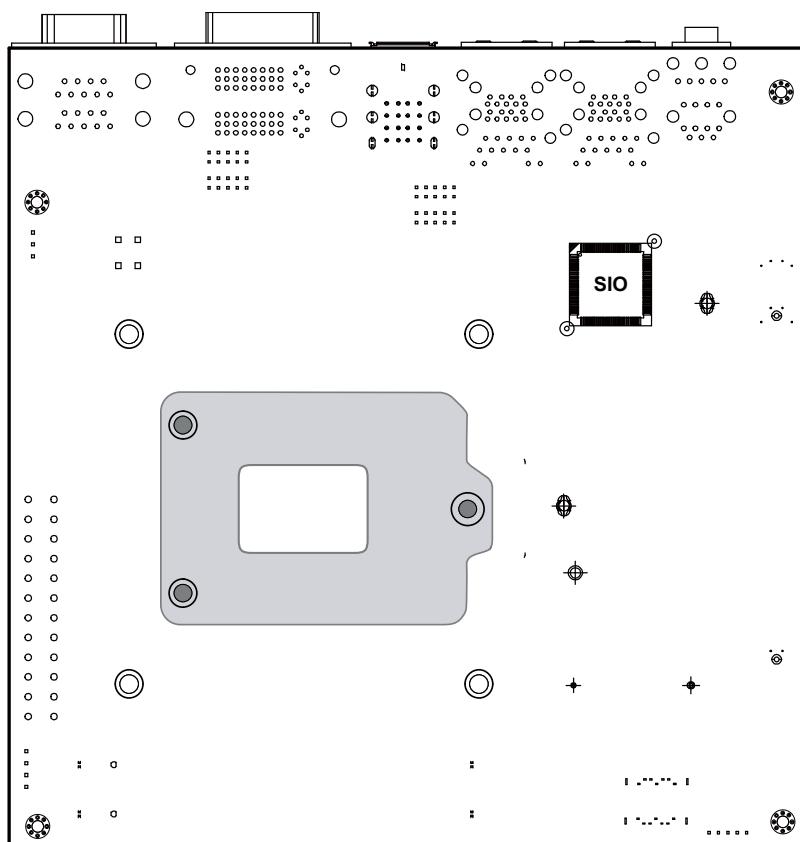


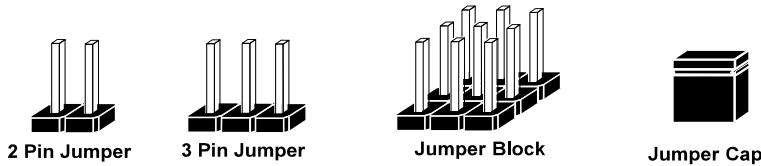
Figure 3-2. BM-2502 Component Location (Bottom View)

3.4 How to Set Jumpers

You can configure your board by setting the jumpers. A jumper consists of two or three metal pins with a plastic base mounted on the card. By using a small plastic "cap", also known as the jumper cap (with a metal contact inside), you are able to connect the pins. So you can configure your hardware settings by "opening" or "closing" jumpers.

Jumpers can be combined into sets that are called jumper blocks. When jumpers are all in the block, you have to put them together to set up the hardware configuration. The figure below shows what this looks like.

Jumpers & Caps

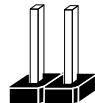


If a jumper has three pins, for example, labeled 1, 2 and 3. You can connect pins 1 and 2 to create one setting and shorting. You can also select to connect pins 2 and 3 to create another setting. The format of the jumper picture will be illustrated throughout this manual. The figure below shows different types of jumpers and jumper settings.

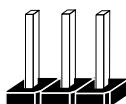
Jumper diagrams



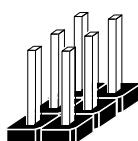
Jumper Cap looks like this



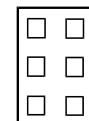
2 pin Jumper looks like this



3 pin Jumper looks like this



Jumper Block looks like this



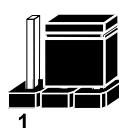
Jumper settings



2 pin Jumper closed(enabled)
looks like this



1



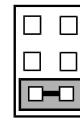
3 pin Jumper
2-3 pin closed(enabled)
looks like this



1



Jumper Block
1-2 pin closed(enabled)
looks like this



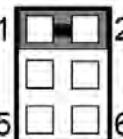
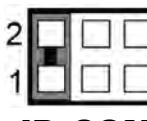
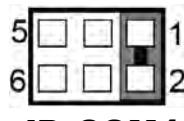
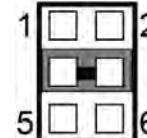
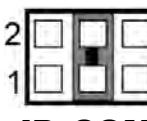
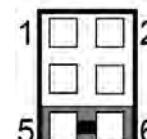
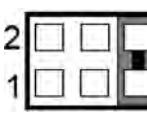
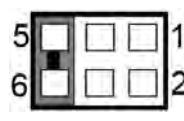
1 2

3.5 Setting Main Board Connectors and Jumpers

3.5.1 COM1, COM2, COM4 Port Pin9 Definition Selection Guide

Jumper Location: JP_COM1 & JP_COM2 & JP_COM4

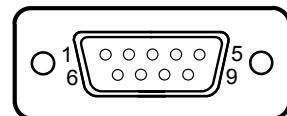
Description: COM1, COM2 and COM4 Port pin9 RI/+5V/+12V Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION		
		JP_COM1	JP_COM2	JP_COM4
RI	1-2 (Default Setting)	 JP_COM1	 JP_COM2	 JP_COM4
12V	3-4	 JP_COM1	 JP_COM2	 JP_COM4
5V	5-6	 JP_COM1	 JP_COM2	 JP_COM4

3.5.2 COM Ports

COM1(RS-232/422/485) Connector Pin Assignment:

PIN	ASSIGNMENT		
	RS-232	RS-422	RS-485
1	DCD#	TX-	RS-485-
2	RX	TX+	RS-485+
3	TX	RX+	X
4	DTR#	RX-	X
5	GND	GND	GND
6	DSR#	X	X
7	RTS#	X	X
8	CTS#	X	X
9	RI#	X	X



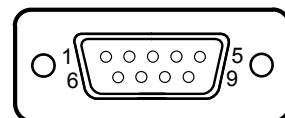
COM1

Notes:

- 1 COM1 is selectable as RS-232, RS-422, RS-485 by JP6.
- 2 Default setting is RS-232. Please see “**COM1 RS-232/422/485 Selection**” section for details.
- 3 COM1 Pin 9 is selectable for RI, +5V or +12V by jumper setting. Default setting is RI. Please see “**COM1, COM2, COM4 Port Pin9 Definition Selection Guide**” section for selection details.

COM2(RS-232) Connector Pin Assignment:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DCD#	6	DSR#
2	RX	7	RTS#
3	TX	8	CTS#
4	DTR#	9	RI#
5	GND	-	-



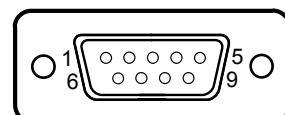
COM2

Note:

- 1 COM2 Pin 9 is selectable for RI, +5V or +12V by jumper setting. Default setting is RI. Please see “**COM1, COM2, COM4 Port Pin9 Definition Selection Guide**” section for selection details.

COM3(RS-232), COM4(RS-232) Connector Pin Assignment:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DCD#	6	DSR#
2	RX	7	RTS#
3	TX	8	CTS#
4	DTR#	9	RI#
5	GND	-	-



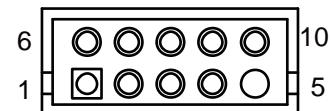
**COM3/
COM4**

Note:

1. COM4 Pin 9 is selectable for RI, +5V or +12V by jumper setting. Default setting is RI. Please see “**COM1, COM2, COM4 Port Pin9 Definition Selection Guide**” section for selection details.

COM3(RS-232), COM4(RS-232) Connector Pin Header Pin Assignment:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DCD#	6	DSR#
2	RX	7	RTS#
3	TX	8	CTS#
4	DTR#	9	RI#
5	GND	10	NC

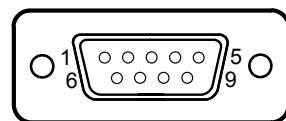


**COM3/
COM4
Pin Header**

Chapter 3 System Configuration

(Optional) COM5(RS-232), COM6 (RS-232) Connector Pin Assignment:

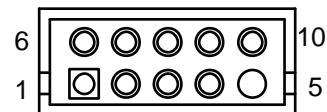
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DCD#	6	DSR#
2	RX	7	RTS#
3	TX	8	CTS#
4	DTR#	9	RI#
5	GND	-	-



**COM5/
COM6
(optional)**

(Optional) COM5(RS-232),COM6 (RS-232) Connector Pin Header Pin Assignment:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DCD#	6	DSR#
2	RX	7	RTS#
3	TX	8	CTS#
4	DTR#	9	RI#
5	GND	10	NC



**COM5/
COM6
Pin Header
(optional)**

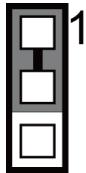
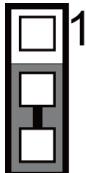
3.5.3 COM1 RS-232/422/485 Selection**Jumper Location:** JP6**Description:** COM1 RS-232/422/485 Selection

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
RS-232	<i>Open (Default Setting)</i>	
RS-422	1-2, 3-4, 9-10	
RS-485	1-2, 5-6, 7-8	

3.5.4 COM1 RS-485 Auto Flow Selection

Jumper Location: JP11

Description: COM1 RS-485 Auto Flow Selection

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
Disable	1-2	 JP11
Enable <i>(Default Setting)</i>	2-3	 JP11

3.5.5 LAN and USB 3.0 Ports

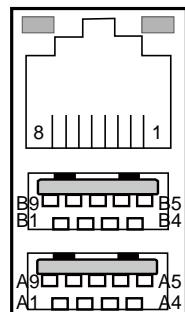
Port Name: LAN1_USB1 (rear I/O)

Description: LAN1 & Dual USB 3.0 Ports

LAN1 signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	MDI_0P	5	MDI_2N
2	MDI_0N	6	MDI_1N
3	MDI_1P	7	MDI_3P
4	MDI_2P	8	MDI_3N

Green/Orange Yellow



LAN LED Indicator:

Left Side LED

Green Color On	10/100Mbps LAN Speed Indicator
Orange Color On	Giga LAN Speed Indicator
Off	No LAN switch/hub connected

LAN1_USB1

Right Side LED

Yellow Color Blinking	LAN Message Active
Off	No LAN Message Active

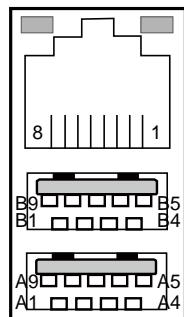
USB 3.0 signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
A1	VCC5	B1	VCC5
A2	USBP1N	B2	USBP2N
A3	USBP1P	B3	USBP2P
A4	GND	B4	GND
A5	RX1_DN	B5	RX2_DN
A6	RX1_DP	B6	RX2_DP
A7	GND	B7	GND
A8	TX1_DN	B8	TX2_DN
A9	TX1_DP	B9	TX2_DP

Port Name: LAN2_USB2 (rear I/O)**Description:** LAN2 Port & Dual USB3.0 Ports**LAN2 signals:**

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	MDI_0P	5	MDI_2N
2	MDI_0N	6	MDI_1N
3	MDI_1P	7	MDI_3P
4	MDI_2P	8	MDI_3N

Green/Orange Yellow

**LAN LED Indicator:****Left Side LED**

Green Color On	10/100Mbps LAN Speed Indicator
Orange Color On	Giga LAN Speed Indicator
Off	No LAN switch/hub connected

LAN2_USB2**Right Side LED**

Yellow Color Blinking	LAN Message Active
Off	No LAN Message Active

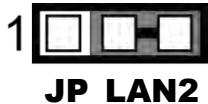
USB 3.0 signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
A1	VCC5	B1	VCC5
A2	USBP3N	B2	USBP4N
A3	USBP3P	B3	USBP4P
A4	GND	B4	GND
A5	RX3_DN	B5	RX4_DN
A6	RX3_DP	B6	RX4_DP
A7	GND	B7	GND
A8	TX3_DN	B8	TX4_DN
A9	TX3_DP	B9	TX4_DP

3.5.6 LAN2 Port Enable Selection

Jumper Location: JP_LAN2

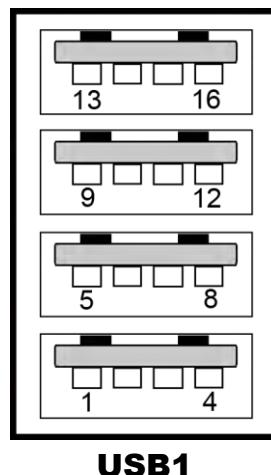
Description: LAN2 Port Enable Selection

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
Enable	<i>1-2 (Default Setting)</i>	
Disable	2-3	

3.5.7 4 x 1 USB 2.0 Connectors

Port Name: **USB1** (rear I/O)

Description: USB 2.0 Connectors x 4



USB 2.0 signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT
4	GND	3	USB2_P5_DP_L	2	USB2_P5_DN_L	1	VCC5
8	GND	7	USB2_P6_DP_L	6	USB2_P6_DN_L	5	VCC5
12	GND	11	USB2_P7_DP_L	10	USB2_P7_DN_L	9	VCC5
16	GND	15	USB2_P8_DP_L	14	USB2_P8_DN_L	13	VCC5

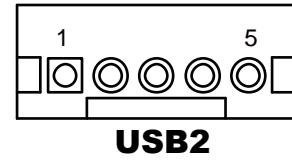
3.5.8 Internal USB 2.0 Connector

Connector Location: USB2

Description: Internal USB 2.0 Connector

USB 2.0 connector signals:

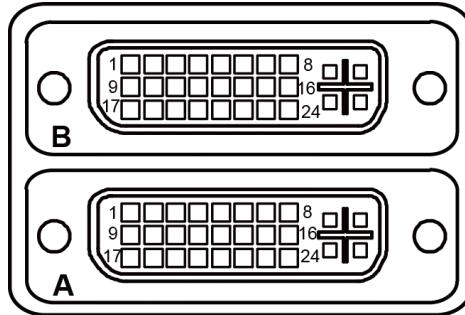
PIN	ASSIGNMENT
1	VCC5
2	USB2_P9_DN_L
3	USB2_P9_DP_L
4	GND
5	GND



3.5.9 Dual DVI-D Ports (DVI1)

Port Name: DVI1 (rear I/O)

Description: 2 x DVI (Digital Visual Interface) Integrated Connectors
(Supports only DVI-D signals)



DVI1 connector signals (A):

PIN	ASSIGNMENT	PIN	ASSIGNMENT
A1	DP_Data2-	A13	NC
A2	DP_Data2+	A14	+5V Power
A3	Ground	A15	Ground
A4	NC	A16	HOT Plug Detect
A5	NC	A17	DP_Data0-
A6	DP_Ctrl_Clock	A18	DP_Data0+
A7	DP_Ctrl_Data	A19	Ground
A8	CRT_VSYNC	A20	NC
A9	DP_Data1-	A21	NC
A10	DP_Data1+	A22	Ground
A11	Ground	A23	DP_Clock+
A12	NC	A24	DP_Clock-

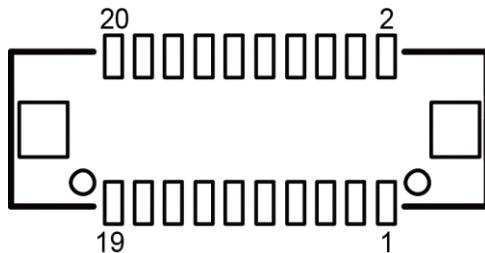
DVI1 connector signals (B):

PIN	ASSIGNMENT	PIN	ASSIGNMENT
B1	DP_Data2-	B13	NC
B2	DP_Data2+	B14	+5V Power
B3	Ground	B15	Ground
B4	NC	B16	HOT Plug Detect
B5	NC	B17	DP_Data0-
B6	DP_Ctrl_Clock	B18	DP_Data0+
B7	DP_Ctrl_Data	B19	Ground
B8	CRT_VSYNC	B20	NC
B9	DP_Data1-	B21	NC
B10	DP_Data1+	B22	Ground
B11	Ground	B23	DP_Clock+
B12	NC	B24	DP_Clock-

3.5.10 DisplayPort Connector (Optional)

Connector Location: DP1

Description: DisplayPort Connector



DP1 (optional)

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DP_C_DATA0+	2	GND
3	DP_C_DATA0-	4	DP_C_DATA1+
5	GND	6	DP_C_DATA1-
7	DP_C_DATA2+	8	GND
9	DP_C_DATA2-	10	DP_C_DATA3+
11	GND	12	DP_C_DATA3-
13	DP_C_AUX_ENJ	14	GND
15	DP_C_AUX+	16	DP_C_HPD
17	DP_C_AUX-	18	DP_VCC3_3
19	DP_VCC5	20	DP_VCC3_3

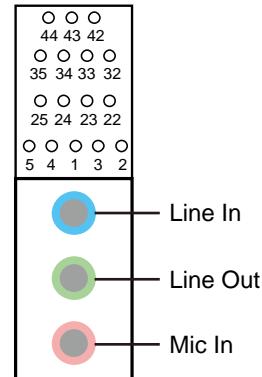
3.5.11 HD Audio Connector

Connector Location: AUDIO1

Description: HD Audio Connector for Line In/Line Out/Mic In.

Line In:

PIN	ASSIGNMENT
42	NC
43	NC
44	NC
32	HD LINE-IN-L
33	GND
34	GND
35	HD LINE-IN-R



Line Out:

PIN	ASSIGNMENT
22	LINE-OUT-L
23	GND
24	GND
25	LINE-OUT-R

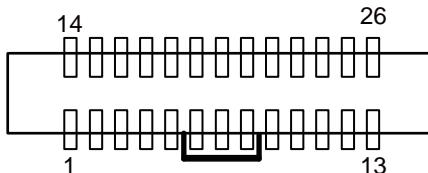
Mic In:

PIN	ASSIGNMENT
2	HD MIC1-L_L
3	GND
1	GND
4	GND
5	HD MIC1-R_L

3.5.12 Printer Port Connector (Optional)

Connector Location: LPT1

Description: Printer Port Connector



LPT1 (optional)

Printer Port (LPT1) signals:

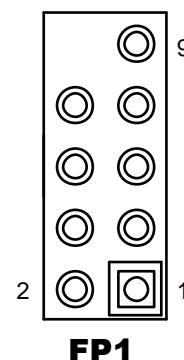
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	STRB#	14	AFD
2	D0	15	ERR#
3	D1	16	INIT
4	D2	17	SLIN
5	D3	18	GND
6	D4	19	GND
7	D5	20	GND
8	D6	21	GND
9	D7	22	GND
10	ACK#	23	GND
11	BUSY	24	GND
12	PE	25	GND
13	SLCT	26	GND

3.5.13 Front Panel Connector

Connector Name: FP1

Description: Front Panel Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	HDD+	2	PWR+
3	HDD-	4	PWR-
5	GND	6	Power Button
7	Reset Button	8	GND
9	5V	-	-



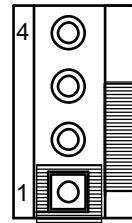
3.5.14 CPU Fan Connector

Connector Location: CPU_FAN1

Description: CPU Fan Connector

CPU Fan Connector (CPU_FAN1) signals:

PIN	ASSIGNMENT
1	GND
2	VCC12
3	TAC
4	CTL



CPU_FAN1

Notes:

1. CPU Fan speed mode can be set by BIOS.
2. Default BIOS setting is “Auto Duty-Cycle Mode”. Please see **Chapter 5** for more details.

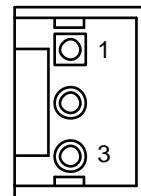
3.5.15 System Fan Connector

Connector Location: SYS_FAN1

Description: System Fan Connector

System Fan Connector (SYS_FAN1) signals:

PIN	ASSIGNMENT
1	GND
2	VCC12
3	NC

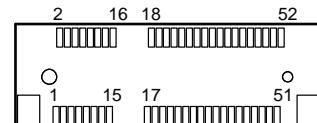


SYS_FAN1

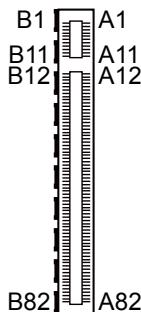
3.5.16 Half-Sized Mini PCI Express Slot (M_PCIE1)**Connector Location:** M_PCIE1**Description:** Half-Sized Mini-PCI Express Slot

Mini-PCI Express Slot 1 (M_PCIE1) signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	WAKEJ	2	VCC 3_3 SB
3	NC	4	GND
5	NC	6	VCC1_5
7	CLKREQJ	8	NC
9	GND	10	NC
11	CLK_DN	12	NC
13	CLK_DP	14	NC
15	GND	16	NC
17	NC	18	GND
19	NC	20	NC
21	GND	22	PLTRSTJ_BUF
23	PCIE_RXN	24	VCC3_3_SB
25	PCIE_RXP	26	GND
27	GND	28	VCC1_5
29	GND	30	SMB_CLK
31	PCIE_TXN	32	SMB_DATA
33	PCIE_TXP	34	GND
35	GND	36	USBN
37	GND	38	USBP
39	VCC3_3_SB	40	GND
41	VCC3_3_SB	42	NC
43	GND	44	NC
45	NC	46	NC
47	NC	48	VCC1_5
49	NC	50	GND
51	NC	52	VCC3_3_SB

**M_PCIE1**

Mini PCI Express is the successor of the Mini PCI card and provides an increased data throughput. The cards have a detached network interface and are equipped with one lane. They are used in particular in embedded designs or compact box PCs.

3.5.17 PCI Express Slots**Connector Location:** PCI_E1 (PCIe x16)**Description:** PCI Express Slots**PCI_E1**

PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT
B2	+ 12V	B1	+ 12V	A2	+ 12V	A1	PRSNT#1
B4	GND	B3	+ 12V	A4	GND	A3	+ 12V
B6	SMB_DATA	B5	SMB_CLK	A6	NC	A5	NC
B8	+ 3.3V	B7	GND	A8	NC	A7	NC
B10	+ 3.3V_AXU	B9	NC	A10	+ 3.3V	A9	+ 3.3V
-	-	B11	WAKE#	-	-	A11	PERST#
B12	RSVD	B13	GND	A12	GND	A13	REFCLK+
B14	HSOP0	B15	HSON0	A14	REFCLK-	A15	GND
B16	GND	B17	PRSNT#2	A16	HSIP0	A17	HSIN0
B18	GND	B19	HSOP1	A18	GND	A19	RSVD
B20	HSON1	B21	GND	A20	GND	A21	HSIP1
B22	GND	B23	HSOP2	A22	HSIN1	A23	GND
B24	HSON2	B25	GND	A24	GND	A25	HSIP2
B26	GND	B27	HSOP3	A26	HSIN2	A27	GND
B28	HSON3	B29	GND	A28	GND	A29	HSIP3
B30	RSVD	B31	PRSNT#2	A30	HSIN3	A31	GND
B32	GND	B33	HSOP4	A32	RSVD	A33	RSVD
B34	HSON4	B35	GND	A34	GND	A35	HSIP4
B36	GND	B37	HSOP5	A36	HSIN4	A37	GND
B38	HSON5	B39	GND	A38	GND	A39	HSIP5
B40	GND	B41	HSOP6	A40	HSIN5	A41	GND
B42	HSON6	B43	GND	A42	GND	A43	HSIP6
B44	GND	B45	HSOP7	A44	HSIN6	A45	GND
B46	HSON7	B47	GND	A46	GND	A47	HSIP7

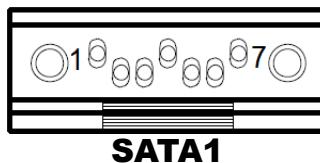
Chapter 3 System Configuration

PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT
B48	PRSNT#2	B49	GND	A48	HSIN7	A49	GND
B50	HSOP8	B51	HSON8	A50	RSVD	A51	GND
B52	GND	B53	GND	A52	HSIP8	A53	HSIN8
B54	HSOP9	B55	HSON9	A54	GND	A55	GND
B56	GND	B57	GND	A56	HSIP9	A57	HSIN9
B58	HSOP10	B59	HSON10	A58	GND	A59	GND
B60	GND	B61	GND	A60	HSIP10	A61	HSIN10
B62	HSOP11	B63	HSON11	A62	GND	A63	GND
B64	GND	B65	GND	A64	HSIP11	A65	HSIN11
B66	HSOP12	B67	HSON12	A66	GND	A67	GND
B68	GND	B69	GND	A68	HSIP12	A69	HSIN12
B70	HSOP13	B71	HSON13	A70	GND	A71	GND
B72	GND	B73	GND	A72	HSIP13	A73	HSIN13
B74	HSOP14	B75	HSIN14	A74	GND	A75	GND
B76	GND	B77	GND	A76	HSIP14	A77	HSIN14
B78	HSIP15	B79	HSIN15	A78	GND	A79	GND
B80	GND	B81	PRSNT#2	A80	HSIP15	A81	HSIN15
B82	RSVD	-	-	A82	GND	-	-

3.5.18 SATA 3.0 Connectors

Connector Location: SATA1

Description: Serial ATA (SATA) 6GB/s Connector

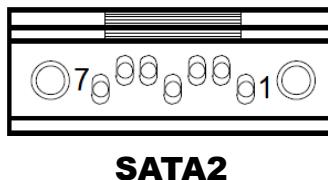


Serial ATA 6GB/s Connector (SATA1) signals:

PIN	ASSIGNMENT
1	GND
2	TXPC
3	TXNC
4	GND
5	RXNC
6	RXPC
7	GND

Connector Location: SATA2

Description: Serial ATA (SATA) 6GB/s Connector



Serial ATA 6GB/s Connector (SATA2) signals:

PIN	ASSIGNMENT
1	GND
2	TXPC
3	TXNC
4	GND
5	RXNC
6	RXPC
7	GND

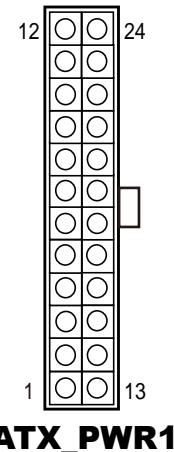
3.5.19 ATX Power Connectors

Connector Location: ATX_PWR1

Description: ATX Power Connector

ATX_PWR1 ATX Power Connector signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	+3.3V	13	+3.3V
2	+3.3V	14	12V
3	GND	15	GND
4	+5V	16	PSON
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	PWROK	20	-5V
9	5VSB	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	+3.3V	24	GND



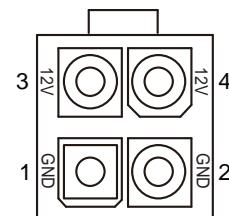
ATX_PWR1

Connector Location: ATX_PWR2

Description: DC Power Input Connector

ATX_PWR2 DC Power Input Connector signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	GND	3	+12V
2	GND	4	+12V

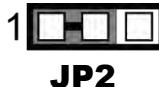


ATX_PWR2

3.5.20 AT / ATX Mode Selection

Jumper Location: JP2

Description: AT / ATX Mode Selection

SELECTION	JUMPTER SETTING	JUMPER ILLUSTRATION
AT	1-2	 JP2
ATX	2-3 (Default Setting)	 JP2

Jumper Location: JP3

Description: AT / ATX Mode Selection

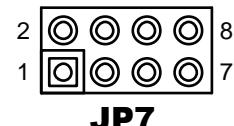
SELECTION	JUMPTER SETTING	JUMPER ILLUSTRATION
AT	Close	 JP3
ATX	Open (Default Setting)	 JP3

3.5.21 SPI Connector

Connector Location: JP7

Description: SPI (Serial Peripheral Interface Bus) Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	3.3V	2	GND
3	CSJ	4	CLK
5	MISO	6	MOSI
7	NC	8	NC



3.5.22 SPI Override Protection Selection

Jumper Location: JP9

Description: SPI Override Protection Selection

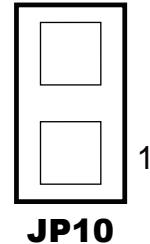
SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
Enable	<i>Open (Default Setting)</i>	
Disable	Close	

3.5.23 Case Open Detection Connector

Connector Location: JP10

Description: Case Open Detection Connector

PIN	ASSIGNMENT
1	Caseopen+
2	Caseopen-



3.5.24 Intruder Detection Selection

Jumper Location: JP4

Description: Intruder Detection Selection

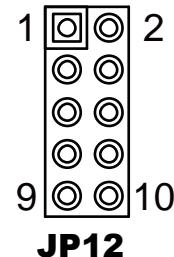
SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
Enable	<i>Close (Default Setting)</i>	A diagram of a two-pin jumper. Both Pin 1 and Pin 2 are connected to each other by a horizontal bar, forming a solid connection. Below the diagram is the label "JP4".
Disable	Open	A diagram of a two-pin jumper. There is no horizontal bar connecting Pin 1 and Pin 2. Below the diagram is the label "JP4".

3.5.25 Low Pin Count (LPC) Connector

Connector Location: JP12

Description: Low Pin Count (LPC) Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	CLK	2	GND
3	FRAME#	4	GND
5	PLTRST#	6	LPC_AD0
7	LPC_AD3	8	LPC_AD2
9	3.3V	10	LPC_AD1



3.5.26 Clear RTC Data Selection

Jumper Location: JRTC1

Description: Clear RTC Data Selection

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
Normal	<i>Open (Default Setting)</i>	 1 JRTC1
Clear RTC Data	Close	 1 JRTC1

3.5.27 VCCIO Voltage Selection

Jumper Location: JP1

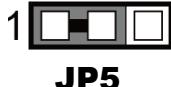
Description: VCCIO Voltage Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
0.95V	2-3 (Default Setting)	 JP1
1.0V	1-2	 JP1

3.5.28 BIOS Recovery Mode Selection

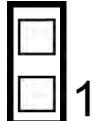
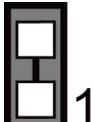
Jumper Location: JP5

Description: BIOS Recovery Mode Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
Normal	<i>1-2 (Default Setting)</i>	 JP5
Configure	2-3	 JP5
Recovery	Open	 JP5

3.5.29 Clear CMOS Data Selection**Jumper Location:** JCMOS1**Description:** Clear CMOS Data Selection

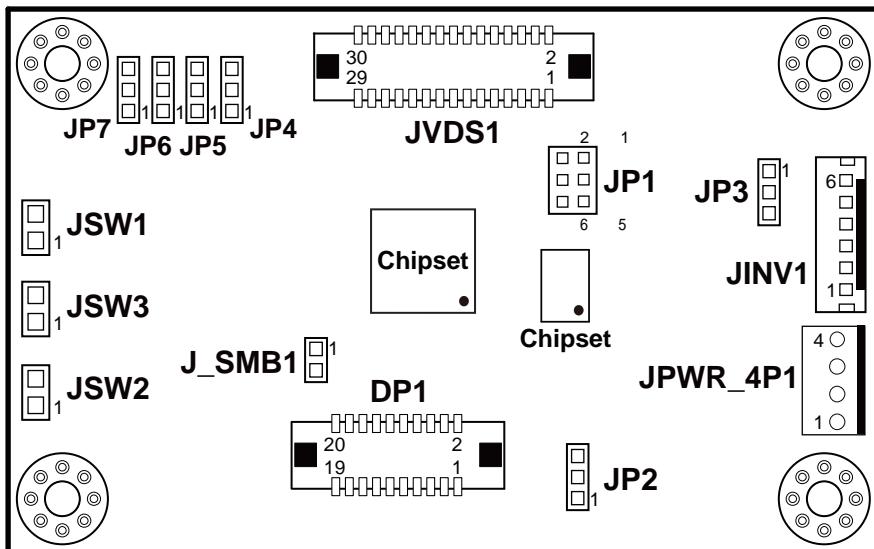
- Step 1. Remove the main power of the PC.
- Step 2. Close **JCMOS1** (pins 1-2) for 6 seconds by a cap.
- Step 3. Remove the cap which is just used on **JCMOS1** (1-2), so that **JCMOS1** returns to “OPEN”.
- Step 4. Power on the PC and the PC will then auto-reboot for once in order to set SoC’s register.
- Step 5. Done!

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
Normal	<i>Open (Default Setting)</i>	 JCMOS1
Clear CMOS Data	Close	 JCMOS1

Note: Please make sure the main power is off before you clear CMOS.

3.6 Component Locations of System Daughter Board

3.6.1 Top View of System Daughter Board BR-8080

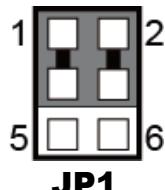
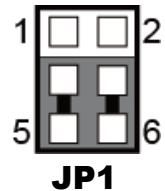
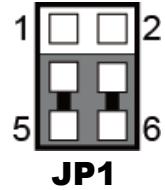


3.7 Setting Daughter Board Jumpers

3.7.1 VDD Selection

Jumper Location: JP1

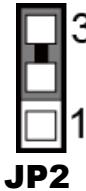
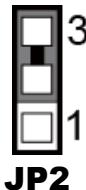
Description: VDD Selection

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
SP-S154 (3.3V)	1-3 2-4	 JP1
SP-S174 (5V)	3-5 4-6	 JP1
SP-S214 (5V)	3-5 4-6	 JP1

3.7.2 Backlight Voltage Selection

Jumper Location: JP2

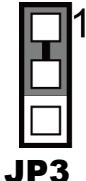
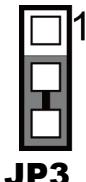
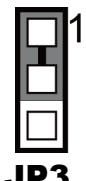
Description: Backlight Voltage Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
SP-S154 (3.3V)	1-2	 JP2
SP-S174 (3.3V)	2-3	 JP2
SP-S214 (5V)	2-3	 JP2

3.7.3 PWM Voltage Selection

Jumper Location: JP3

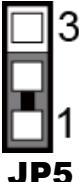
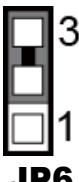
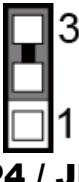
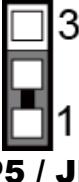
Description: PWM Voltage Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
SP-S154 (3.3V)	1-2	 JP3
SP-S174 (5V)	2-3	 JP3
SP-S214 (3.3V)	1-2	 JP3

3.7.4 LVDS (Low Voltage Differential Signaling) Resolution Selection

Jumper Location: JP4~JP7

Description: LVDS (Low Voltage Differential Signaling) Resolution Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
SP-S154 (1024x768) / 1CH,24bit	JP4(2-3) JP5(1-2) JP6(2-3) JP7(2-3)	 JP5
		 JP4 / JP6 / JP7
SP-S174 (1280x1024) / 2CH,24bit	JP4(2-3) JP5(1-2) JP6(1-2) JP7(2-3)	 JP4 / JP7
		 JP5 / JP6

Chapter 3 System Configuration

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
SP-S214 (1980x1080) / 2CH, 24bit	JP4(2-3) JP5(1-2) JP6(1-2) JP7(1-2)	 JP4
		 JP5 / JP6 / JP7

4

Software Utilities

This chapter provides the detailed information that guides users to install driver utilities for the system. The following topics are included:

- Installing Intel® Chipset Software Installation Utility
- Installing Graphics Driver Utility
- Installing LAN Driver Utility
- Installing Intel® Management Engine Interface Driver Utility
- Installing Sound Driver Utility
- Installing Intel® USB 3.0 Extensible Host Controller Driver Utility
- Installing eGalaxTouch Driver Utility
- Installing Intel Rapid Storage Technology Installation Utility

4.1 Introduction

Enclosed with the SP-S154/ SP-S174 / SP-S214 Series package is our driver utilities contained in a DVD-ROM disk. Refer to the following table for driver locations:

Filename (Assume that DVD- ROM drive is D:)	Purpose	OS			
		DOS	Win7 (32/64bit)	Win8.1 (64bit)	Win10 (64bit)
D:\Driver\Flash BIOS	For Aptio(UEFI) BIOS update utility	✓	✗	✗	✗
D:\Driver\Platform\Main Chip	Intel(R) Chipset Device Software Installation Utility	✗	✓	✓	✓
D:\Driver\Platform\Sky Lake_Graphics	Intel Sky Lake For VGA Driver installation	✗	✓	✓	✓
D:\Driver\Platform\LAN Chip	Intel I219LM & I210AT For LAN Driver installation	✗	✓	✓	✓
D:\Driver\Platform\ME	For Intel Management Engine Interface	✗	✓	✓	✓
D:\Driver\Platform\KMDFor Win7	Windows 7 update KMDF	✗	✓	✗	✗
D:\Driver\Platform\Sound Codec	Realtek ALC888 For Sound driver installation	✗	✓	✓	✓
D:\Driver\Platform\USB3	Intel(R) USB 3.0 eXtensible Host Controller	✗	✓	✗	✗
D:\Driver\Device\Touch	For eGalaxTouch Installation utility	✗	✓	✓	✓
D:\Driver\Platform\RST	Intel Rapid Storage Technology	✗	✓	✓	✓
D:\Driver\Platform\Kaby Lake_Graphics	Intel Kaby Lake For VGA Driver installation	✗	✗	✗	✓

Note: Install the driver utilities immediately after the OS installation is completed.

✗ : Not supported

✓ : Supported

4.2 Installing Intel® Chipset Software Installation Utility

4.2.1 Introduction

The Intel® Chipset Software Installation Utility installs the Windows *.INF files to the target system. These files outline to the operating system how to configure the Intel chipset components in order to ensure that the following functions work properly:

- Core PCI and ISAPNP Services
- PCIe Support
- SATA Storage Support
- USB Support
- Identification of Intel® Chipset Components in the Device Manager

4.2.2 Intel® Chipset Software Installation Utility

The utility pack is to be installed only for Windows® 7 / Windows® 8.1 / Win10 series, and it should be installed immediately after the OS installation is finished. Please follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-S154 / SP-S174 / SP-S214 and insert the driver disk.
- 2** Enter the **Main Chip** folder where the Chipset driver is located.
- 3** Click **SetupChipset.exe** file for driver installation.
- 4** Follow the on-screen instructions to install the driver.
- 5** Once the installation is completed, shut down the system and restart SP-S154 / SP-S174 / SP-S214 for the changes to take effect.

4.3 Installing Graphics Driver Utility

Sky Lake_Graphics

The GRAPHICS interface embedded in SP-S154 / SP-S174 / SP-S214 can support a wide range of display types. You can have dual displays via LVDS interfaces and make the system work simultaneously.

To install the GRAPHICS driver utility, follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-S154 / SP-S174 / SP-S214 and insert the driver disk.
- 2** Enter the **Sky Lake_Graphics** folder where the driver is located.
- 3** Click the **Setup.exe** file for driver installation.
- 4** Follow the on-screen instructions to complete the installation.
- 5** Once the installation is completed, shut down the system and restart SP-S154 / SP-S174 / SP-S214 for the changes to take effect.

Kaby Lake_Graphics (Win10 only)

To install the GRAPHICS driver utility, follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-S154 / SP-S174 / SP-S214 and insert the driver disk.
- 2** Enter the **Kaby Lake_Graphics** folder where the driver is located. (depending on your OS platform).
- 3** Click the **Setup.exe** file for driver installation.
- 4** Follow the on-screen instructions to complete the installation.
- 5** Once the installation is completed, shut down the system and restart SP/S154 / SP-S174 / SP-S214 for the changes to take effect.

4.4 Installing LAN Driver Utility

Enhanced with LAN function, SP-S154 / SP-S174 / SP-S214 supports various network adapters. To install the LAN Driver, follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-S154 / SP-S174 / SP-S214 and insert the driver disk.
- 2** Enter the **LAN Chip** folder where the driver is located.
- 3** Click the **PROWin32.exe** file for driver installation.
- 4** Follow the on-screen instructions to complete the installation.
- 5** Once the installation is completed, shut down the system and restart SP-S154 / SP-S174 / SP-S214 for the changes to take effect.

For more details on the installation procedure, refer to the README.txt file that you can find on LAN Driver Utility.

4.5 Installing Intel® Management Engine Interface Driver Utility

For Windows® 7 only. Pre-install Microsoft's Kernel-Mode Driver Framework (KMDF) version 1.11 before you install the Intel® Management Engine Components Installer (ME) in order to avoid errors in Device Manager.

Installation Instructions for Kernel-Mode Driver Framework (KMDF)

To install the Kernel-Mode Driver Framework (KMDF), follow the steps below:

- 1** Insert the driver disk into a DVD-ROM device.
- 2** (For Windows 7 only) Enter the **KMDF For Win7** folder where the installation driver file is located.
- 3** (For Windows 7 only) Click the Setup **kmdf-1.11-Win-6.1-x86 & kmdf-1.11-Win-6.1-x64.exe** file for driver installation.
- 4** Follow the on-screen instructions to complete the installation.

Installation Instructions for Intel® Management Engine Interface Installer

- 1** Connect the USB DVD-ROM device to SP-S154 / SP-S174 / SP-S214 and insert the driver disk.
- 2** Enter the **ME** folder where the driver is located.
- 3** Select Windows 7 (32/64-bit) for your OS platform.
- 4** Click **SetupME.exe** file for ME driver installation.
- 5** Follow the on-screen instructions to complete the installation.
- 6** Once the installation is completed, shut down the system and restart SP-S154 / SP-S174 / SP-S214 for the changes to take effect.

4.6 Installing Sound Driver Utility

The sound function enhanced in this system is fully compatible with Windows® 7 / Windows® 8.1 / Win10 series.

To install the Sound Driver, follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-S154 / SP-S174 / SP-S214 and insert the driver disk.
- 2** Open the **Sound Codec** folder where the driver is located.
- 3** Click the **0005-Win7_Win8_Win81_Win10_R279.exe** file for driver installation.
- 4** Follow the on-screen instructions to complete the installation.
- 5** Once the installation is completed, shut down the system and restart SP-S154 / SP-S174 / SP-S214 for the changes to take effect.

4.7 Installing Intel® USB 3.0 eXtensible Host Controller Utility

Intel® USB 3.0 eXtensible Host Controller Driver supports the following Intel® Chipsets/Processors:

- Intel® 8 Series/C220 series Chipset Family
- Intel® 4th Generation Core™ Processors
- Intel® C610 series Chipset Family
- Intel® 9 Series Chipset Family
- Intel® Pentium® Processor or Intel® Celeron® Processor N- & J-Series
- Intel® 5th generation Intel® Core™ Processors
- Intel® Core™ M Processor
- Intel® 6th generation Intel® Core™ processors
- Intel® 100 Series Chipset Family

To install the utility, follow the steps below:

- 1** Insert the driver disk into a DVD-ROM device.
- 2** Open the **USB3** folder where the driver is located.
- 3** Click the **Setup.exe** file for driver installation.
- 4** Follow the on-screen instructions to complete the installation.
- 5** Once the installation is completed, shut down the system and restart SP-S154 / SP-S174 / SP-S214 for the changes to take effect.

4.8 Installing eGalaxTouch Driver Utility

To install the eGalaxTouch Driver, follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-S154 / SP-S174 / SP-S214 and insert the driver disk.
- 2** Open the **Touch** folder where the driver is located.
- 3** Click the **setup.exe** file for driver installation.
- 4** Follow the on-screen instructions to complete the installation.
- 5** Once the installation is completed, shut down the system and restart SP-S154 / SP-S174 / SP-S214 for the changes to take effect.

4.9 Installing Intel® Rapid Storage Technology Installation Utility

To install the Intel Rapid Storage Technology Driver, follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-S154 / SP-S174 / SP-S214 and insert the driver disk.
- 2** Open the **RST** folder where the driver is located.
- 3** Select Windows 7 (32/64-bit) / Windows® 8.1 / Win10 for your OS platform.
- 4** Click the **SetupRST.exe** file for driver installation.
- 5** Follow the on-screen instructions to complete the installation.
- 6** Once the installation is completed, shut down the system and restart SP-S154 / SP-S174 / SP-S214 for the changes to take effect.

5

BIOS SETUP

This chapter guides users how to configure the basic system configurations via the BIOS Setup Utilities. The information of the system configuration is saved in BIOS NVRAM so that the Setup information is retained when the system is powered off. The BIOS Setup Utilities consist of the following menu items:

- Main Menu
- Advanced Menu
- Chipset Menu
- Security Menu
- Boot Menu
- Save & Exit Menu

5.1 Introduction

The board uses an AMI (American Megatrends Incorporated) Aptio BIOS that is stored in the Serial Peripheral Interface Flash Memory (SPI Flash) and can be updated. The SPI Flash contains the built-in BIOS setup program, Power-On Self-Test (POST), PCI auto-configuration utility, LAN EEPROM information, and Plug and Play support.

Aptio is AMI's BIOS firmware based on the UEFI (Unified Extensible Firmware Interface) specifications and the Intel Platform Innovation Framework for EFI. The UEFI specification defines an interface between the operating system and platform firmware. The interface consists of data tables that contain platform-related information, boot service calls, and runtime service calls that are available to the operating system and its loader. These elements have combined to provide a standard environment for booting the operating system and running pre-boot applications.

The diagram below shows the Extensible Firmware Interface's location in the software stack.

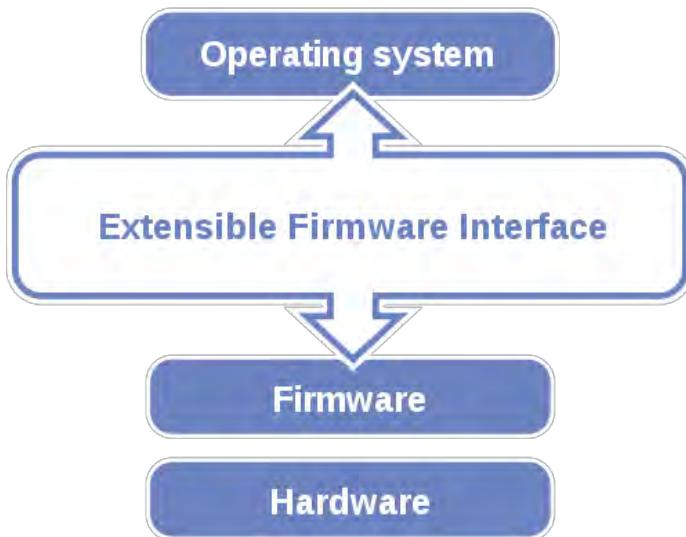


Figure 5-1. Extensible Firmware Interface Diagram

EFI BIOS provides an user interface that allows you to modify hardware configuration, e.g. change the system date and time, enable/disable a system component, determine bootable device priority, set up personal password, etc., which is convenient for engineers to perform modifications and customize the computer system and allows technicians to troubleshoot the occurred errors when the hardware is faulty.

The BIOS setup menu allows users to view and modify the BIOS settings for the computer. After the system is powered on, users can access the BIOS setup menu by pressing or <Esc> immediately while the POST message is running before the operating system is loading.

All the menu settings are described in details in this chapter.

5.2 Accessing Setup Utility

After the system is powered on, BIOS will enter the Power-On Self-Test (POST) routines and the POST message will be displayed:

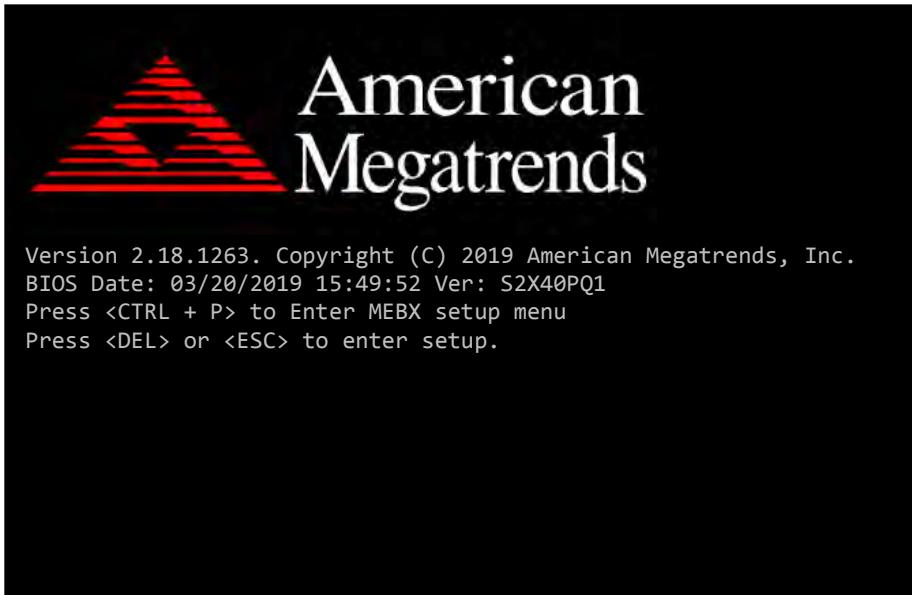
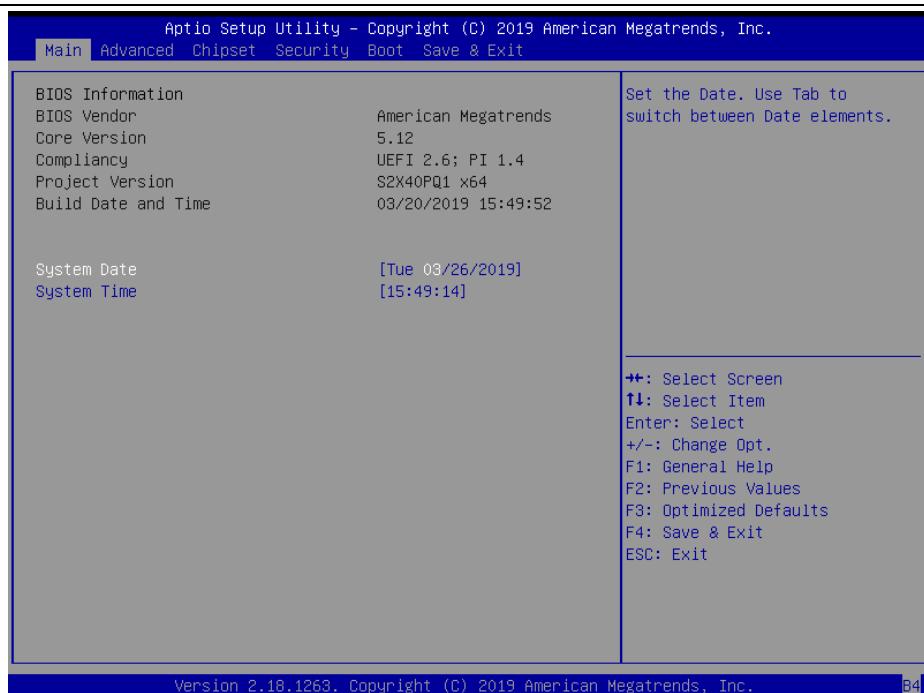


Figure 5-2. POST Screen with AMI Logo

Press **** or **<Esc>** to access the Setup Utility program and the **Main** menu of the Aptio Setup Utility will appear on the screen as below:



BIOS Setup Menu Initialization Screen

You may move the cursor by $\langle \uparrow \rangle$ and $\langle \downarrow \rangle$ keys to highlight the individual menu items. As you highlight each item, a brief description of the highlighted selection will appear on the right side of the screen.

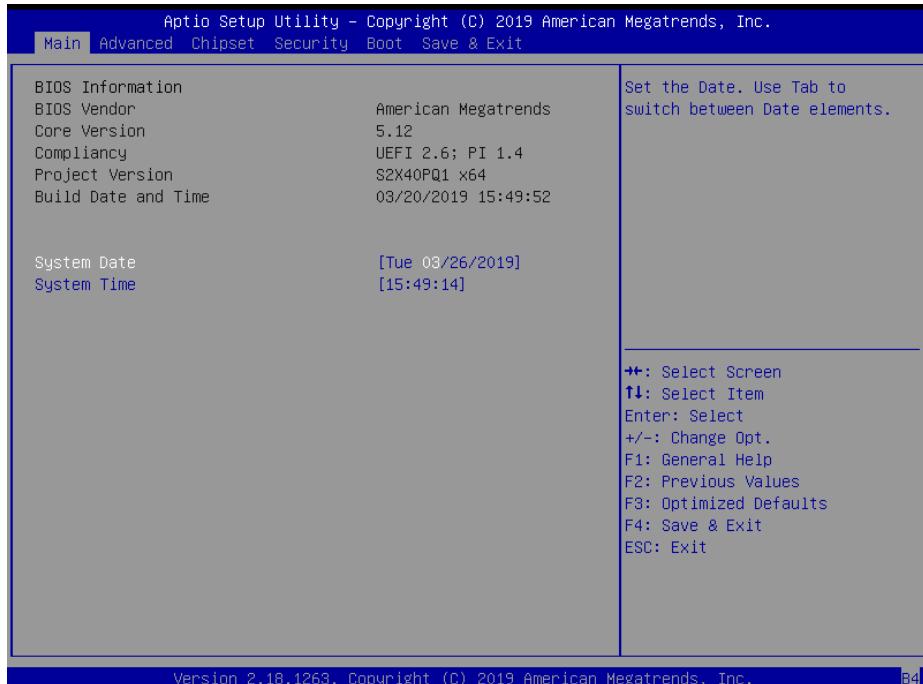
The language of the BIOS setup menu interface and help messages are shown in US English. You may use $\langle \uparrow \rangle$ or $\langle \downarrow \rangle$ key to select among the items and press $\langle \text{Enter} \rangle$ to confirm and enter the sub-menu. The following table provides the list of the navigation keys that you can use while operating the BIOS setup menu.

BIOS Setup Navigation Key	Description
<↔> and <→>	Select a different menu screen (move the cursor from the selected menu to the left or right).
<↑> and <↓>	Select a different item (move the cursor from the selected item upwards or downwards)
<Enter>	Execute the command or select the sub-menu.
<F2>	Load the previous configuration values.
<F3>	Load the default configuration values.
<F4>	Save the current values and exit the BIOS setup menu.
<Esc>	Close the sub-menu. Trigger the confirmation to exit BIOS setup menu.

5.3 Main

Menu Path *Main*

The **Main** menu allows you to view the BIOS Information, change the system date and time, and view the user access privilege level. Use tab to switch between date elements. Use $<\uparrow>$ or $<\downarrow>$ arrow keys to highlight the item and enter the value you want in each item. This screen also displays the BIOS version (project) and BIOS Build Date and Time.



Main Screen

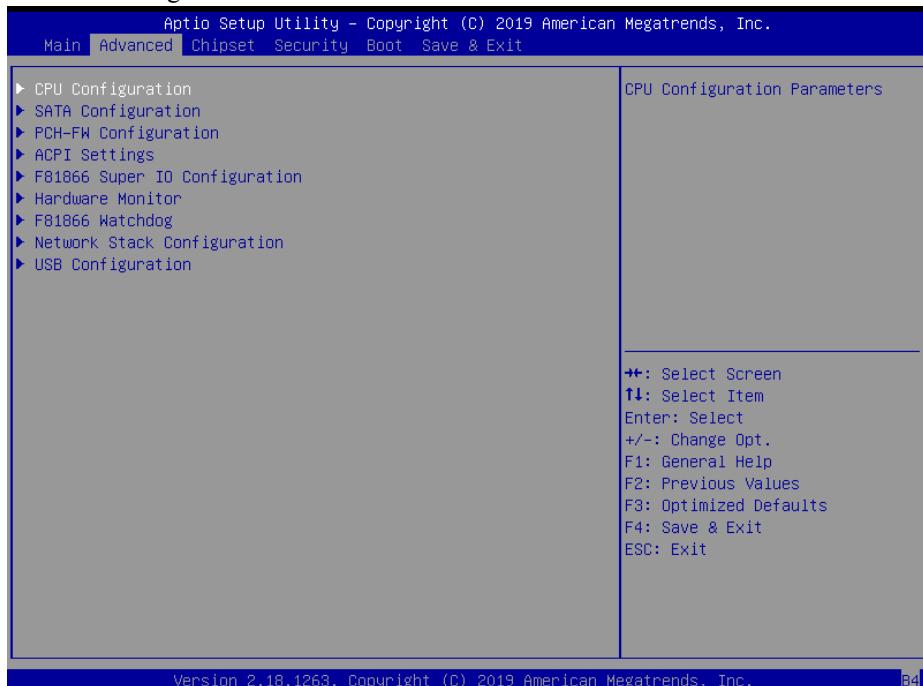
BIOS Setting	Options	Description/Purpose
BIOS Vendor	No changeable options	Displays the name of the BIOS vendor.
Core Version	No changeable options	Displays the current BIOS core version.
Compliance	No changeable options	Displays the current UEFI version.
Project Version	No changeable options	Displays the version of the BIOS currently installed on the platform.
Build Date and Time	No changeable options	Displays the date that the current BIOS version is built.
System Date	month, day, year	Sets the system date. The format is [Day Month/ Date/ Year]. Users can directly enter values or use $<+>$ or $<->$ arrow keys

BIOS Setting	Options	Description/Purpose
		to increase/decrease it. The “Day” is automatically changed.
System Time	hour, minute, second	Sets the system time. The format is [Hour: Minute: Second]. Users can directly enter values or use <+> or <-> arrow keys to increase/decrease it.

5.4 Advanced

Menu Path *Advanced*

This menu provides advanced configurations such as CPU Configuration, SATA Configuration, PCH-FW Configuration, ACPI Settings, F81866 Super I/O Configuration, Hardware Monitor, F81866 Watchdog, Network Stack Configuration and USB Configuration.



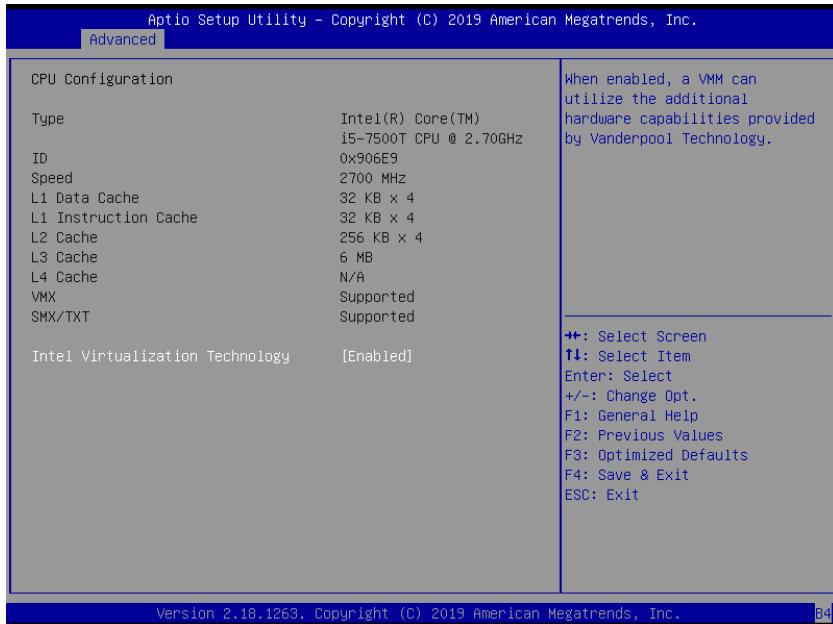
Advanced Menu Screen

BIOS Setting	Options	Description/Purpose
CPU Configuration	Sub-Menu	CPU Configuration Parameters.
SATA Configuration	Sub-Menu	SATA Device Options Settings.
PCH-FW Configuration	Sub-Menu	Management Engine Technology Parameters.
ACPI Settings	Sub-Menu	System ACPI Parameters.
F81866 Super IO Configuration	Sub-Menu	System Super IO Chip Parameters
Hardware Monitor	Sub-Menu	Monitor hardware status.
F81866 Watchdog	Sub-Menu	F81866 Watchdog Parameters.
Network Stack Configuration	Sub-Menu	Network Stack Settings
USB Configuration	Sub-Menu	USB Configuration Parameters.

5.4.1 Advanced – CPU Configuration

Menu Path *Advanced > CPU Configuration*

The **CPU Configuration** provides advanced CPU settings and some information about CPU.



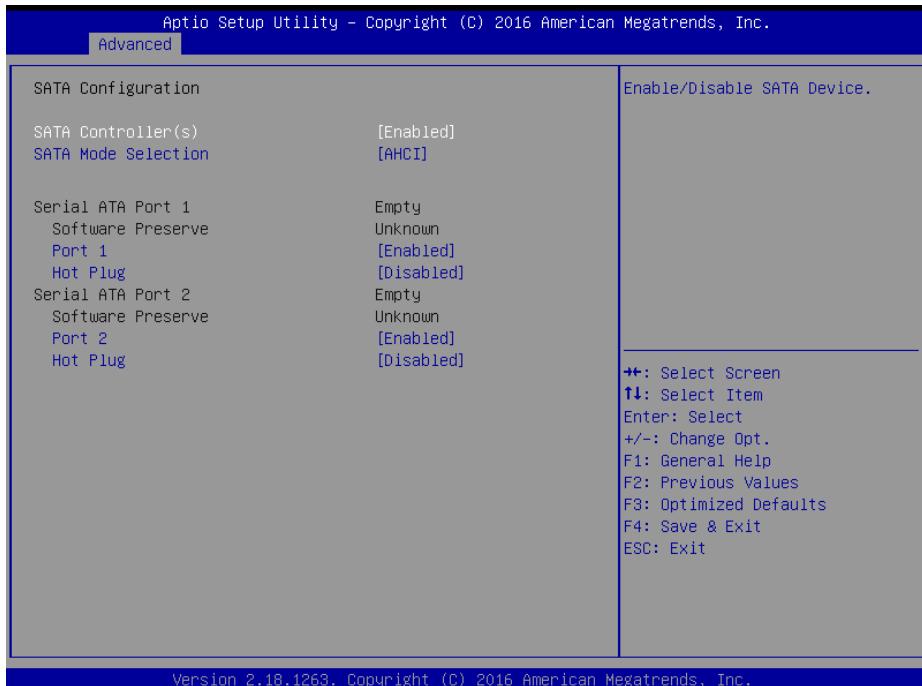
CPU Configuration Screen

BIOS Setting	Options	Description/Purpose
Type	No changeable options	Display the CPU Type.
ID	No changeable options	Display the CPU ID.
Speed	No changeable options	Display the CPU Speed.
L1 Data Cache	No changeable options	L1 Data Cache Size
L1 Instruction Cache	No changeable options	L1 Instruction Cache Size
L2 Cache	No changeable options	L2 Cache Size
L3 Cache	No changeable options	L3 Cache Size
L4 Cache	No changeable options	L4 Cache Size
VMX	No changeable options	CPU VMX hardware support for virtual machines.
SMX/TXT	No changeable options	Secure Mode extensions support.
Intel Virtualization Technology	- Disabled - Enabled	When enabled, VMM can utilize the additional hardware capabilities provided by Vanderpool Technology

5.4.2 Advanced – SATA Monitor

Menu Path *Advanced > SATA Monitor*

The **SATA Configuration** allows users to enable / disable the SATA controller as well as the operational mode after the SATA controller is enabled. The following screen indicates the functions available when the SATA hard drive is set to work in AHCI mode.



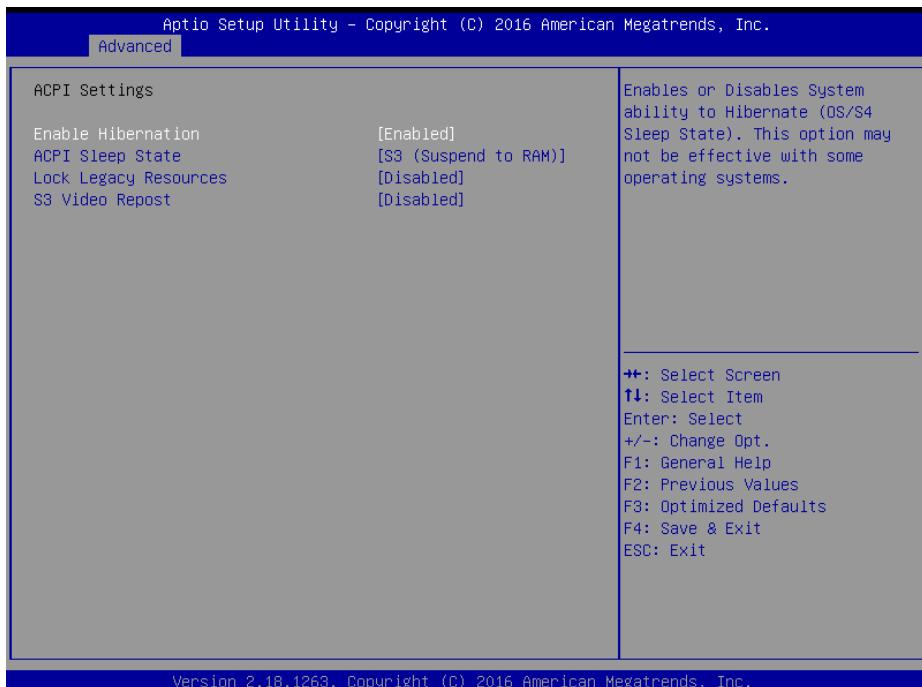
CPU Configuration Screen

BIOS Setting	Options	Description/Purpose
SATA Controller(s)	- Disabled - Enabled (Default)	Enables or Disables the on-chip SATA Device..
SATA Mode Selection	- AHCI - Intel RST Premium	Determines how SATA controller(s) operate.
Serial ATA Port 1 – 2	No changeable options	Displays the SATA device's name.
Software Preserve	No changeable options	Indicates whether the connected SATA device supports Software Setting Preservation (SSP).
Port 1 – 2	- Disabled - Enabled	Enables or Disables SATA Port Device.
Hot Plug	- Disabled - Enabled	Enables or Disables SATA Port Device HotPlug function to designate a SATA port device as hot-pluggable.

5.4.3 Advanced – ACPI Settings

Menu Path *Advanced > ACPI Settings*

The **ACPI Settings** allows users to configure relevant ACPI (Advanced Configuration and Power Management Interface) settings such as Enable/Disable Hibernation, ACPI sleep state, Lock Legacy Resources and S3 Video Repost.

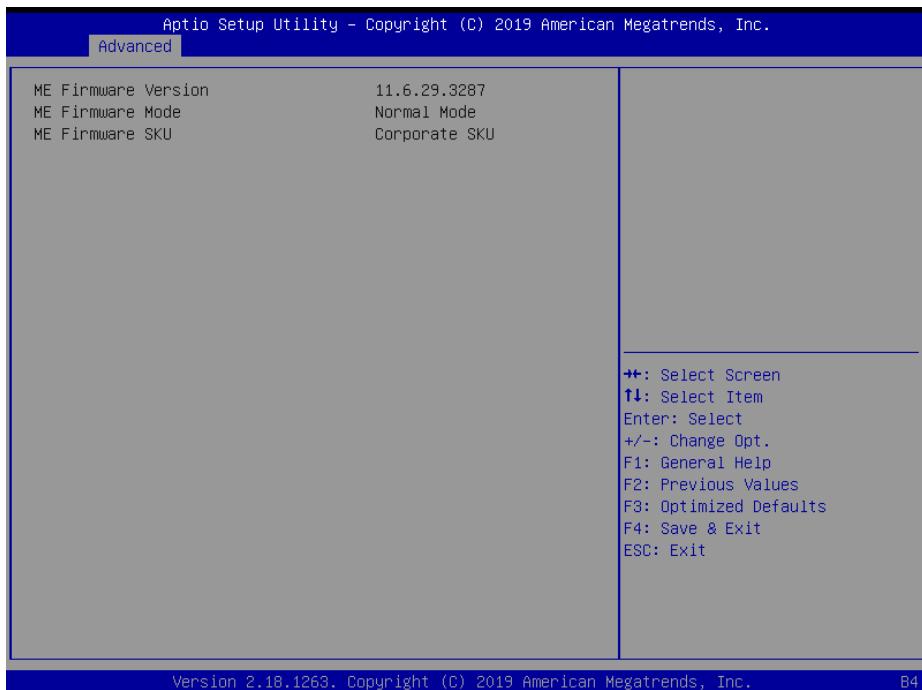


BIOS Setting	Options	Description/Purpose
Enable Hibernation	- Disabled - Enabled (Default)	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some OS.
ACPI Sleep State	- Suspend Disabled - S3 (Suspend to RAM)	Selects the ACPI sleep state the system will enter when the SUSPEND button is pressed.
Lock Legacy Resources	- Disabled - Enabled	Enables or Disables Lock of Legacy Resources.
S3 Video Repost	- Disabled - Enabled	Enable or Disable S3 Video Repost.

5.4.4 Advanced – PCH-FW Settings

Menu Path *Advanced > PCH-FW Settings*

The **PCH-FW** allows users to view the information about ME (Management Engine) firmware information, such as ME firmware version, firmware mode and firmware SKU.



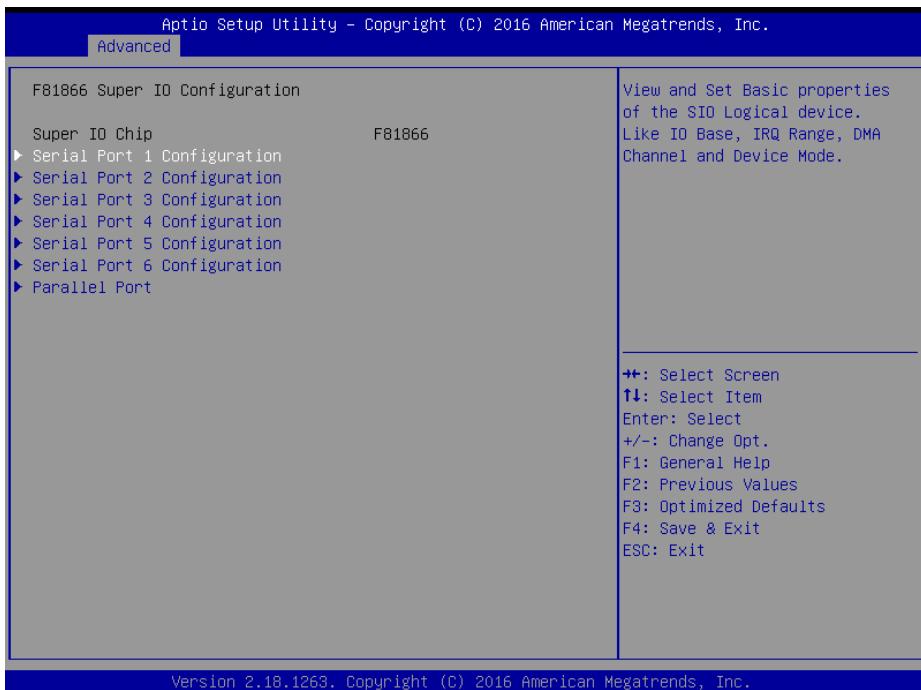
PCH-EW Configuration Screen

BIOS Setting	Options	Description/Purpose
ME Firmware Version	No changeable options	Displays the ME Firmware Version.
ME Firmware Mode	No changeable options	Displays the ME Firmware Mode.
ME Firmware SKU	No changeable options	Displays the ME Firmware SKU.

5.4.5 Advanced – F81866 Super IO Configuration

Menu Path *Advanced > F81866 Super IO Configuration*

The **F81866 Super IO Configuration** allows users to configure the serial ports 1-6.



F81866 Watchdog Configuration Screen

BIOS Setting	Options	Description/Purpose
Super IO Chip (F81866)	No changeable options	Displays the super I/O chip model.
Serial Port 1 Configuration	Sub-Menu	Set Parameters of Serial Port 1 (COMA)
Serial Port 2 Configuration	Sub-Menu	Set Parameters of Serial Port 2 (COMB)
Serial Port 3 Configuration	Sub-Menu	Set Parameters of Serial Port 3 (COMC)
Serial Port 4 Configuration	Sub-Menu	Set Parameters of Serial Port 4 (COMD)
Serial Port 5 Configuration	Sub-Menu	Set Parameters of Serial Port 5 (COME)
Serial Port 6 Configuration	Sub-Menu	Set Parameters of Serial Port 6 (COMF)
Parallel Port	Sub-menu	Sets the parameters of Parallel Port (LPT).

5.4.5.1 F81866 Super IO Configuration - Serial Port 1 Configuration

Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 1 Configuration*

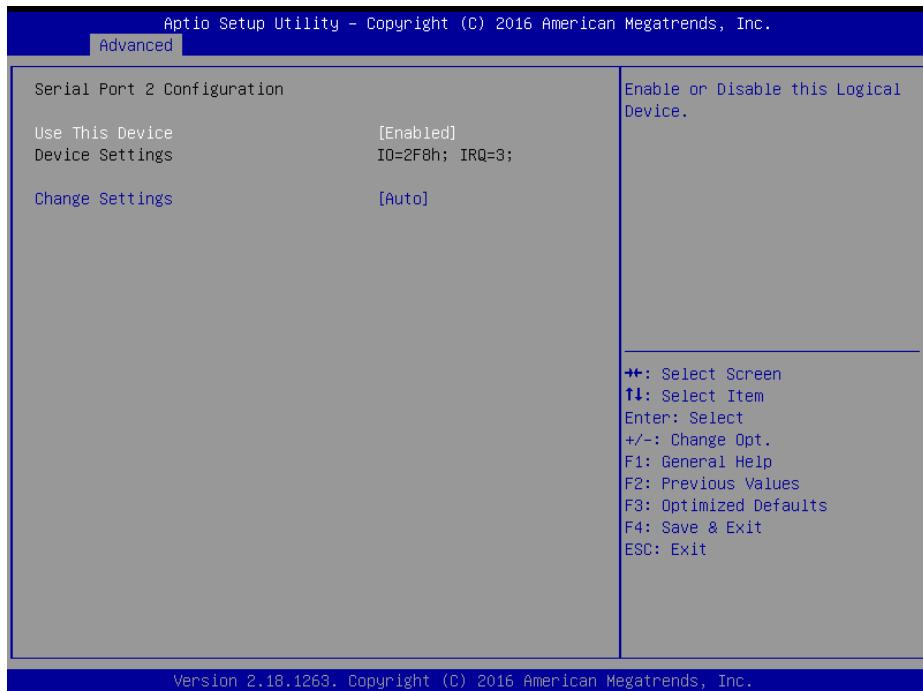


Serial Port 1 Configuration Screen

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled (default)	Enables or Disables Serial Port 1.
Device Settings	No changeable options	Displays the current settings of Serial Port 1.
Change Settings	- Auto - IO=3F8h; IRQ=4; - IO=3F8h; IRQ=3,4,5,6,7,10,11; - IO=2F8h; IRQ=3,4,5,6,7,10,11; - IO=3E8h; IRQ=3,4,5,6,7,10,11; - IO=2E8h; IRQ=3,4,5,6,7,10,11;	Allows users to change Device's Resource settings. New settings will be reflected on this Setup Page after System restarts.

5.4.5.2 F81866 Super IO Configuration - Serial Port 2 Configuration

Menu Path Advanced > F81866 Super IO Configuration > Serial Port 2 Configuration

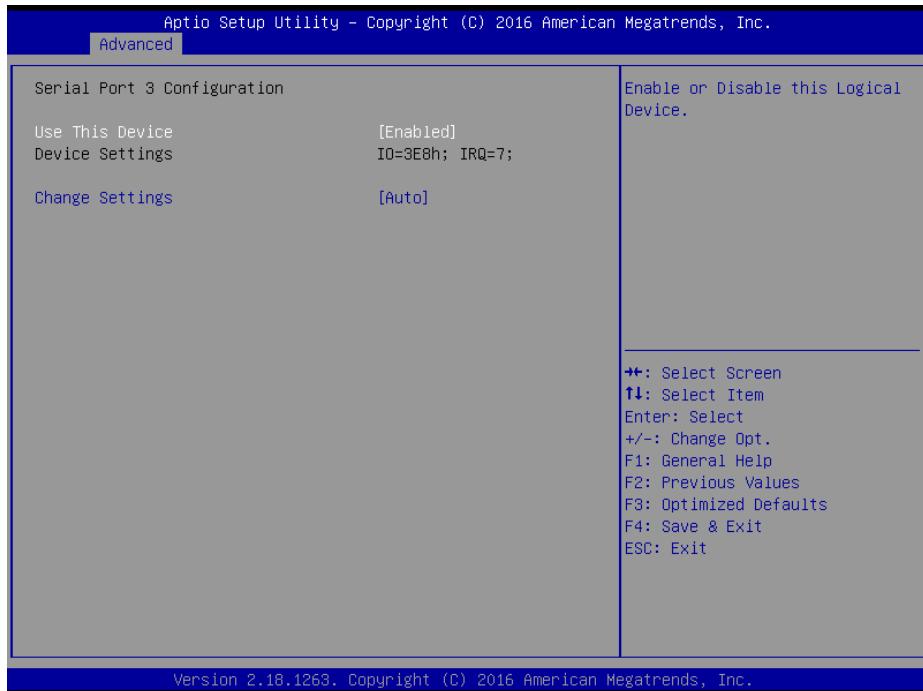


Serial Port 2 Configuration Screen

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled (default)	Enables or Disables Serial Port 2.
Device Settings	No changeable options	Displays the current settings of Serial Port 2.
Change Settings	- Auto - IO=2F8h; IRQ=3; - IO=3F8h; IRQ=3,4,5,6,7,10,11; - IO=2F8h; IRQ=3,4,5,6,7,10,11; - IO=3E8h; IRQ=3,4,5,6,7,10,11; - IO=2E8h; IRQ=3,4,5,6,7,10,11;	Allows users to change Device's Resource settings. New settings will be reflected on this Setup Page after System restarts.

5.4.5.3 F81866 Super IO Configuration - Serial Port 3 Configuration

Menu Path Advanced > F81866 Super IO Configuration > Serial Port 3 Configuration



Serial Port 3 Configuration Screen

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled (default)	Enables or Disables Serial Port 3.
Device Settings	No changeable options	Displays the current settings of Serial Port 3.
Change Settings	- Auto - IO=3E8h; IRQ=7; - IO=3E8h; IRQ=3,4,5,6,7,10,11; - IO=2E8h; IRQ=3,4,5,6,7,10,11; - IO=2F0h; IRQ=3,4,5,6,7,10,11; - IO=2E0h; IRQ=3,4,5,6,7,10,11;	Allows users to change Device's Resource settings. New settings will be reflected on this Setup Page after System restarts.

5.4.5.4 F81866 Super IO Configuration - Serial Port 4 Configuration

Menu Path Advanced > F81866 Super IO Configuration > Serial Port 4 Configuration

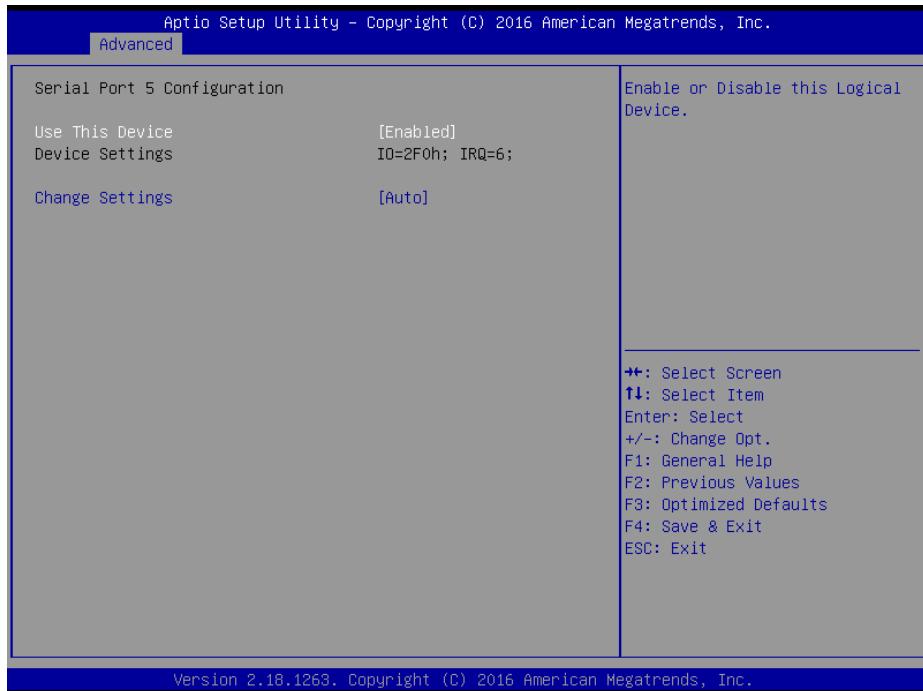


Serial Port 4 Configuration Screen

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled (default)	Enables or Disables Serial Port 4.
Device Settings	No changeable options	Displays the current settings of Serial Port 4.
Change Settings	- Auto - IO=2E8h; IRQ=10; - IO=3E8h; IRQ=3,4,5,6,7,10,11; - IO=2E8h; IRQ=3,4,5,6,7,10,11; - IO=2F0h; IRQ=3,4,5,6,7,10,11; - IO=2E0h; IRQ=3,4,5,6,7,10,11;	Allows users to change Device's Resource settings. New settings will be reflected on this Setup Page after System restarts.

5.4.5.5 F81866 Super IO Configuration - Serial Port 5 Configuration

Menu Path Advanced > F81866 Super IO Configuration > Serial Port 5 Configuration

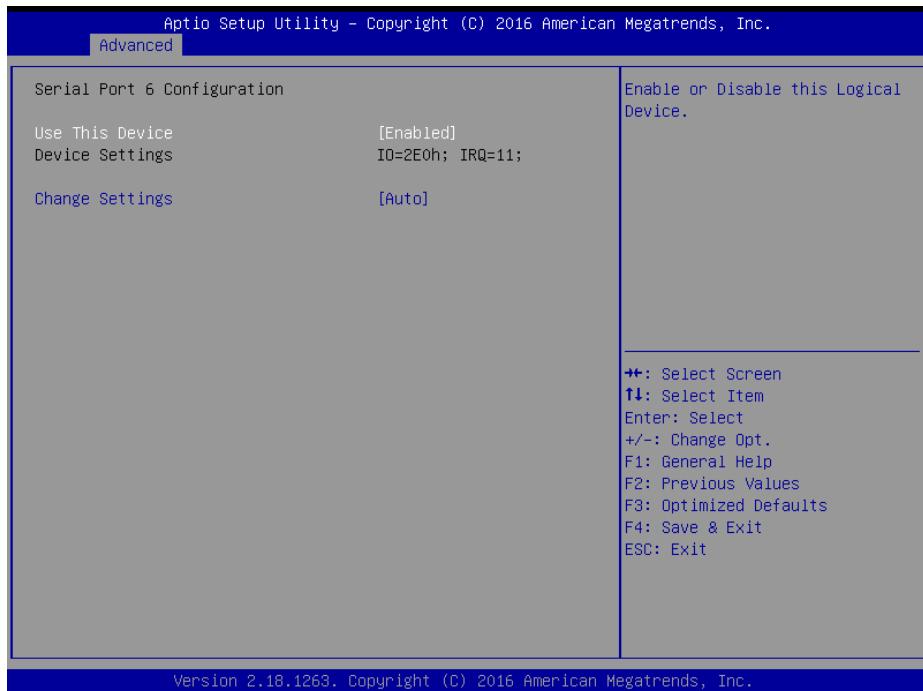


Serial Port 5 Configuration Screen

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled (default)	Enables or Disables Serial Port 5.
Device Settings	No changeable options	Displays the current settings of Serial Port 5.
Change Settings	- Auto - IO=2F0h; IRQ=6; - IO=3E8h; IRQ=3,4,5,6,7,10,11; - IO=2E8h; IRQ=3,4,5,6,7,10,11; - IO=2F0h; IRQ=3,4,5,6,7,10,11; - IO=2E0h; IRQ=3,4,5,6,7,10,11;	Allows users to change Device's Resource settings. New settings will be reflected on this Setup Page after System restarts.

5.4.5.6 F81866 Super IO Configuration - Serial Port 6 Configuration

Menu Path Advanced > F81866 Super IO Configuration > Serial Port 6 Configuration

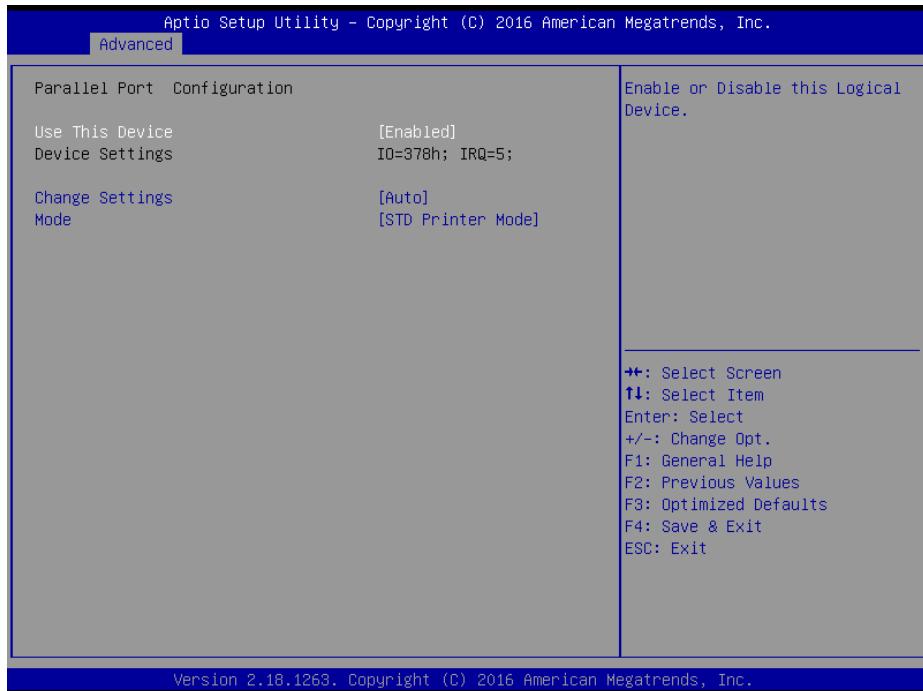


Serial Port 6 Configuration Screen

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled (default)	Enables or Disables Serial Port 6.
Device Settings	No changeable options	Displays the current settings of Serial Port 6.
Change Settings	- Auto - IO=2E0h; IRQ=11; - IO=3E8h; IRQ=3,4,5,6,7,10,11; - IO=2E8h; IRQ=3,4,5,6,7,10,11; - IO=2F0h; IRQ=3,4,5,6,7,10,11; - IO=2E0h; IRQ=3,4,5,6,7,10,11;	Allows users to change Device's Resource settings. New settings will be reflected on this Setup Page after System restarts.

5.4.5.7 F81866 Super IO Configuration - Parallel Port Configuration

Menu Path Advanced > F81866 Super IO Configuration > Parallel Port Configuration



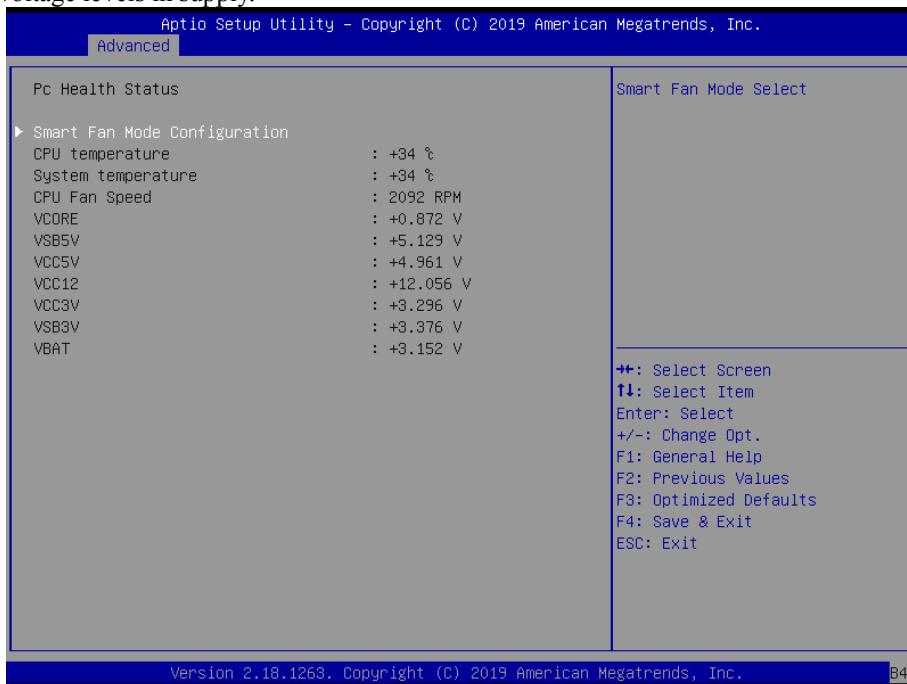
Parallel Port Configuration Screen

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables parallel port.
Device Settings	No changeable options	Displays the current settings of Parallel Port.
Change Settings	- Auto - IO=378h; IRQ=5; - IO=378h; IRQ=5,6,7,9,10,11,12; - IO=278h; IRQ=5,6,7,9,10,11,12; - IO=3BCh; IRQ=5,6,7,9,10,11,12;	Allows you to select specific IO address and IRQ for Parallel Port.
Mode	- STD Printer Mode - SPP Mode - EPP-1.9 and SPP Mode - EPP-1.7 and SPP Mode - ECP Mode - ECP and EPP 1.9 Mode - ECP and EPP 1.7 Mode	Allows you to change Parallel Port mode.

5.4.6 Advanced – Hardware Monitor

Menu Path *Advanced > Hardware Monitor*

The **Hardware Monitor** allows users to monitor the health and status of the system such as Smart Fan Mode Configuration, CPU temperature, system temperature and voltage levels in supply.



Hardware Monitor Screen

BIOS Setting	Options	Description/Purpose
Smart Fan Mode Configuration	Sub-Menu	Smart Fan Mode Settings
CPU Temperature	No changeable options	Displays the processor's temperature.
System Temperature	No changeable options	Displays the system's temperature.
CPU Fan Speed	No changeable options	Display CPU Fan speed
VCORE	No changeable options	Detects and displays the VCORE CPU voltage.
VSB5V	No changeable options	Displays voltage level of the VSB5V in supply.
VCC5V	No changeable options	Detects and displays 5V voltage.
VCC12	No changeable options	Detects and displays 12V voltage.
VCC3V	No changeable options	Detects and displays 3V voltage.
VSB3V	No changeable options	Detects and displays VSB3V voltage.
VBAT	No changeable options	Detects and displays the battery voltage.

5.4.7 Hardware Monitor – Smart Fan Mode Configuration

Menu Path *Advanced > Hardware Monitor > Smart Fan Mode Configuration*



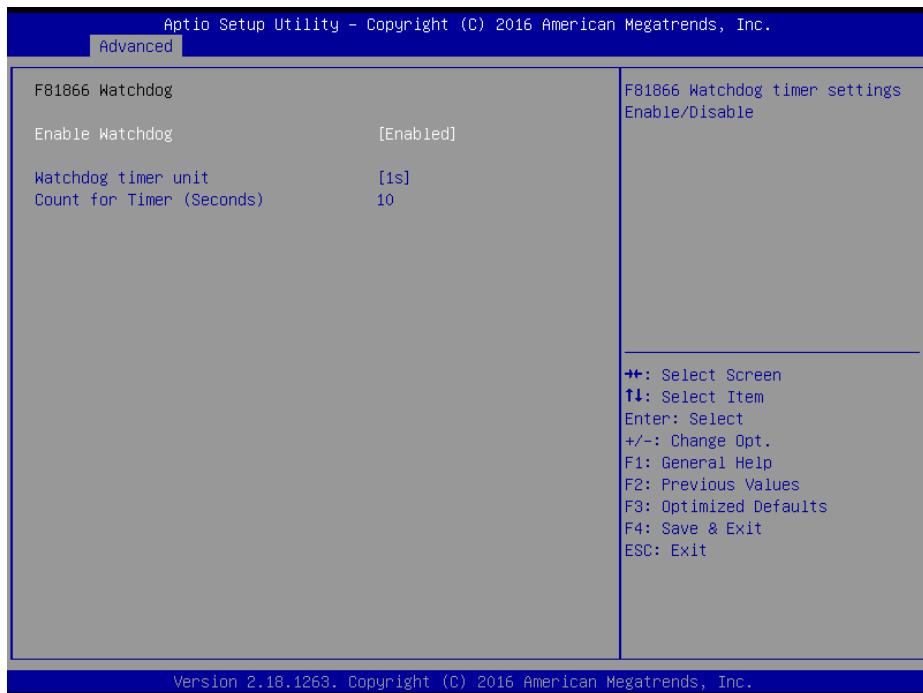
Smart Fan Mode Configuration Screen

BIOS Setting	Options	Description/Purpose
CPU Fan Smart Fan Control	- Manual Duty Mode - Auto Duty-Cycle Mode	Smart Fan Mode select for CPU Fan.
Manual Duty Mode	Numeric (from 1 to 100)	Manual mode fan control, user can write expected duty cycle (PWM fan type) 1-100.

5.4.8 Advanced – F81866 Watchdog Configuration

Menu Path *Advanced > F81866 Watchdog*

If the system hangs or fails to respond, enable the F81866 watchdog function to trigger a system reset via the 255-level watchdog timer.



F81866 Watchdog Configuration Screen

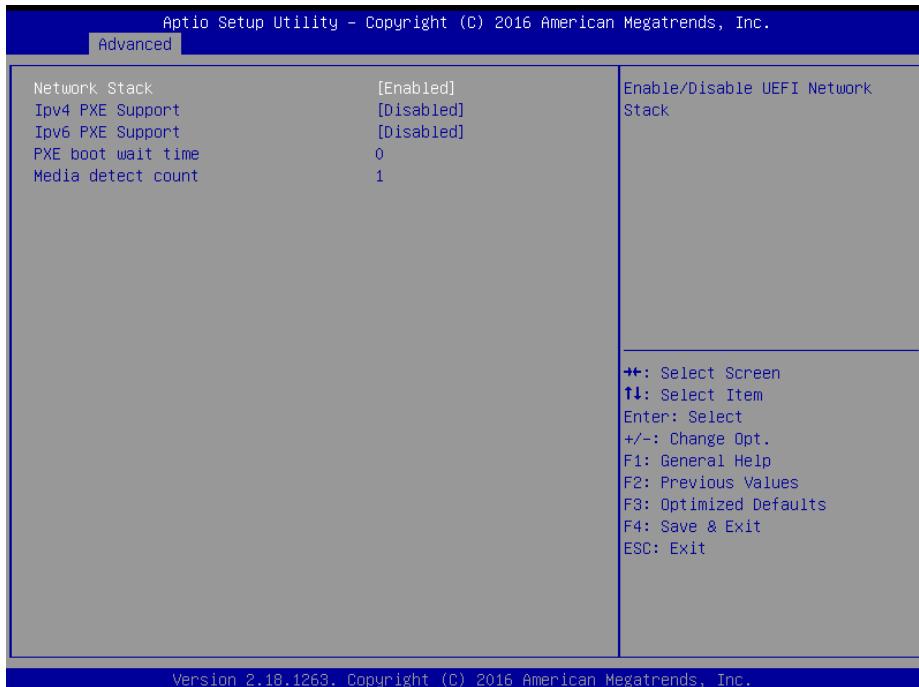
BIOS Setting	Options	Description/Purpose
Enable Watchdog	- Enabled - Disabled	Enables/Disables F81866 Watchdog timer settings.
Watchdog Timer Unit	- 1s - 60s	Selects 1s (second) or 60s (minute) as the time unit of Watchdog timer.
Count for Timer (Seconds)	Numeric (from 1 to 255)	Sets the timeout for Watchdog timer. (Max. value: 255 seconds or minutes) 1s unit: 10-59, 60s unit: 1-255 Watchdog Timer = Unit * Count.

5.4.9 Advanced – Network Stack Configuration

Menu Path *Advanced > Network Stack Configuration*

The **Network Stack Configuration** allows users to enable/disable Network Stack, IPv4 / IPv6 PXE (Pre-Boot Execution) support and configure PXE boot wait time and the Media detect count.

PXE allows a workstation to boot from a server on a network prior to booting the operating system on the local hard drive. A PXE-enabled workstation connects its NIC to the LAN via a jumper, which keeps the workstation connected to the network even when the power is turned off.



Network Stack Configuration Screen

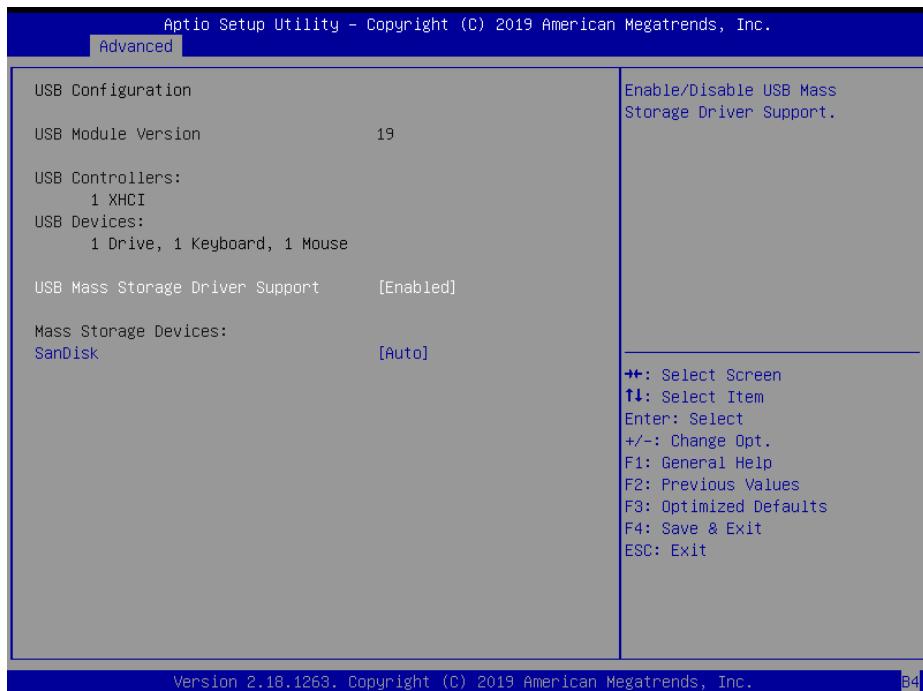
BIOS Setting	Options	Description/Purpose
Network Stack	- Disabled - Enabled	Enables or Disables UEFI Network Stack.
Ipv4 PXE Support	- Disabled - Enabled	Enables IPv4 PXE Boot Support. If disabled, IPv4 PXE boot option will not be created.
Ipv6 PXE Support	- Disabled - Enabled	Enables IPv6 PXE Boot Support. If disabled, IPv6 PXE boot option will not

BIOS Setting	Options	Description/Purpose
		be created.
PXE boot wait time	Numeric (from 0 to 5)	Wait time to press ESC key to abort the PXE boot.
Media detect count	Numeric (from 1 to 50)	Number of times that the media presence will be checked.

5.4.10 Advanced – USB Configuration

Menu Path *Advanced > USB Configuration*

The **USB Configuration** allows users to configure advanced USB settings such as USB mass storage driver support.



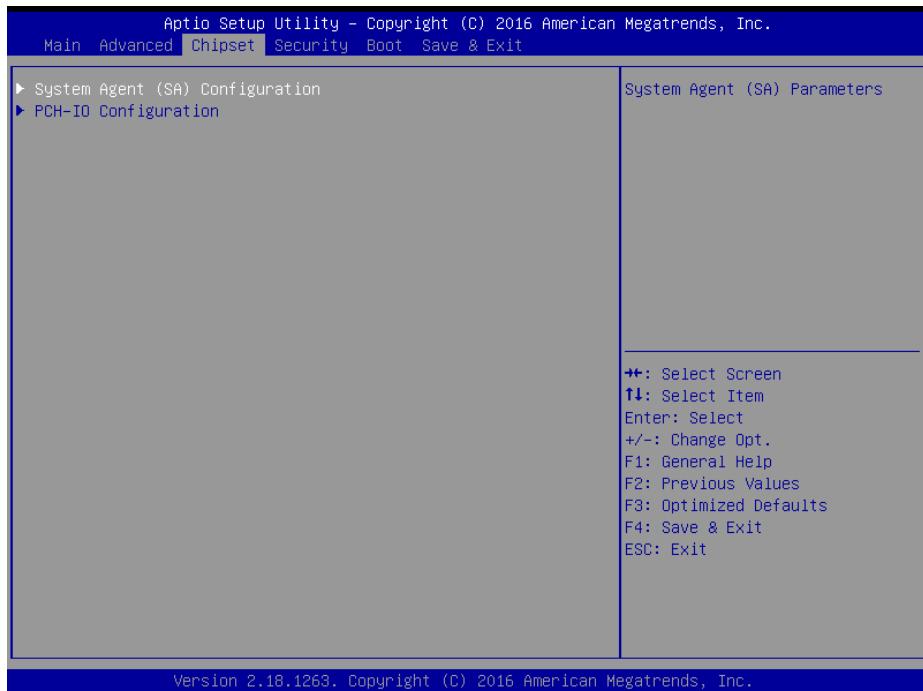
USB Configuration Screen

BIOS Setting	Options	Description/Purpose
USB Mass Storage Driver Support	- Disabled - Enabled	Enables or Disables USB Mass Storage Driver Support.

5.5 Chipset

Menu Path	<i>Chipset</i>
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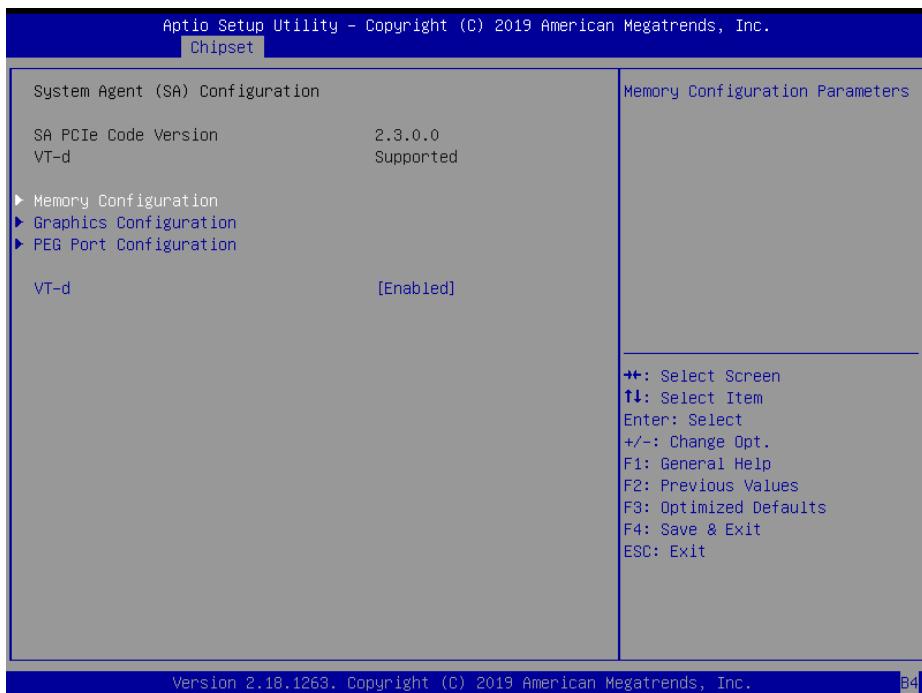
This menu allows users to configure advanced Chipset settings such as System Agent (SA) and PCH-IO configuration parameters.

**Chipset Screen**

BIOS Setting	Options	Description/Purpose
System Agent (SA) Parameters	Sub-Menu	System Agent (SA) Parameters.
PCH-IO Configuration	Sub-Menu	PCH Parameters.

5.5.1 Chipset – System Agent (SA) Configuration

Menu Path

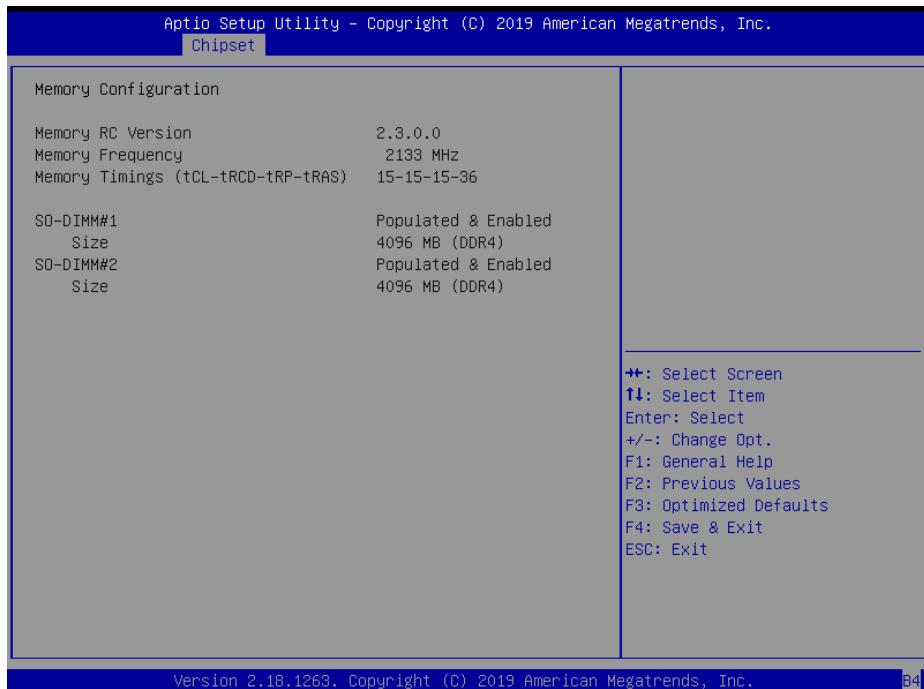
Chipset > System Agent (SA) Configuration

System Agent (SA) Configuration Screen

BIOS Setting	Options	Description/Purpose
SA PCIe Code Version	No changeable options	Display the SA PCIe Code Version.
VT-d	No changeable options	VT-d capability.
Memory Configuration	Sub-menu	Memory Configuration
Graphic Configuration	Sub-menu	Graphic Configuration
PEG Port Configuration	Sub-menu	PEG Port Configuration
VT-d	- Disabled - Enabled	Enable or Disable VT-d function.

5.5.2 System Agent (SA) Configuration –Memory Configuration

Menu Path *Chupset > System Agent (SA) Configuration > Memory Configuration*



Memory Configuration Screen

BIOS Setting	Options	Description/Purpose
Memory RC Version	No changeable options	Display the Memory RC Version.
Memory Frequency	No changeable options	Display the Frequency of Memory.
Memory Timings (tCL-tRCD-tRP-tRAS)	No changeable options	Display the Memory Timings.
SO-DIMM#1	No changeable options	Display the size of SO-DIMM#1.
SO-DIMM#2	No changeable options	Display the size of SO-DIMM#2.

5.5.3 System Agent (SA) Configuration – Graphics Configuration

Menu Path *Chupset > System Agent (SA) Configuration > Graphics Configuration*

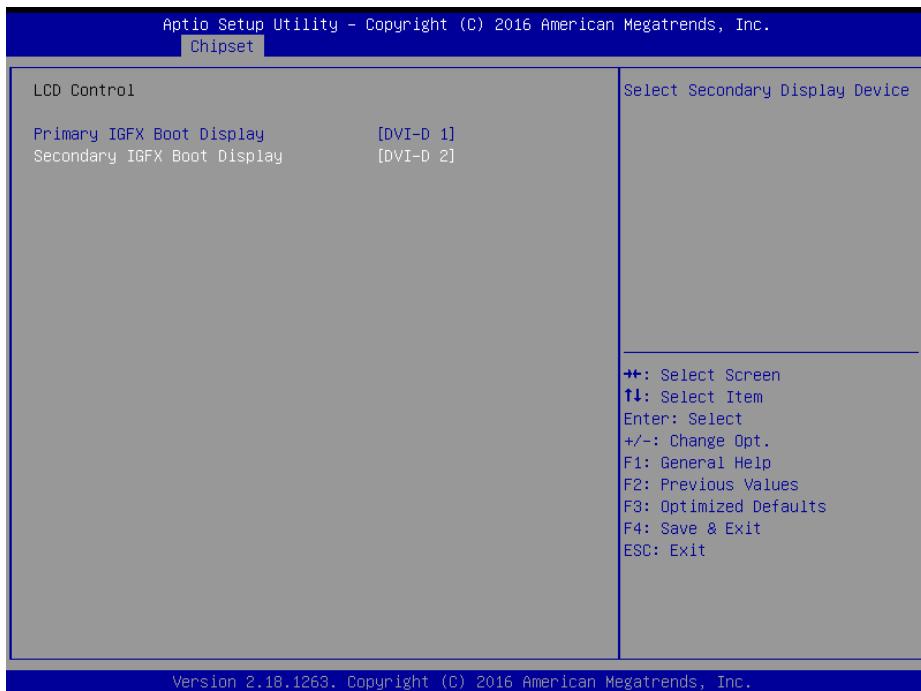


Graphics Configuration Screen

BIOS Setting	Options	Description/Purpose
Primary Display	- Auto - IGFX - PEG	Select which of IGFX/PEG Graphics device should be Primary Display.
LCD Control	Sub-menu	LCD Control menu.

Graphics Configuration – LCD Control

Menu Path *Chupset > System Agent (SA) Configuration > Graphics Configuration > LCD Control*

**LCD Control Screen**

BIOS Setting	Options	Description/Purpose
Primary IGFX Boot Display	<ul style="list-style-type: none"> - VBIOS default - DVI-D 1 - DVI-D 2 - DisplayPort 	Select Primary Display Device
Secondary IGFX Boot Display	<ul style="list-style-type: none"> - Disabled - DVI-D 1 - DVI-D 2 - DisplayPort 	Select Secondary Display Device

5.5.4 System Agent (SA) Configuration – PEG Port Configuration

Menu Path *Chupset > System Agent (SA) Configuration > PEG Port Configuration*



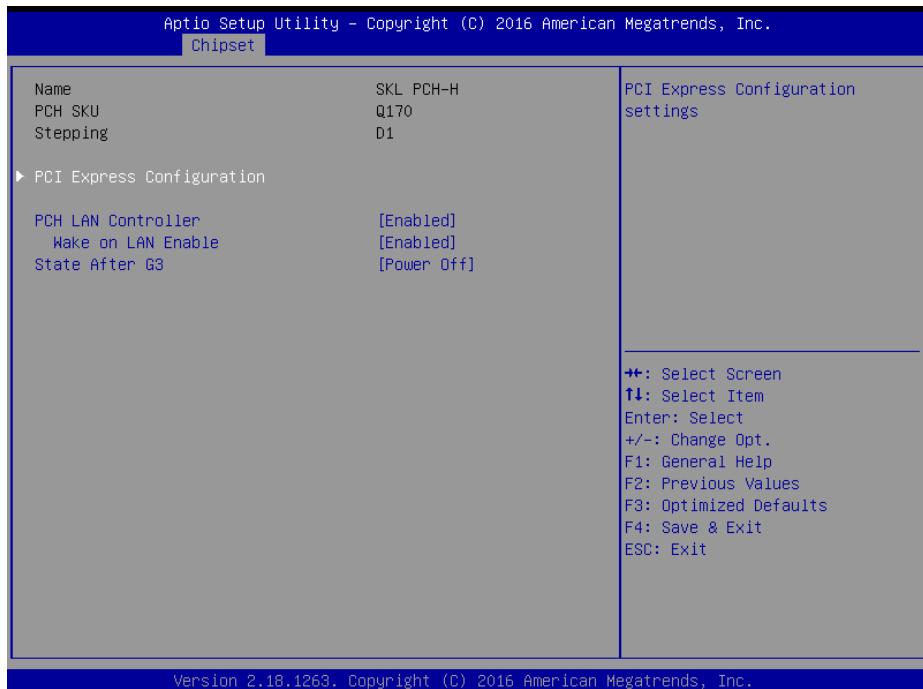
PEG Port Configuration Screen

BIOS Setting	Options	Description/Purpose
PCI-E1	No changeable options	PCI-E1 Link and Speed information.
Enable Root Port	- Disabled - Enabled - Auto	Enable or Disable the Root Port.
Max Link Speed	- Auto - Gen1 - Gen2 - Gen3	Configure PCI-E1 Max Speed.
Detect Non-Compliance Device	- Disabled - Enabled	Detect Non-Compliance PCI Express Device in PEG.

5.5.5 Chipset – PCH-IO Configuration

Menu Path *Chipset > PCH-IO Configuration*

The **PCH-IO Configuration** allows users to configure computer's I/O functions.



PCH-IO Configuration Screen

BIOS Setting	Options	Description/Purpose
Name	No changeable options	Display the Intel PCH Name.
PCH SKU	No changeable options	Display the Intel PCH SKU.
Stepping	No changeable options	Display the Intel PCH Stepping.
PCI Express Configuration	Sub-menu	PCI Express Configuration settings.
PCH LAN Controller	- Disabled - Enabled	Enable or Disable onboard NIC.
Wake On LAN	- Disabled - Enabled	Enable or Disable integrated LAN to wake the system.
State After G3	- Power On - Power Off	Specify what state to go to when power is re-applied after a power failure (G3 state).

5.5.6 PCH-IO Configuration – PCI Express Configuration

Menu Path *Chupset > PCH-IO Configuration > PCI Express Configuration*

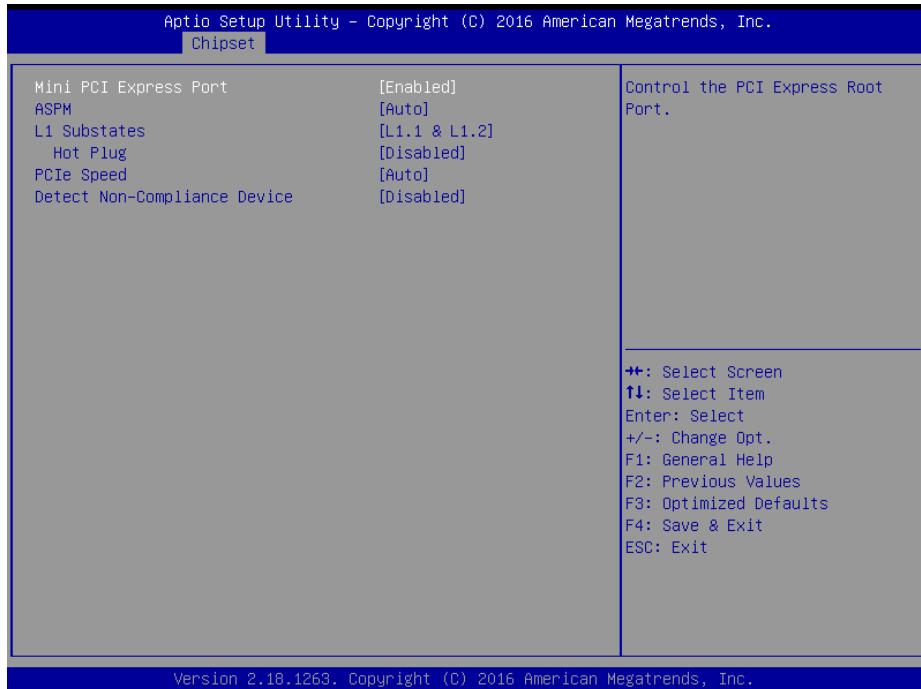


PCI Express Configuration Screen

BIOS Setting	Options	Description/Purpose
PCIE Port assigned to LAN	No changeable options	Display the LAN assigned PCIE Port.
Mini PCI Express Port	Sub-menu	Mini PCI Express Port Settings.

PCH-IO Configuration – Mini PCI Express Port Configuration

Menu Path *Chuset > PCH-IO Configuration > Mini PCI Express Port Configuration*

**Mini PCI Express Configuration Screen**

BIOS Setting	Options	Description/Purpose
Mini PCI Express Port	- Disabled - Enabled	Control the PCI Express Root Port.
ASPM	- Disabled - L0s - L1 - L0sL1 - Auto	Set the ASPM Level.
L1 Substates	- Disabled - L1.1 - L1.2 - L1.1 & L1.2	PCI Express L1 Substates settings.
Hot Plug	- Disabled - Enabled	Enable or Disable PCI Express Hot Plug.
PCIe Speed	- Auto - Gen1 - Gen2	Select PCI Express Port Speed.

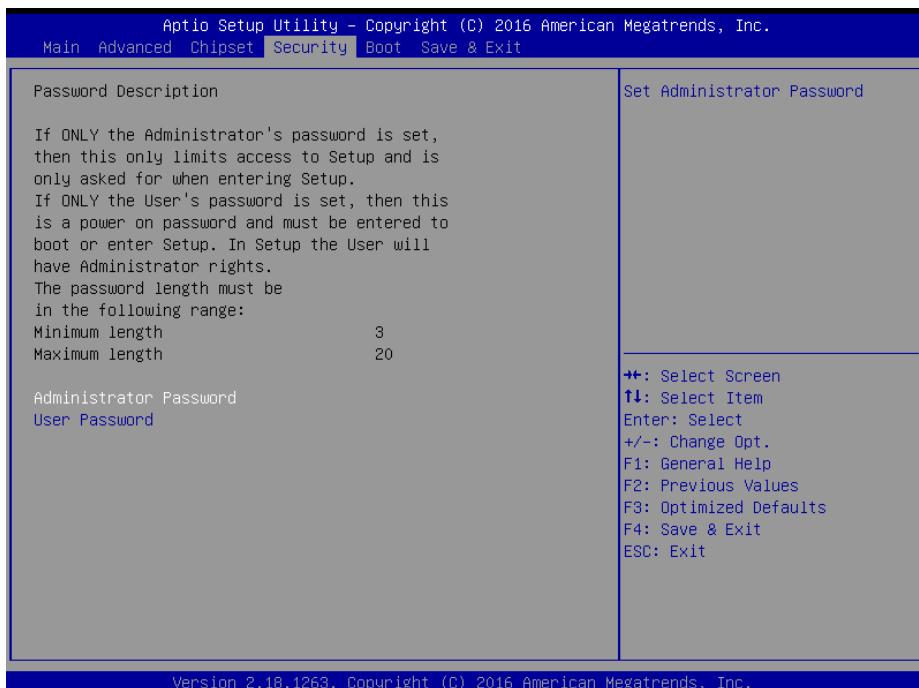
BIOS Setting	Options	Description/Purpose
	- Gen3	
Detect Non-Compliance Device	- Disabled - Enabled	Detect Non-Compliance PCI Express Device. If enable, it will take more time at POST time.

5.6 Security

Menu Path *Security*

From the **Security** menu, you are allowed to create, change or clear the administrator password. You will be asked to enter the configured administrator password before you can access the Setup Utility.

By setting an administrator password, you will prevent other users from changing your BIOS settings. You can configure an Administrator password and then configure a user password. An administrator has much more privileges over the settings in the Setup utility than a user. Heed that a user password does not provide access to most of the features in the Setup utility.



Security Screen

BIOS Setting	Options	Description/Purpose
Administrator Password	Password can be 3-20 alphanumeric characters.	Specifies the administrator password.
User Password	Password can be 3-20 alphanumeric characters.	Specifies the user password.

Create an Administrator or User Password

1. Select the **Administrator Password / User Password** option from the Security menu and press <Enter>, and the password dialog entry box appears.
2. Enter the password you want to create. A password can be 3-20 alphanumeric characters. After you have configured the password, press <Enter> to confirm.
3. Type the new password again and press <Enter>.

Change an Administrator or User Password

1. Select the **Administrator Password / User Password** option from the Security menu and press <Enter>, and the password dialog entry box appears.
2. Select the Administrator Password or User Password that you want to change. A password can be 3-20 alphanumeric characters. After you have changed the password, press <Enter> to confirm.
3. Type the changed password again and press <Enter>.

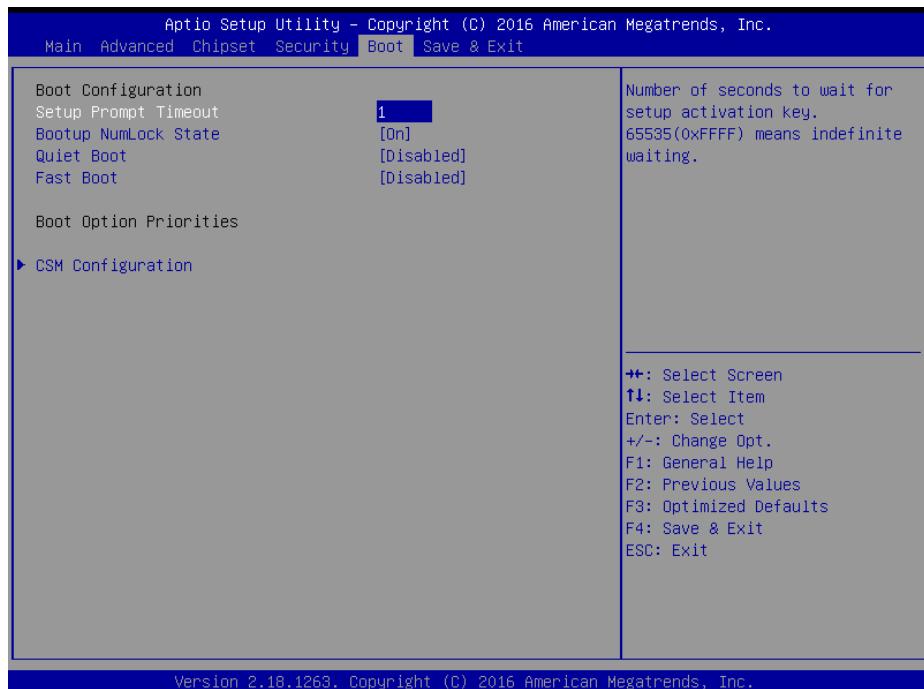
Remove an Administrator or User Password

1. Select the **Administrator Password / User Password** option from the Security menu and press <Enter>, and the password dialog entry box appears.
2. Select the configured Administrator Password or User Password that you want to delete. Leave the dialog box blank and press <Enter>.
3. Press <Enter> again when the password confirmation box appears.

5.7 Boot

Menu Path Boot

This menu provides control items for setting system boot configuration and boot priorities.



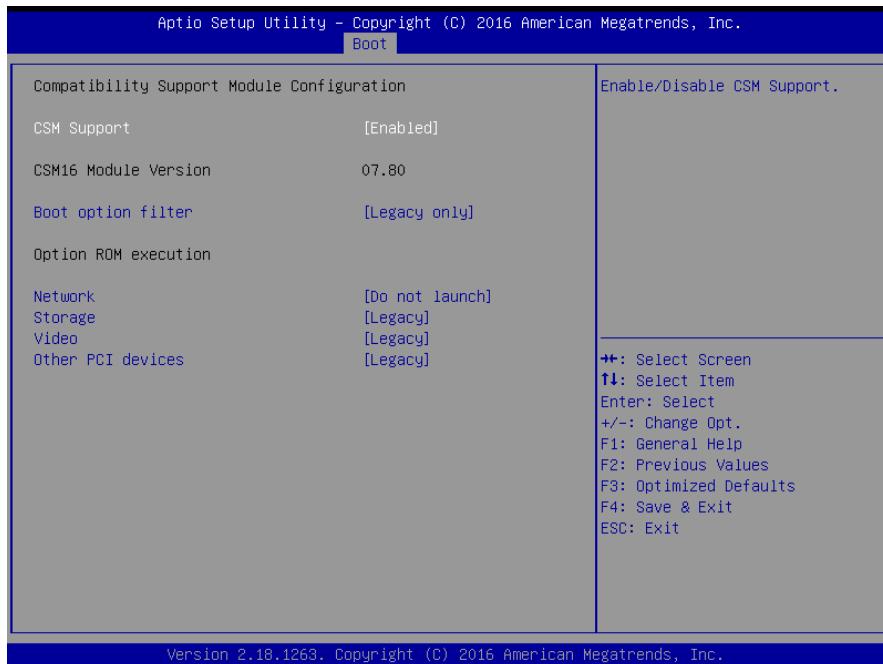
Boot Screen

BIOS Setting	Options	Description/Purpose
Setup Prompt Timeout	Numeric (from 1 to 65535)	Number of seconds to wait for setup activation key.
Bootup NumLock State	- On - Off	Selects the NumLock state after the system is powered on. <ul style="list-style-type: none"> • On: Enables the NumLock function automatically after the system is powered on. • Off: Disables the NumLock function after the system is powered on.

BIOS Setting	Options	Description/Purpose
Quiet Boot	- Disabled - Enabled	Enables or Disables Quiet Boot options. When this option is set to “Disabled”, BIOS will display normal POST messages.
Fast Boot	- Disabled - Enabled	Enables or Disables Fast Boot option. It allows users to reduce the system startup time and start up the system in a fast manner.
Boot Option #1~#n	- [Drive(s)] - Disabled	Allows users to change the boot order from the available device(s). Note that in the menu displayed, you will only see the device with the highest priority for a specific boot device type.
CSM Configuration	Sub-Menu	CSM configuration: Enable/Disable, Option ROM execution settings, etc.

5.7.1 Boot – CSM Configuration

Menu Path *Chipset > CSM Configuration*



CSM Configuration Screen

BIOS Setting	Options	Description/Purpose
CSM Support	- Disabled - Enabled	Enable or Disable CSM Support.
CSM16 Module	No changeable options	Display the CSM 16 Module version.
Boot option filter	- UEFI and Legacy - Legacy only - UEFI only	This option controls Legacy/UEFI ROMs priority.
Network	- Do not launch - UEFI - Legacy	Controls the execution of UEFI and Legacy PXE OpROM.
Storage	- Do not launch - UEFI - Legacy	Controls the execution of UEFI and Legacy Storage OpROM.
Video	- Do not launch - UEFI - Legacy	Controls the execution of UEFI and Legacy Video OpROM.
Other PCI devices	- Do not launch - UEFI - Legacy	Determines OpROM execution policy for devices other than Network, Storage or Video.

5.8 Save & Exit

Menu Path	<i>Save & Exit</i>
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The **Save & Exit** allows users to save or discard changed BIOS settings as well as load factory default settings.

Save Changed BIOS Settings

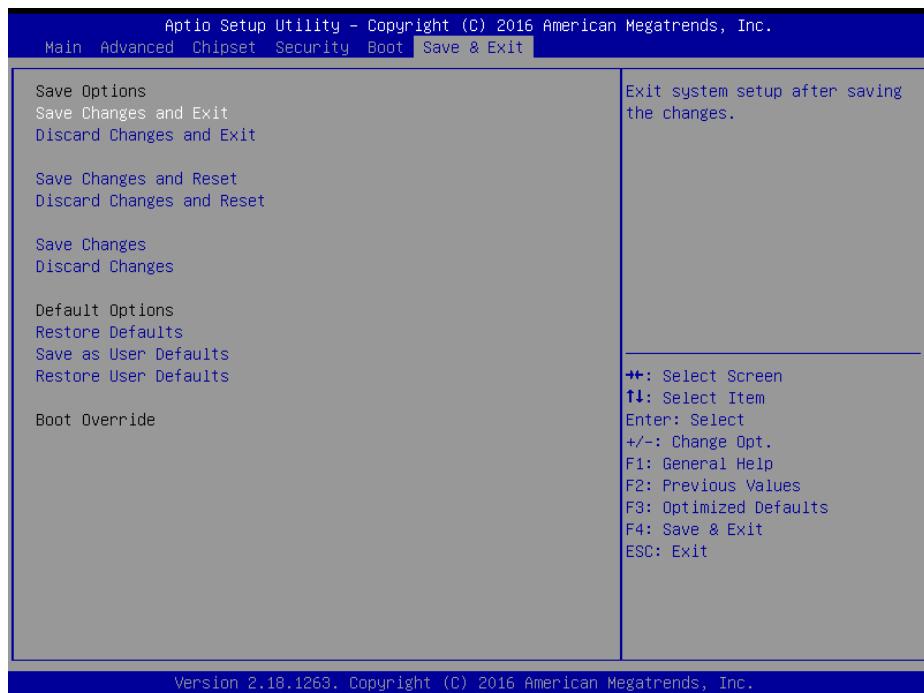
To save and validate the changed BIOS settings, select **Save Changes** from the **Save & Exit** menu, or you can select **Save Changes and Exit** (or press **F4**) to validate the changes and then exit the system. Select **Save Changes and Reset** to validate the changed BIOS settings and then restart the system

Discard Changed BIOS Settings

To cancel the BIOS settings you have previously configured, select **Discard Changes and Exit** from this menu, or simply press **Esc** to exit the BIOS setup. You can also select **Discard Changes and Reset** to discard any changes you have made and restore the factory BIOS defaults.

Load User Defaults

You may simply press **F3** at any time to load the **Optimized Values** which resets all BIOS settings to the factory defaults.



BIOS Setting	Options	Description/Purpose
Save Changes and Exit	No changeable options	Exits the system and saves the changes in NVRAM.
Discard Changes and Exit	No changeable options	Exits the system without saving any changes configured in BIOS settings.
Save Changes and Reset	No changeable options	Saves the changes in NVRAM and resets the system.
Discard Changes and Reset	No changeable options	Resets the system without saving any changes configured in BIOS settings.
Save Changes	No changeable options	Saves the changes done so far to any of the setup options.
Discard Changes	No changeable options	Discards the changes done so far to any of the BIOS settings.
Restore Defaults	No changeable options	Loads the optimized defaults for BIOS settings.
Save as User Defaults	No changeable options	Saves the changes done so far as User Defaults.
Restore User Defaults	No changeable options	Restores the User Defaults to all the BIOS settings.
Boot Override	- [Drive(s)]	Forces to boot the system from selected [drive(s)].

Appendix A System Diagrams

This appendix includes the exploded diagrams of the system and the parts list as well as the part numbers of the SP-S154 / SP-S174 / SP-S214 system.

SP-S154 (Resistance / Capacitance)

- System Exploded Diagram
- Touch Panel & LCD Display Exploded Diagram
- Box PC Exploded Diagram
- HDD Module Exploded Diagrams
- HDD Tray Exploded Diagram
- Packing Exploded Diagram

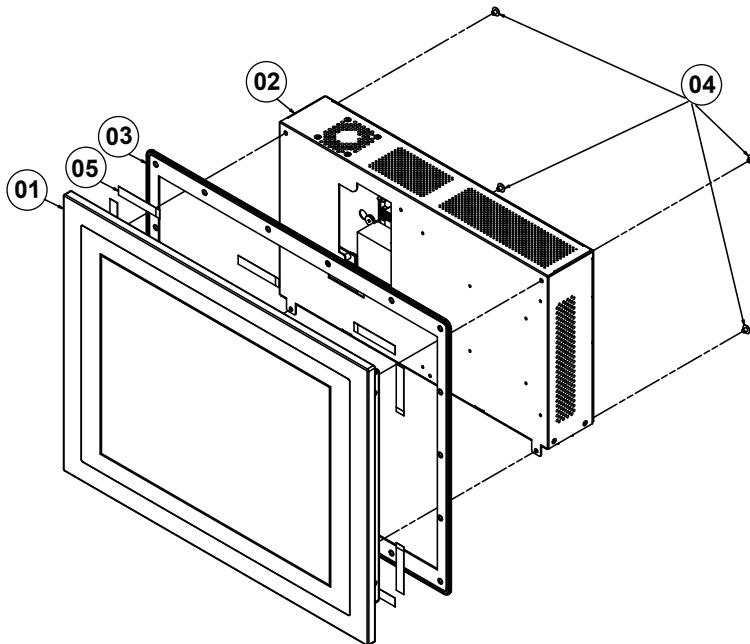
SP-S174 (Resistance / Capacitance)

- System Exploded Diagram
- Touch Panel & LCD Display Exploded Diagram
- Box PC Exploded Diagram
- HDD Module Exploded Diagrams
- HDD Tray Exploded Diagram
- Packing Exploded Diagram

SP-S214

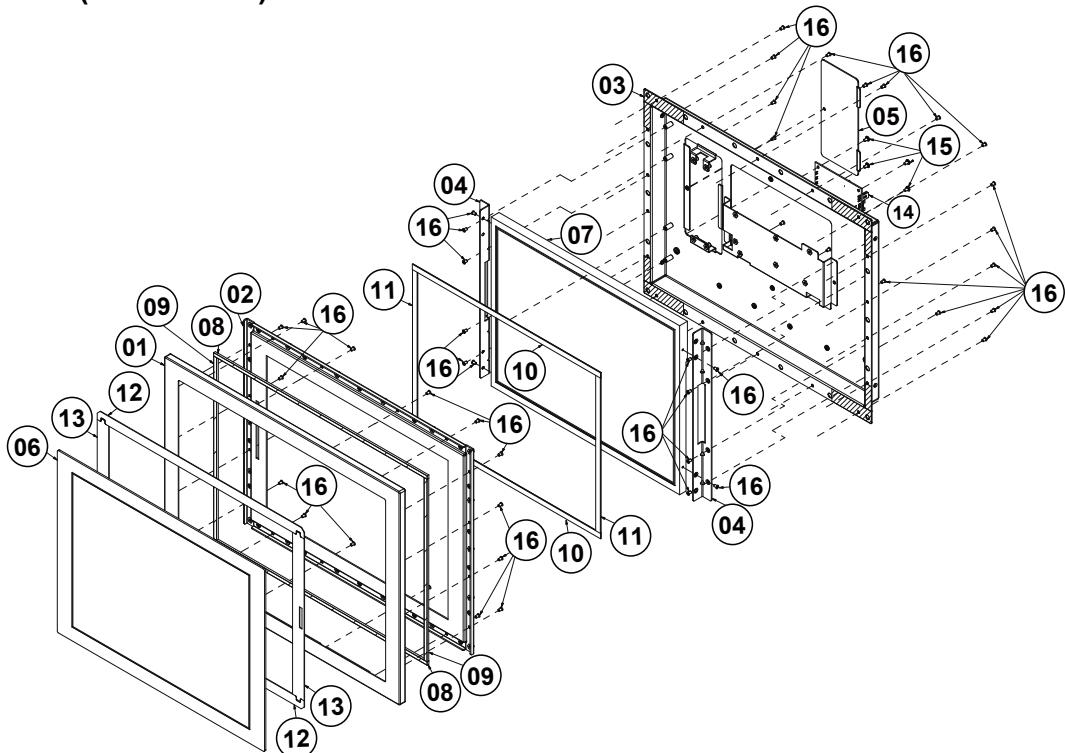
- System Exploded Diagram
- Touch Panel & LCD Display Exploded Diagram
- LCD Panel Exploded Diagram (1)
- LCD Panel Exploded Diagram (2)
- Box PC Exploded Diagram
- HDD Module Exploded Diagrams
- HDD Tray Exploded Diagram
- Packing Exploded Diagram

SP-S154 System Exploded Diagram (Resistance)



No.	Component Name	P/N No.	Q'ty
1	7145_Panel_SUS_EXP	See Page A-3 & A-4	1
2	Box_PC_BM-2502_ASM	See Page A-5 & A-6	1
3	SP-7145 Wall Oring	30-013-01300411	1
4	Truss Head Screw #2 / M4x0.7Px4mm	22-232-40004011	4
5	DSMS Adhesive Tape	94-026-06501000	5

SP-S154 Touch Panel & LCD Display Exploded Diagram (Resistance)

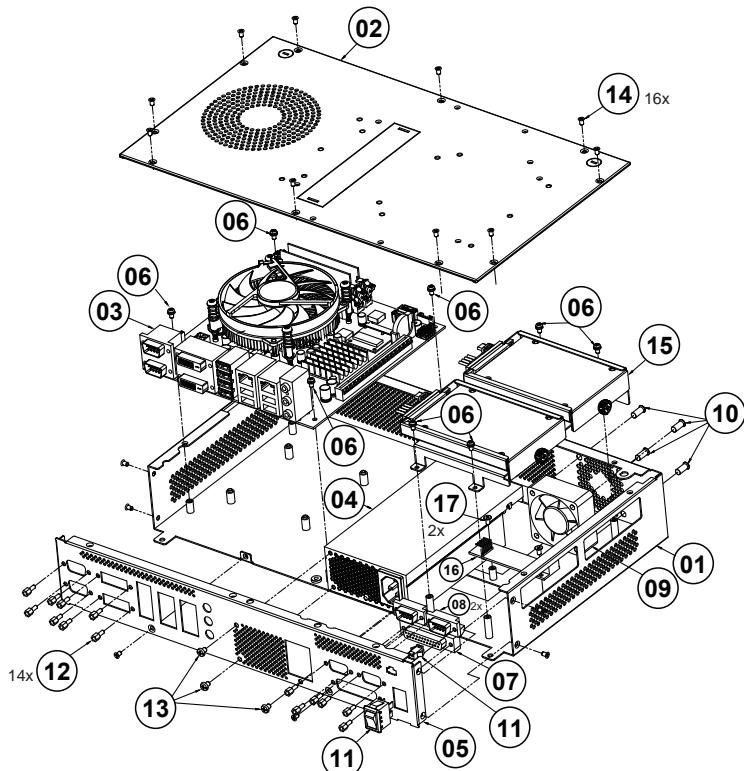


No.	Component Name	P/N No.	Q'ty
1	SP-7145 SUS Panel Cover	20-004-07001411	1
2	SP-7145 Thin Gap SGCC Cover Kit	20-004-21001411	1
3	SP-7145 LCD Cover Kit (w/Paint)(Silver Gray)	20-004-03061411	1
4	SP-7145 LCD Link Holder	20-029-03001411	2
5	SP-7145 Touch Board Cover Kit (w/Paint) (Silver Gray)	20-004-03062411	1
6	15" 5-wire Resistive Touch Panel	52-380-00212114	1
7	15" TFT LCD Panel (LED Backlight), 450nits, HD(1024x768)	52-351-03006802	1
8	SP-7145 Touch Thin GAP EPDM H (370x5x3mm)	30-013-01100411	2
9	SP-7145 Touch Thin Gap EPDM V (274x5x3mm)	30-013-01200411	2
10	SP-7145 Thin Gap LCD Poron H (326x8x1mm)	30-013-24100411	2

Appendix A System Diagrams

No.	Component Name	P/N No.	Q'ty
11	SP-7145 Thin Gap LCD Poron V(233x8x1mm)	30-013-24200411	2
12	SP-7145 Double Coated Tape A (358.49x14.5x0.8mm)	34-026-06201411	2
13	SP-7145 Double Coated Tape B (252.83x20x0.8mm)	34-026-06202411	2
14	BR-8080RB-S0N	N/A	1
15	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	4
16	Flat Head Screw #2 /ψ 5/M3x0.5Px5mm	22-212-30005311	47

SP-S154 Box PC Exploded Diagram (Resistance)

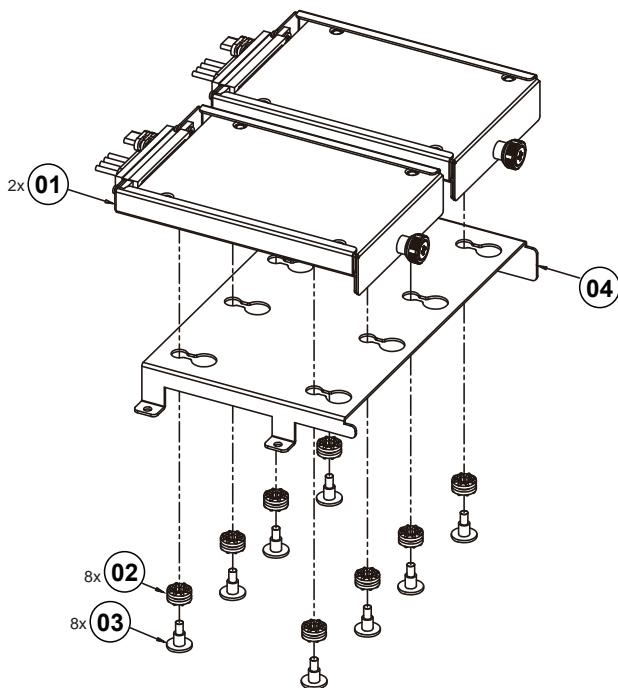


No.	Component Name	P/N No.	Q'ty
1	SP-S214 BR Base BM2502 (w/Paint)(Silver-Gray)	20-132-03061454	1
2	SP-S214 BR Rear Cover BM2502 (w/Paint)(Silver-Gray)	20-104-03062454	1
3	BM-2502_PCB_ASSY_ASM	BM-2502	1
4	ATX 250W AC Power Supply	52-001-00925001	1
5	SP-S214 BR I/O Cover BM2502 (w/Paint) (Silver-Gray)	20-104-03061454	1
6	Round Head With Spring Washer Screw M3x0.5Px6mm	22-232-30060211	8
7	SP-Sxx4 PLPT1 Cable (25p to 26p) L=455mm	27-004-46310031	1
8	SP-Sxx4 COM Cable (9M to 10F) L=350mm	27-024-46307031	2
9	SP-S154/S174/S214 System Fan (50x50x20mm) L=450mm	21-004-05050186	1
10	Flat Head Screw T4.7x11mm	22-112-47011011	4

Appendix A System Diagrams

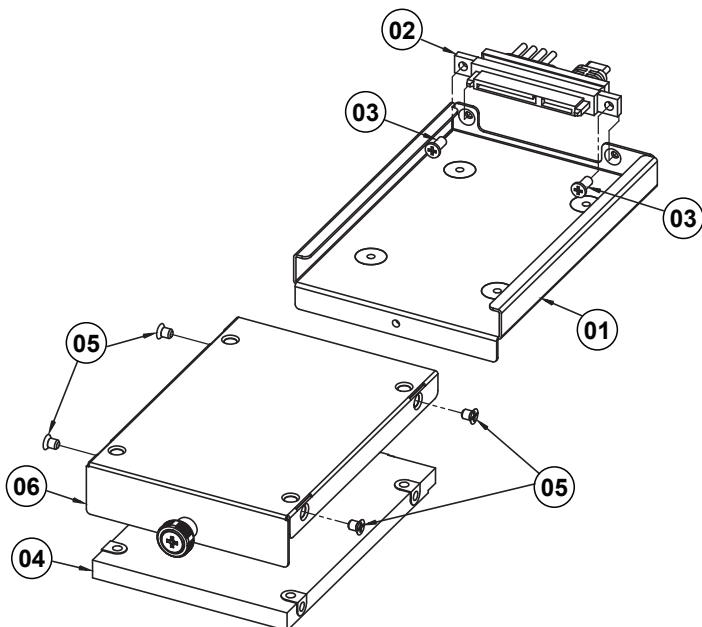
No.	Component Name	P/N No.	Q'ty
11	SP-Sxx4 Power Switch Cable L=380mm+50mm	27-019-46308071	1
12	HEX CU BOSS UNC No.4-40,L=4.8,H=7mm	22-692-40048051	14
13	Pan Head Screw UNC-No.6-32,L=5mm	22-622-60005011	3
14	Flat Head Screw #2/ψ 5 / M3x0.5Px5mm	22-212-30005311	16
15	HDD_Module_Bracket_EXP	See Page A-7	1
16	Touch control board for 5-wire,USB interface	52-370-01040504	1
17	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	2

SP-S154 HDD Module Exploded Diagram (Resistance)



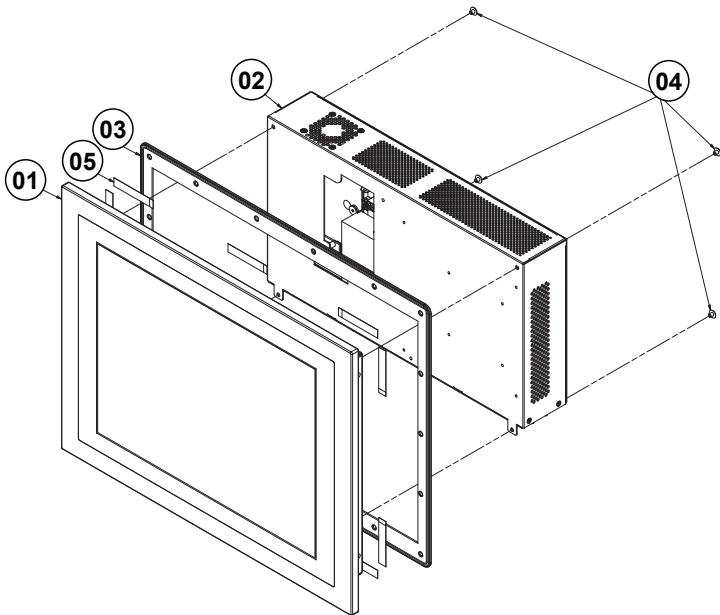
No.	Component Name	P/N No.	Q'ty
1	HDD Module Assy-2_EXP	See Page A-8	2
2	SHOCK-ABSORB Washer	83-680-63580963	8
3	Fillister Head Screw M3x0.5Px4.8mm	82-272-30005013	8
4	SP-S214 HDD Support Bracket	20-106-03002454	1

SP-S154 HDD Tray Exploded Diagram (Resistance)



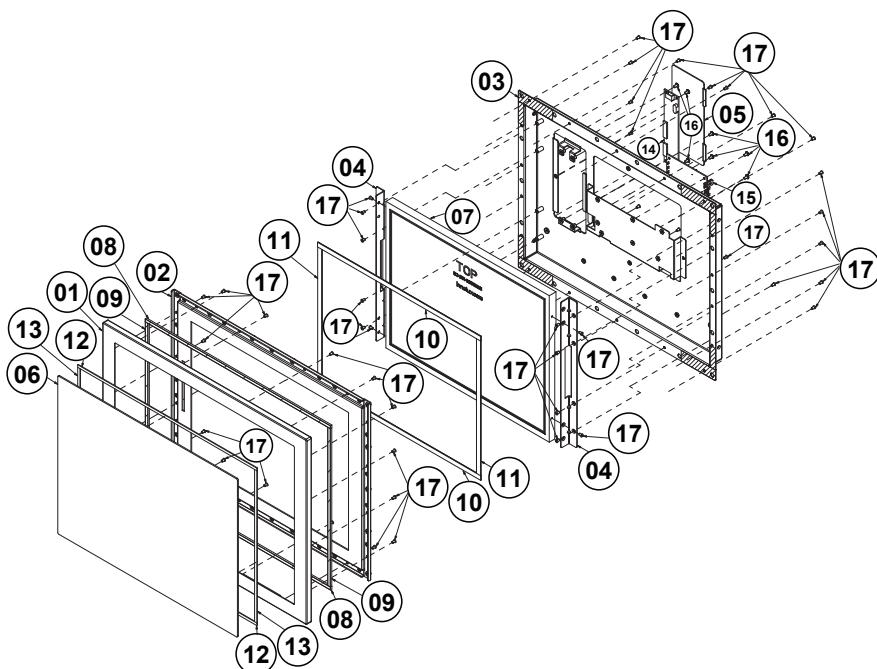
No.	Component Name	P/N No.	Q'ty
1	SP-S214 HDD Cable Holder ASSY	20-129-03001454	1
2	SP-7625 SATA HDD & Power Cable L=150mm+250mm	27-012-32805081	1
3	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	2
4	2.5" HDD / SSD	N/A	1
5	Flat Head Screw #2/ψ 5 / M3x0.5Px5mm	22-212-30005311	4
6	SP-S214 HDD Tray (w/Paint) (Silver-Gray)	20-154-03061454	1

SP-S154 System Exploded Diagram (Capacitance)



No.	Component Name	P/N No.	Q'ty
1	7145_Panel_SUS_EXP	See Page A-10 & A-11	1
2	Box_PC_BM-2502_ASM	See Page A-12 & A-13	1
3	SP-7145 Wall Oring	30-013-01300411	1
4	Truss Head Screw #2 / M4x0.7Px4mm	22-232-40004011	4
5	DSMS Adhesive Tape	94-026-06501000	5

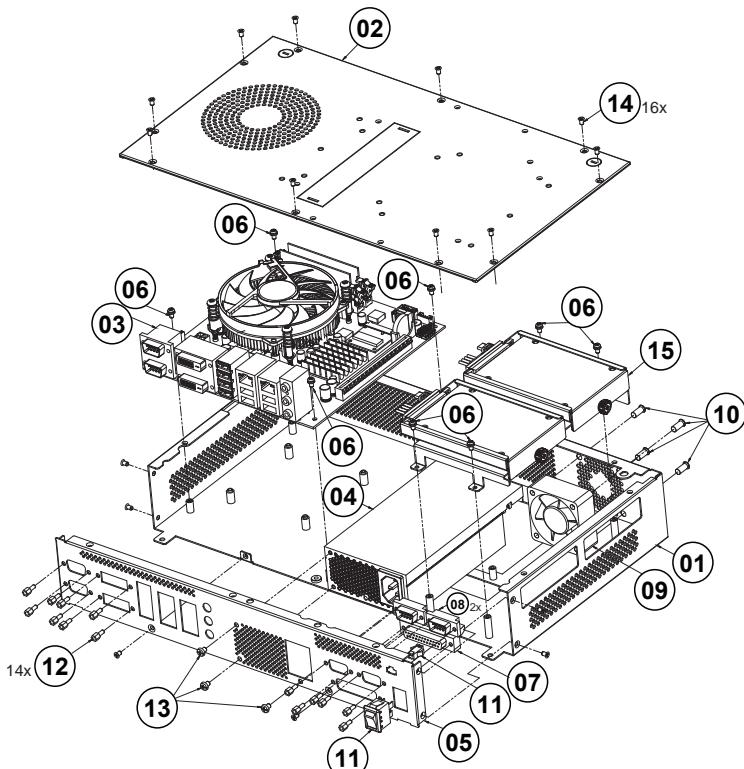
SP-S154 Touch Panel & LCD Display Exploded Diagram (Capacitance)



No.	Component Name	P/N No.	Q'ty
1	SP-7145 SUS Panel Cover	20-004-07001411	1
2	SP-7145 Thin Gap SGCC Cover Kit	20-004-21001411	1
3	SP-7145 LCD Cover Kit (w/Paint)(Silver Gray)	20-004-03061411	1
4	SP-7145 LCD Link Holder	20-029-03001411	2
5	SP-7145 Touch Board Cover Kit (w/Paint) (Silver Gray)	20-004-03062411	1
6	15" Capacitive Multi-Touch Panel	52-380-00075014	1
7	15" TFT LCD Panel (LED Backlight), 450nits, HD(1024x768)	52-351-03006802	1
8	SP-7145 Touch Thin GAP EPDM H (370x5x3mm)	30-013-01100411	2
9	SP-7145 Touch Thin Gap EPDM V (274x5x3mm)	30-013-01200411	2
10	SP-7145 Thin Gap LCD Poron H	30-013-24100411	2

Appendix A System Diagrams

No.	Component Name	P/N No.	Q'ty
	(326x8x1mm)		
11	SP-7145 Thin Gap LCD Poron V(233x8x1mm)	30-013-24200411	2
12	SP-7145 Double Tap H (358x4.5x1.1mm)	34-026-06203411	2
13	SP-7145 Double Tape (263.5x4.5x1.1mm)	34-026-06204411	2
14	Touch Control Board for P-CAP,USB & RS-232 Interface	52-370-01623007	1
15	BR-8080RB-S0N	N/A	1
16	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	8
17	Flat Head Screw #2 /ψ 5/M3x0.5Px5mm	22-212-30005311	47

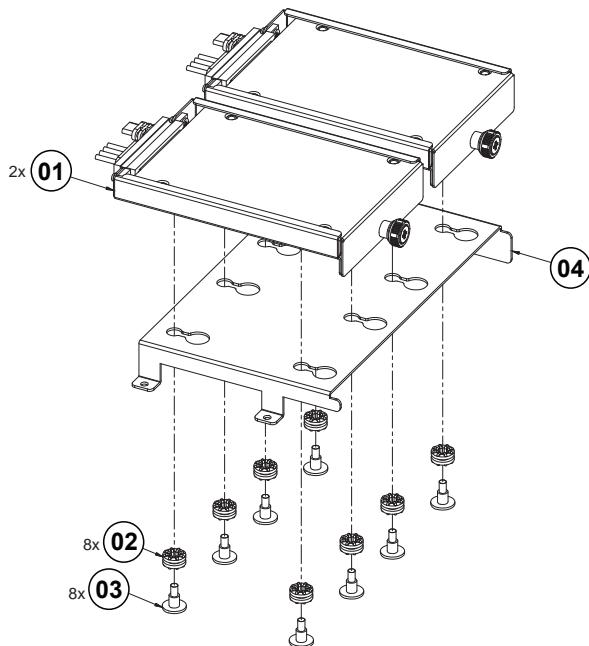
SP-S154 Box PC Exploded Diagram (Capacitance)

No.	Component Name	P/N No.	Q'ty
1	SP-S214 BR Base BM2502 (w/Paint)(Silver-Gray)	20-132-03061454	1
2	SP-S214 BR Rear Cover BM2502 (w/Paint)(Silver-Gray)	20-104-03062454	1
3	BM-2502_PCB_ASSY_ASM	BM-2502	1
4	ATX 250W AC Power Supply	52-001-00925001	1
5	SP-S214 BR I/O Cover BM2502 (w/Paint) (Silver-Gray)	20-104-03061454	1
6	Round Head With Spring Washer Screw M3x0.5Px6mm	22-232-30060211	8
7	SP-SXX4 PLPT1 Cable(25p to 26p) L=455mm	27-004-46310031	1
8	SP-SXX4 COM Cable (9M to 10F) L=350mm	27-024-46307031	2
9	SP-S154/S174/S214 System Fan(50x50x20mm) L=450mm	21-004-05050186	1
10	Flat Head Screw T4.7x11mm	22-112-47011011	4

Appendix A System Diagrams

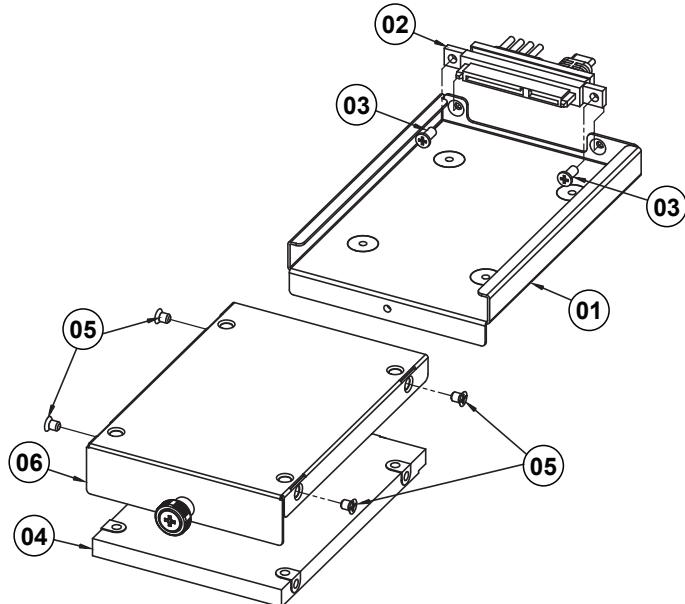
No.	Component Name	P/N No.	Q'ty
11	SP-SXX4 Power Switch Cable L=380mm+50mm	27-019-46308071	1
12	HEX CU BOSS UNC No.4-40, L=4.8, H=7mm	22-692-40048051	14
13	Pan Head Screw UNC-No.6-32,L=5mm	22-622-60005011	3
14	Flat Head Screw #2/ψ 5 / M3x0.5Px5mm	22-212-30005311	16
15	HDD_Module_Bracket_EXP	See Page A-14	1

SP-S154 HDD Module Exploded Diagram (Capacitance)



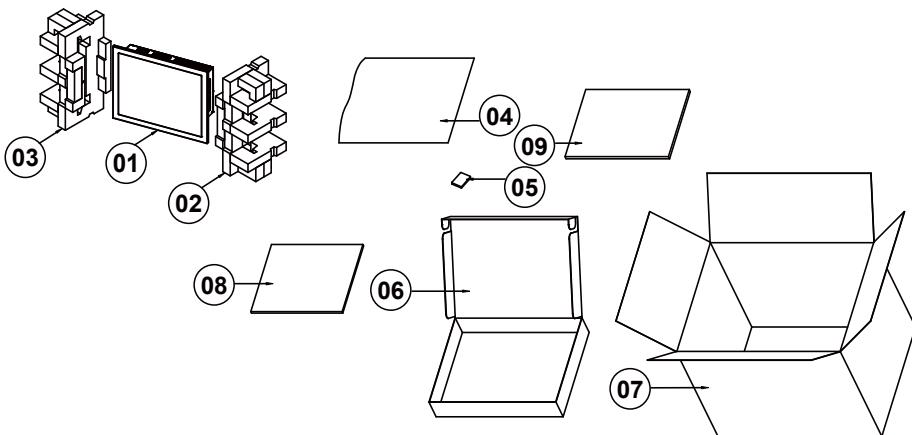
No.	Component Name	P/N No.	Q'ty
1	HDD Module Assy-2_EXP	See Page A-15	2
2	PT-1920 SHOCK-ABSORB Washer	83-680-63580963	8
3	Fillister Head Screw M3x0.5Px4.8mm	82-272-30005013	8
4	SP-S214 HDD Support Bracket	20-106-03002454	1

SP-S154 HDD Tray Exploded Diagram (Capacitance)



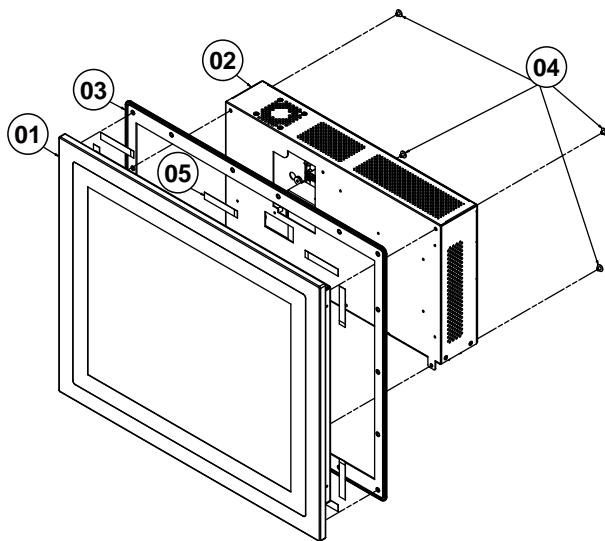
No.	Component Name	P/N No.	Q'ty
1	SP-S214 HDD Cable Holder ASSY	20-129-03001454	1
2	SP-7625 SATA HDD & Power Cable L=150mm+250mm	27-012-32805081	1
3	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	2
4	2.5" HDD / SSD	N/A	1
5	Flat Head Screw #2/ψ 5 / M3x0.5Px5mm	22-212-30005311	4
6	SP-S214 HDD Tray (w/Paint) (Silver-Gray)	20-154-03061454	1

SP-S154 Packing Exploded Diagram



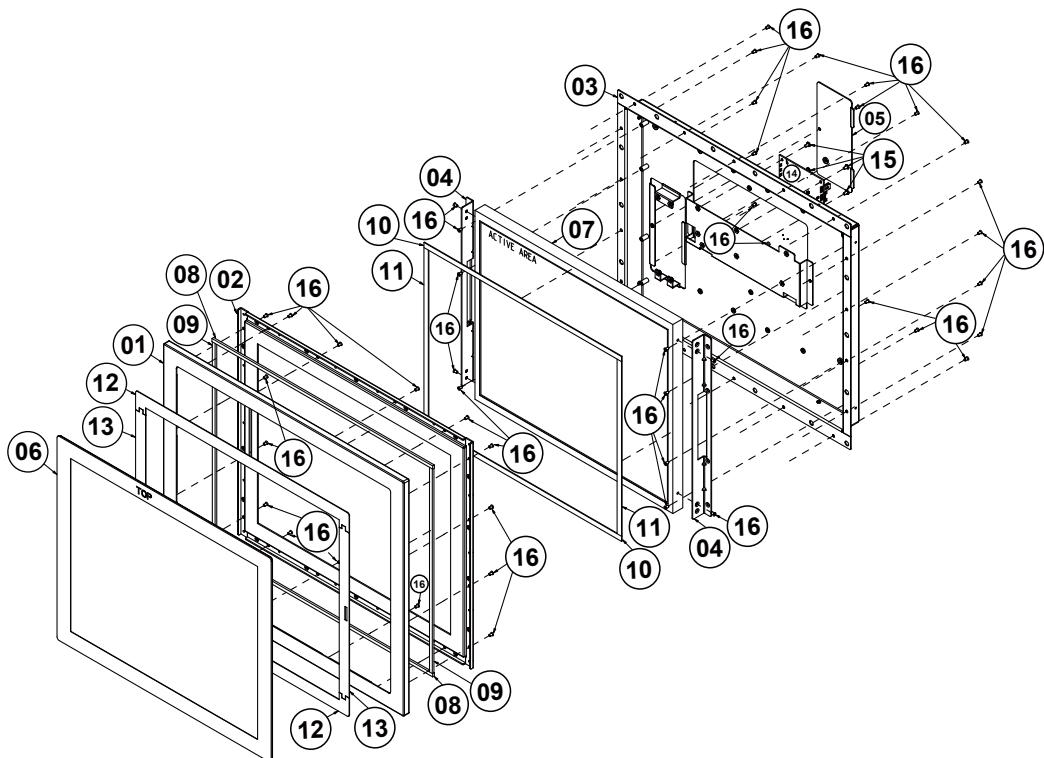
No.	Component Name	P/N No.	Q'ty
1	SP-S154 System	SP-S154	1
2	EPE Right (475x185x186mm)	94-016-00301464	1
3	EPE Left (475x185x186mm)	94-016-00302464	1
4	PE Bag 480X460 (42X56cmX0.07)	32-10020010000	1
5	SILICA GEL 1gm	34-005-00010007	2
6	PS-650X Carton Boxes (332x150x45mm)	34-003-01301086	1
7	MM-7017R Basic Style Outer Carton (532x487x203mm)	94-001-01401258	1
8	Mylar 335x260x0.125	30-056-02100008	1
9	Cardboard (400x300)	34-004-01301008	1

SP-S174 System Exploded Diagram (Resistance)



No.	Component Name	P/N No.	Q'ty
1	7145_Panel_Module_EXP	See Page A-18 & A-19	1
2	Box_PC_BM-2502_ASM	See Page A-20 & A-21	1
3	SP-7147 Wall Oring	30-013-01300412	1
4	Truss Head Screw #2 / M4x0.7Px4mm	22-232-40004011	4
5	DSMS Adhesive Tape	94-026-06501000	5

SP-S174 Touch Panel & LCD Display Exploded Diagram (Resistance)

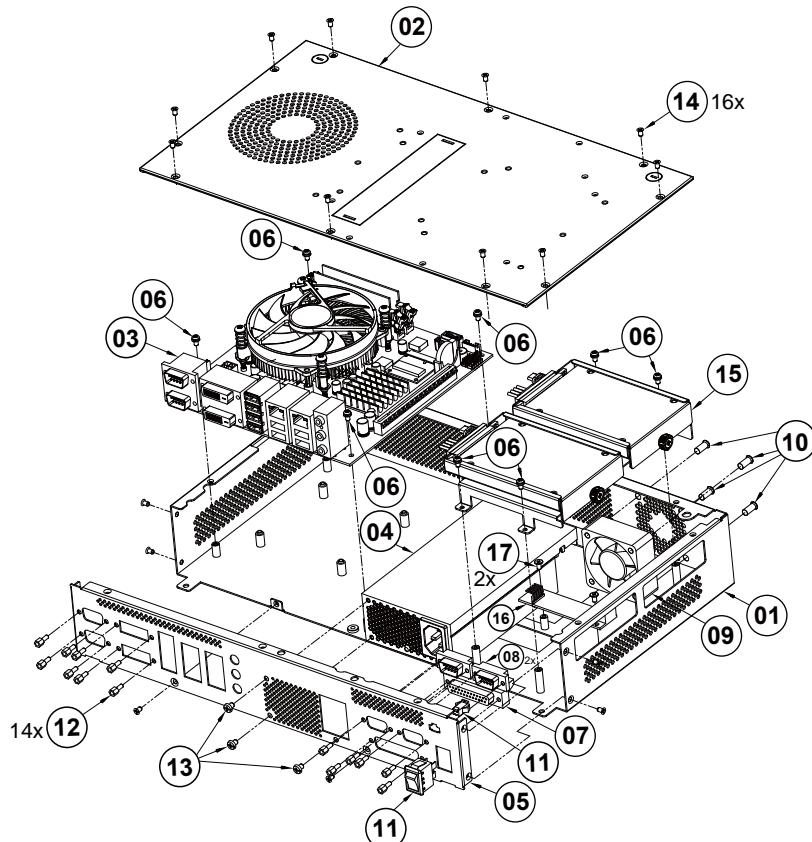


No.	Component Name	P/N No.	Q'ty
1	SP-7147 Thin Gap SUS Cover	20-004-07001412	1
2	SP-7147 Thin Gap SGCC Cover	20-004-21001412	1
3	SP-7147 LCD Cover Kit (w/Paint) (Silver Gray)	20-004-03062412	1
4	SP-7147 LCD Link Holder	20-029-03001412	2
5	SP-7147 ABON Touch Board Cover Kit (w/Paint) (Silver Gray)	20-004-03061412	1
6	17" 5-wire Resistive Touch Panel	52-380-04211114	1
7	17" TFT LCD Panel(LED Backlight), 350nits, SXGA (1280x1024)	52-351-04017002	1
8	SP-7147 Touch Thin Gap EPDM H (400x5x3mm)	30-013-01100412	2
9	SP-7147 Touch Thin Gap EPDM V (321x5x3mm)	30-013-01200412	2
10	ST-2017 Thin Gap LCD PORON-H	30-013-24100366	2

Appendix A System Diagrams

No.	Component Name	P/N No.	Q'ty
	(358x8x1mm)		
11	ST-2017 Thin Gap LCD PORON-V (274x8x1mm)	30-013-24200366	2
12	SP-7147 ABON A-15170-1452 EPDM-H (386.92x30x0.8mm)	34-026-06201412	2
13	SP-7147 ABON A-15170-1452 EPDM-V (271.24x18x0.8mm)	34-026-06202412	2
14	BR-8080RB-00N	N/A	1
15	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	4
16	Flat Head Screw #2/ψ 5/ M3x0.5Px5mm	22-212-30005311	47

SP-S174 Box PC Exploded Diagram (Resistance)

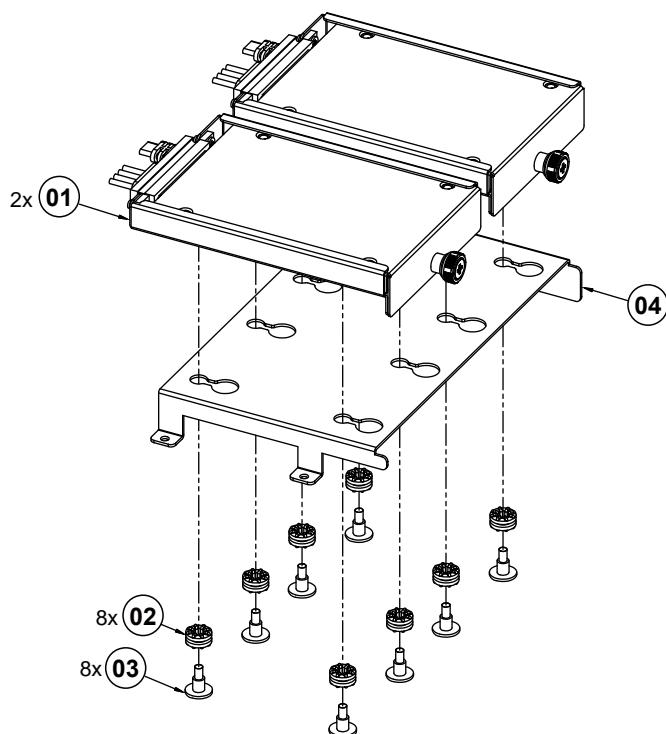


No.	Component Name	P/N No.	Q'ty
1	SP-S214 BR Base BM2502 (w/Paint)(Silver-Gray)	20-132-03061454	1
2	SP-S214 BR Rear Cover BM2502 (w/Paint)(Silver-Gray)	20-104-03062454	1
3	BM-2502_PCB_ASSY_ASM	BM-2502	1
4	ATX 250W AC Power Supply	52-001-00925001	1
5	SP-S214 BR I/O Cover BM2502 (w/Paint) (Silver-Gray)	20-104-03061454	1
6	Round Head With Spring Washer Screw M3x0.5Px6mm	22-232-30060211	8
7	SP-Sxx4 PLPT1 Cable (25p to 26p) L=455mm	27-004-46310031	1
8	SP-Sxx4 COM Cable (9M to 10F) L=350mm	27-024-46307031	2

Appendix A System Diagrams

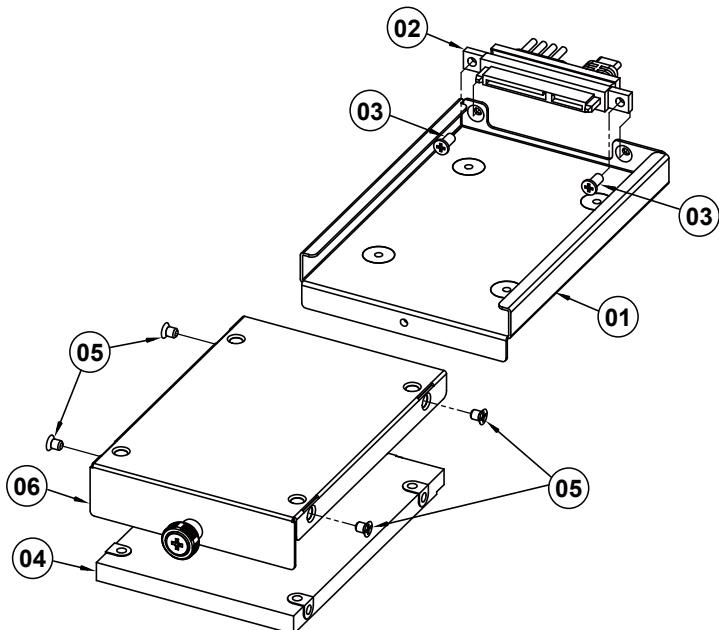
No.	Component Name	P/N No.	Q'ty
9	SP-S154/S174/S214 System Fan (50x50x20mm) L=450mm	21-004-05050186	1
10	Flat Head Screw T4.7x11mm	22-112-47011011	4
11	SP-Sxx4 Power Switch Cable L=380mm+50mm	27-019-46308071	1
12	HEX CU BOSS UNC No.4-40, L=4.8, H=7mm	22-692-40048051	14
13	Pan Head Screw UNC-No.6-32, L=5mm	22-622-60005011	3
14	Flat Head Screw #2/ψ 5 / M3x0.5Px5mm	22-212-30005311	16
15	HDD_Module_Bracket_EXP	See Page A-22	1
16	Touch control board for 5-wire, USB interface	52-370-01040504	1
17	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	2

SP-S174 HDD Module Exploded Diagram (Resistance)



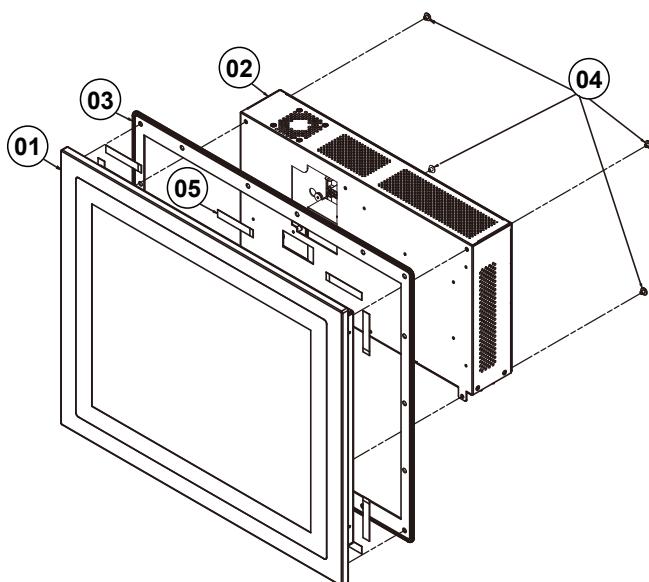
No.	Component Name	P/N No.	Q'ty
1	HDD Module Assy-2_EXP	See Page A-23	2
2	PT-1920 SHOCK-ABSORB Washer	83-680-63580963	8
3	Fillister Head Screw M3x0.5Px4.8mm	82-272-30005013	8
4	HDD Support Bracket	20-106-03002454	1

SP-S174 HDD Tray Exploded Diagram (Resistance)



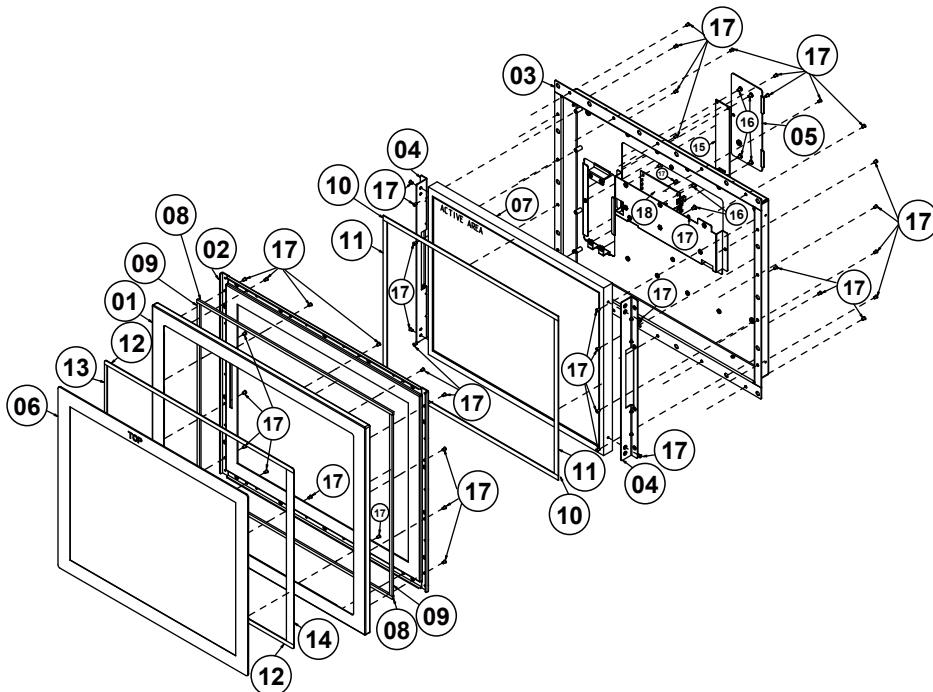
No.	Component Name	P/N No.	Q'ty
1	SP-S214 HDD Cable Holder ASSY	20-129-03001454	1
2	SP-7625 SATA HDD & Power Cable L=150mm+250mm	27-012-32805081	1
3	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	2
4	2.5" HDD / SSD	N/A	1
5	Flat Head Screw #2/ψ 5 / M3x0.5Px5mm	22-212-30005311	4
6	SP-S214 HDD Tray (w/Paint) (Silver-Gray)	20-154-03061454	1

SP-S174 System Exploded Diagram (Capacitance)



No.	Component Name	P/N No.	Q'ty
1	SP-7147_Panel_Module_EXP	See Page A-25 & A-26	1
2	Box PC_BM-2502_ASM	See Page A-27 & A-28	1
3	SP-7147 Wall Oring	30-013-01300412	1
4	Truss Head Screw #2 / M4x0.7Px4mm	22-232-40004011	4
5	DSMS Adhesive Tape	94-026-06501000	5

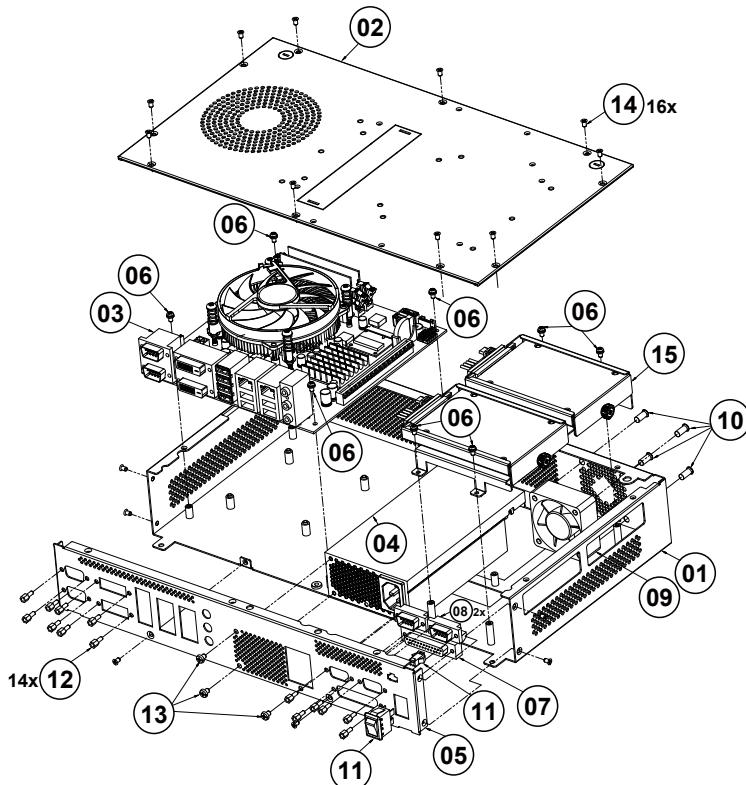
SP-S174 Touch Panel & LCD Display Exploded Diagram (Capacitance)



No.	Component Name	P/N No.	Q'ty
1	SP-7147 Thin Gap SUS Cover	20-004-07001412	1
2	SP-7147 Thin Gap SGCC Cover	20-004-21001412	1
3	SP-7147 LCD Cover Kit (w/Paint) (Silver Gray)	20-004-03062412	1
4	SP-7147 LCD Link Holder	20-029-03001412	2
5	SP-7147 ABON Touch Board Cover Kit (w/Paint) (Silver Gray)	20-004-03061412	1
6	17" P-CAP Touch Panel	52-380-04121114	1
7	17" TFT LCD Panel (LED Backlight), 350nits, SXGA(1280x1024)	52-351-04017002	1
8	SP-7147 Touch Thin Gap EPDM H (400x5x3mm)	30-013-01100412	2
9	SP-7147 Touch Thin Gap EPDM V (321x5x3mm)	30-013-01200412	2
10	ST-2017 Thin Gap LCD PORON-H	30-013-24100366	2

Appendix A System Diagrams

No.	Component Name	P/N No.	Q'ty
	(358x8x1mm)		
11	ST-2017 Thin Gap LCD PORON-V (274x8x1mm)	30-013-24200366	2
12	SP-7147 ABON TP A-68170-0401 EPDM-H (387x7.5x1.1mm)	34-026-06203412	2
13	SP-7147 ABON TP A-68170-0401 EPDM-V A (304x15x1.1mm)	34-026-06204412	1
14	SP-7147 ABON TP A-68170-0401 EPDM-V B (304x5x1.1mm)	34-026-06205412	1
15	Touch Control Board for P-CAP, USB&RS232 Interface	52-370-02318007	1
16	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	8
17	Flat Head Screw #2 /ψ 5 / M3x0.5Px5mm	22-212-30005311	47
18	BR-8080RB-S0N	N/A	1

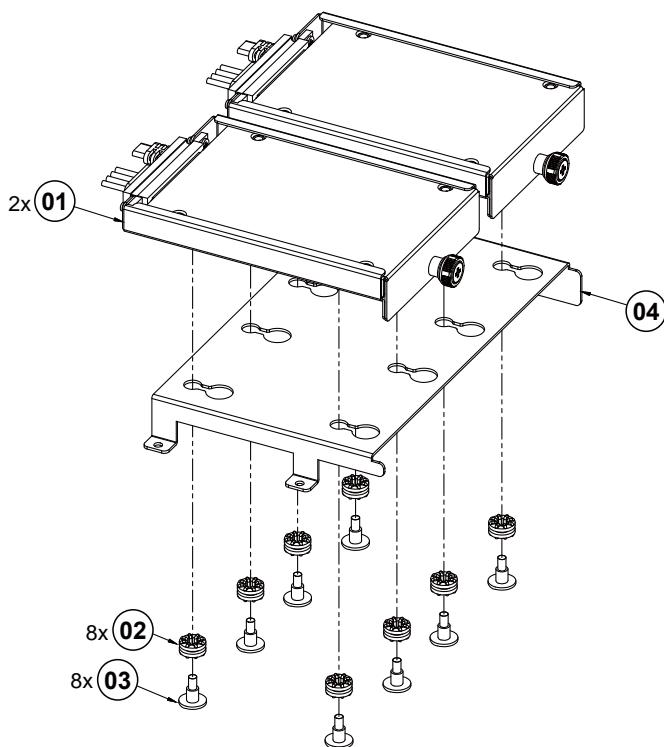
SP-S174 Box PC Exploded Diagram (Capacitance)

No.	Component Name	P/N No.	Q'ty
1	SP-S214 BR Base BM2502 (w/Paint) (Silver-Gray)	20-132-03061454	1
2	SP-S214 BR Rear Cover BM2502 (w/Paint)(Silver-Gray)	20-104-03062454	1
3	BM-2502 PCB ASSY ASM	BM-2502	1
4	ATX 250W AC Power Supply	52-001-00925001	1
5	SP-S214 BR I/O Cover BM2502 (w/Paint) (Silver-Gray)	20-104-03061454	1
6	Round Head With Spring Washer Screw M3x0.5Px6mm	22-232-30060211	8
7	SP-SXX4 PLPT1 Cable(25p to 26p) L=455mm	27-004-46310031	1
8	SP-SXX4 COM CableABLE(9M to 10F) L=350mm	27-024-46307031	2
9	SP-S154/S174/S214 System Fan (50x50x20mm) L=450mm	21-004-05050186	1

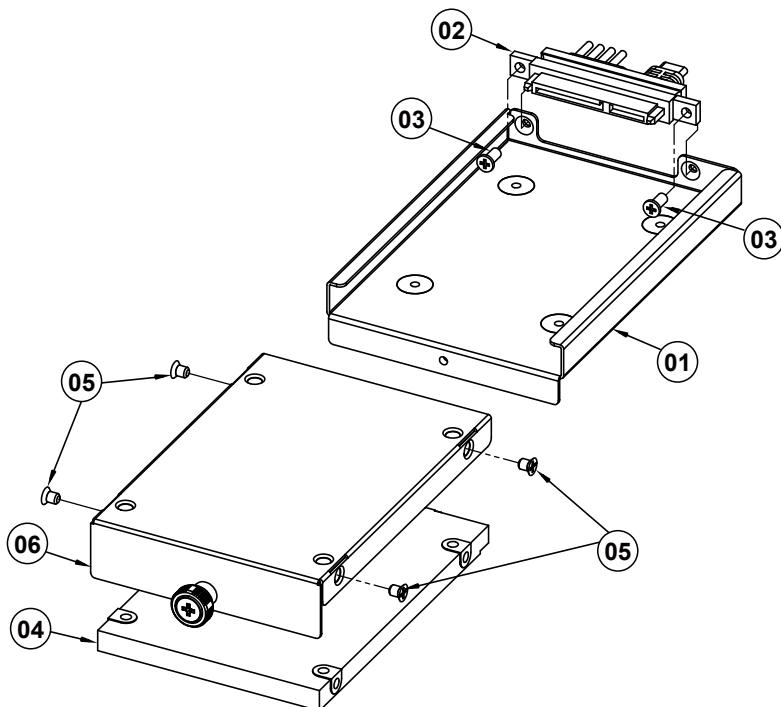
Appendix A System Diagrams

No.	Component Name	P/N No.	Q'ty
10	Flat Head Screw T4.7x11mm	22-112-47011011	4
11	SP-SXX4 Power Switch Cable L=380mm+50mm	27-019-46308071	1
12	HEX CU BOSS UNC No.4-40, L=4.8, H=7mm	22-692-40048051	14
13	Pan Head Screw UNC-No.6-32,L=5mm	22-622-60005011	3
14	Flat Head Screw #2 /ψ 5 / M3x0.5Px5mm	22-212-30005311	16
15	HDD Module Bracket EXP	See Page A-29	1

SP-S174 HDD Module Exploded Diagram (Capacitance)

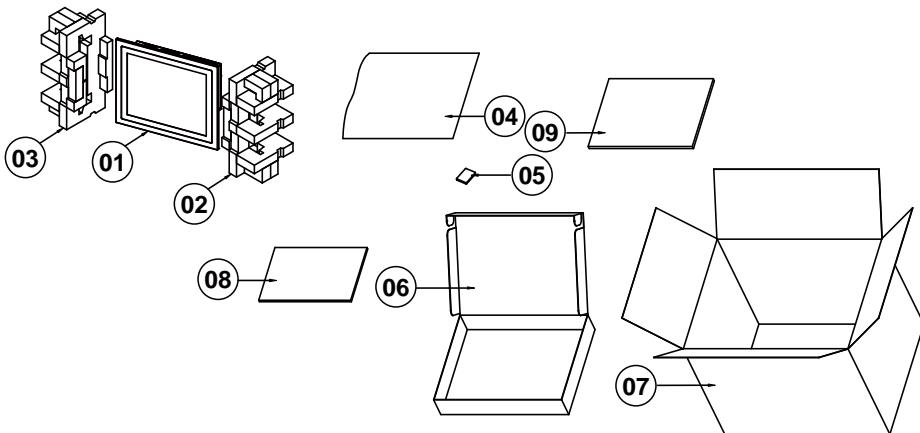


No.	Component Name	P/N No.	Q'ty
1	HDD_Module_Assy-2_EXP	See Page A-30	2
2	PT-1920 SHOCK-ABSORB Washer	83-680-63580963	8
3	Fillister Head Screw M3x0.5Px4.8mm	82-272-30005013	8
4	HDD Support Bracket	20-106-03002454	1

SP-S174 HDD Tray Exploded Diagram (Capacitance)

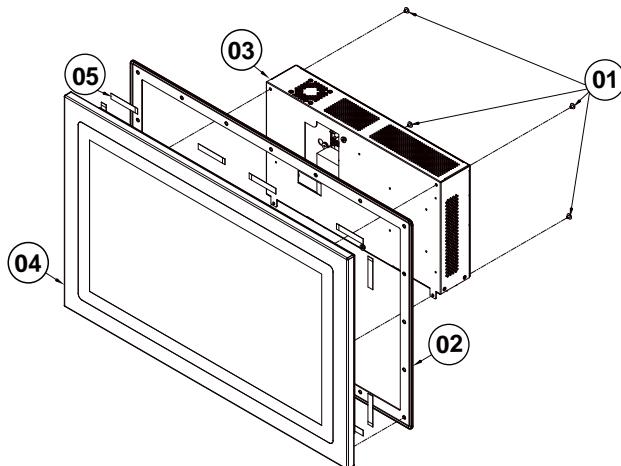
No.	Component Name	P/N No.	Q'ty
1	SP-S214 HDD Cable Holder ASSY	20-129-03001454	1
2	SP-7625 SATA HDD & Power Cable L=150mm+250mm	27-012-32805081	1
3	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	2
4	2.5" HDD / SSD	N/A	1
5	Flat Head Screw #2/ψ 5 / M3x0.5Px5mm	22-212-30005311	4
6	SP-S214 HDD Tray (w/Paint) (Silver-Gray)	20-154-03061454	1

SP-S174 Packing Exploded Diagram

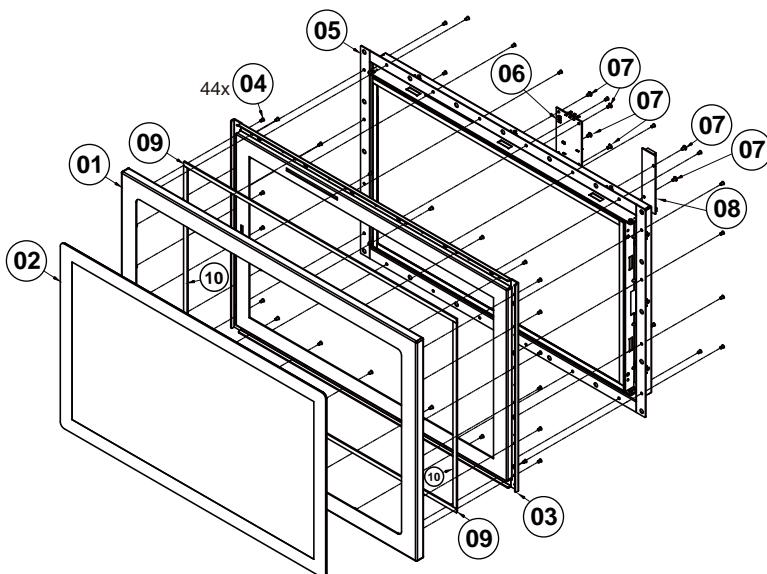


No.	Component Name	P/N No.	Q'ty
1	SP-S174 System	SP-S174	1
2	EPE Right (475x185x186mm)	94-016-00301464	1
3	EPE Left (475x185x186mm)	94-016-00302464	1
4	PE Bag 870x670x0.07mm	34-010-00210003	1
5	SILICA GEL 1gm	34-005-00010007	2
6	Carton Boxes (332x150x45mm)	34-003-01301086	1
7	MM-7017R Basic Style Outer Carton (532x487x203mm)	94-001-01401258	1
8	PS-8710A Mylar FOR 17" Panel	30-056-02100071	1
9	Cardboard (400x300)	34-004-01301008	1

SP-S214 System Exploded Diagram

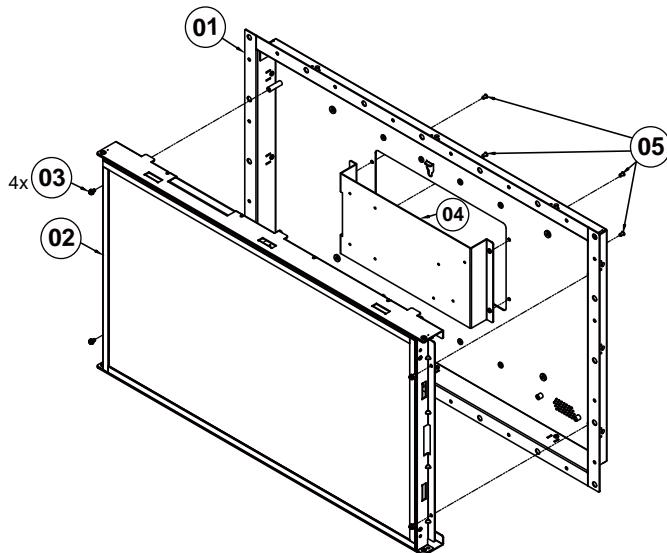


No.	Component Name	P/N No.	Q'ty
1	Truss Head Screw #2 / M4x0.7Px4mm	22-232-40004011	4
2	Wall Oring	90-013-06100454	1
3	BOX_PC_BM-2502_ASM	See Page A-36	1
4	Panel_LCD_Module_Kit	See Page A-33	1
5	DSMS Adhesive Tape	94-026-06501000	6

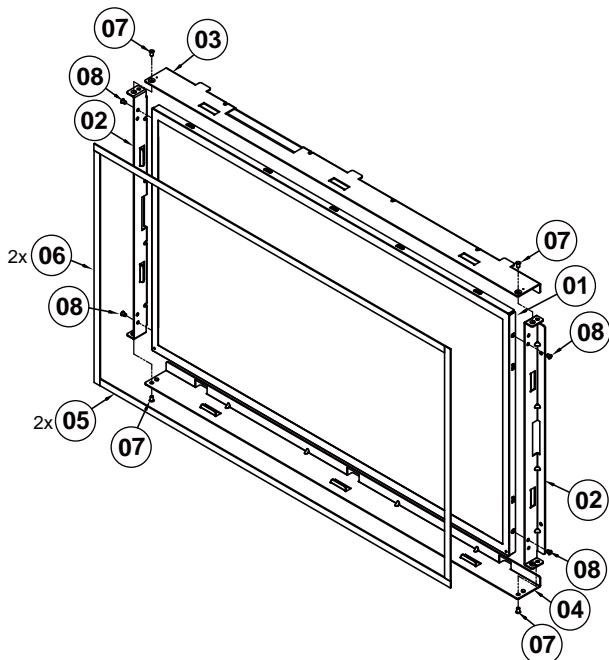
SP-S214 Touch Panel & LCD Display Exploded Diagram

No.	Component Name	P/N No.	Q'ty
1	SP-S214 SUS Front Cover ASSY	20-104-07001454	1
2	21.5" Capacitive Multi-Touch Panel	52-380-13216717	1
3	SP-S214 Touch Panel Holder	20-129-21001454	1
4	Flat Head Screw #2/ψ 5 / M3x0.5Px5mm	22-212-30005311	44
5	LCD Module Kit EXP	See Page A-34	1
6	BR-8080 ASM	BR-8080RB-00N	1
7	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	6
8	Z LED Driver Board	52-152-29201530	1
9	SP-S214 Touch Thin Gap EPDM H (540x7x2mm)	30-013-01200454	2
10	SP-S214 Touch Thin Gap EPDM V (318x7x2mm)	30-013-01100454	2

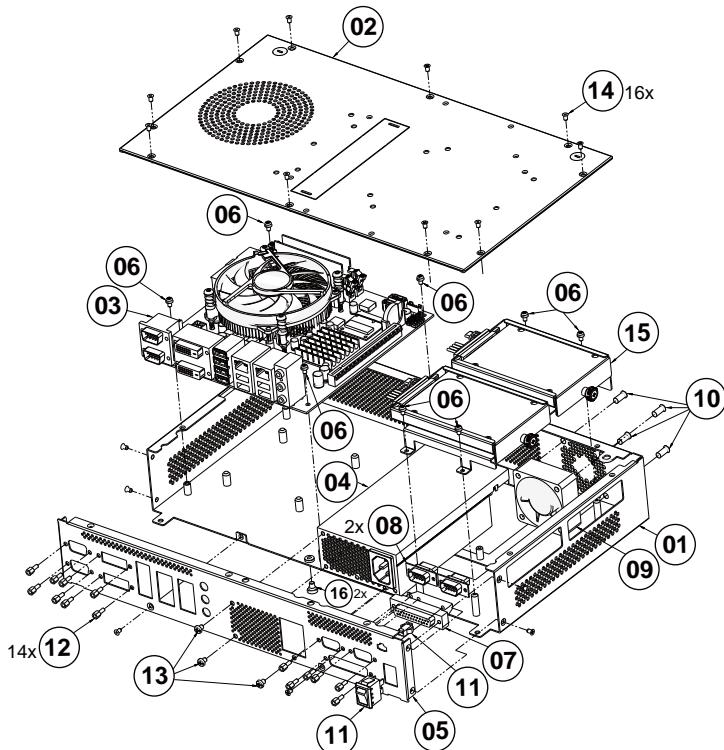
SP-S214 LCD Panel Exploded Diagram (1)



No.	Component Name	P/N No.	Q'ty
1	SP-S214 LCD Cover (w/Paint) (Silver-Gray)	20-104-03063454	1
2	G215HAN01_2_LCD_ASM_EXP	See Page A-35	1
3	Round Head With Spring Washer Screw M3x0.5Px6mm	22-232-30060211	4
4	SP-S214 Driver Board Steady	20-106-03001454	1
5	Flat Head Screw #2 /ψ 5 / M3x0.5Px5mm	22-212-30005311	4

SP-S214 LCD Panel Exploded Diagram (2)

No.	Component Name	P/N No.	Q'ty
1	21.5" TFT LCD Panel (LED Backlight), 350nits(1920x1080)	52-351-13215202	1
2	SP-S214 LCD Holder LR	20-129-03004454	2
3	SP-S214 LCD Holder HT	20-129-03003454	1
4	SP-S214 LCD Holder HB	20-129-03002454	1
5	SP-S214 Thin Gap LCD PORON H (488x8x1mm)	30-013-24200454	2
6	SP-S214 Thin Gap LCD PORON V (279.5x8x1mm)()	30-013-24100454	2
7	Flat Head Screw #2/ψ 5 / M3x0.5Px5mm	22-212-30005311	4
8	Fillister Head Screw M3x0.5Px3mm	22-272-30003011	4

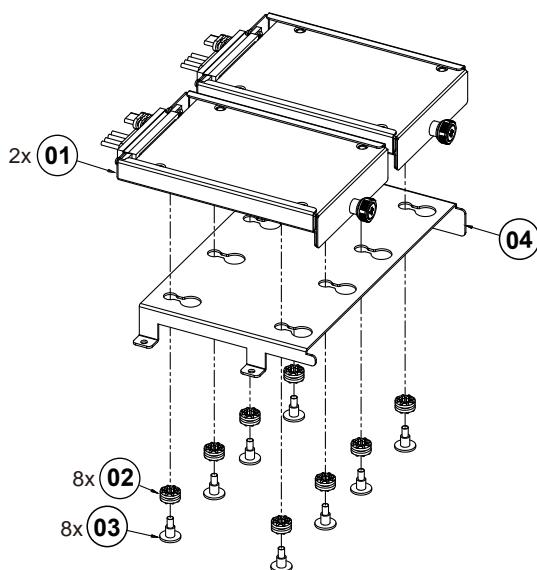
SP-S214 Box PC Exploded Diagram

No.	Component Name	P/N No.	Q'ty
1	SP-S214 BR Base BM2502 (w/Paint) (Silver-Gray)	20-132-03061454	1
2	SP-S214 BR Rear Cover BM2502 (w/Paint)(Silver-Gray)	20-104-03062454	1
3	BM-2502_PCB_ASSY_ASM	BM-2502	1
4	ATX 250W AC Power Supply	52-001-00925001	1
5	SP-S214 BR I/O Cover BM2502 (w/Paint) (Silver-Gray)	20-104-03061454	1
6	Round Head With Spring Washer Screw M3x0.5Px6mm	22-232-30060211	8
7	SP-SXX4 PLPT1 Cable(25p to 26p) L=455mm	27-004-46310031	1
8	SP-SXX4 COM CableABLE(9M to 10F) L=350mm	27-024-46307031	2

Appendix A System Diagrams

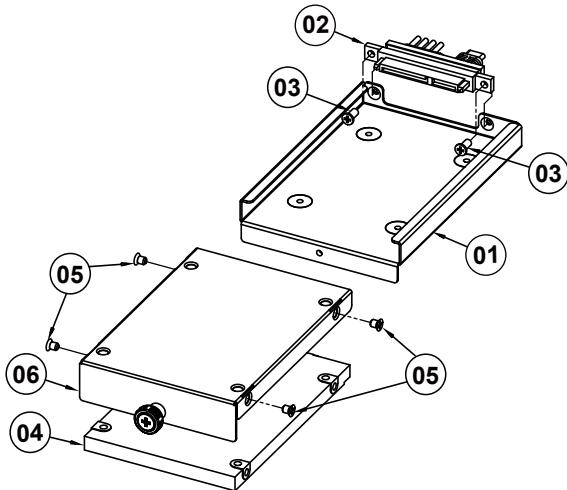
No.	Component Name	P/N No.	Q'ty
9	SP-S154/S174/S214 System Fan (50x50x20mm) L=450mm	21-004-05050186	1
10	Flat Head Screw T4.7x11mm	22-112-47011011	4
11	SP-SXX4 Power Switch Cable L=380mm+50mm	27-019-46308071	1
12	HEX CU BOSS UNC No.4-40, L=4.8, H=7mm	22-692-40048051	14
13	Pan Head Screw UNC-No.6-32,L=5mm	22-622-60005011	3
14	Flat Head Screw #2 /ψ 5 / M3x0.5Px5mm	22-212-30005311	16
15	HDD_Module_Bracket_EXP	See Page A-38	1
16	Fillister Head Screw #2/M4x0.7Px6mm	22-272-40006911	2

SP-S214 HDD Module Exploded Diagram



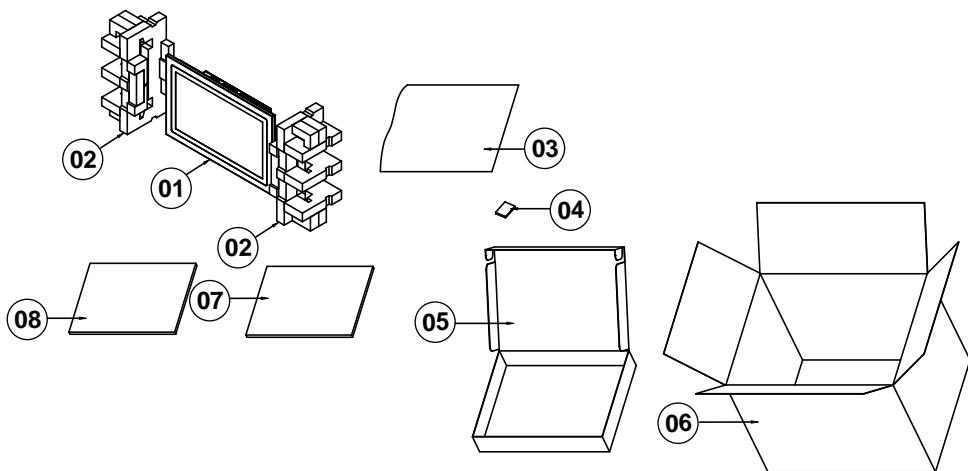
No.	Component Name	P/N No.	Q'ty
1	HDD_Module_Assy-2_EXP	See Page A-39	2
2	PT-1920 SHOCK-ABSORB Washer	83-680-63580963	8
3	Fillister Head Screw M3x0.5Px4.8mm	82-272-30005013	8
4	HDD Support Bracket	20-106-03002454	1

SP-S214 HDD Tray Exploded Diagram



No.	Component Name	P/N No.	Q'ty
1	SP-S214 HDD Cable Holder ASSY	20-129-03001454	1
2	SP-7625 SATA HDD & Power Cable L=150mm+250mm	27-012-32805081	1
3	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	2
4	2.5" HDD / SSD	N/A	1
5	Flat Head Screw #2/ψ 5 / M3x0.5Px5mm	22-212-30005311	4
6	SP-S214 HDD Tray (w/Paint) (Silver-Grey)	20-154-03061454	1

SP-S214 Packing Exploded Diagram



No.	Component Name	P/N No.	Q'ty
1	SP-S214 System	SP-S214	1
2	SP-S214 EPE (455x215x182mm)	94-016-00301454	2
3	PE Bag 870x670x0.07mm	34-010-00210003	1
4	SILICA GEL 1gm	34-005-00010007	2
5	Carton Boxes (332x150x45mm)	34-003-01301086	1
6	Outer Carton (668x468x233mm)	94-001-01401454	1
7	KT-7290 21.5" Mylar for Touch Panel (525x316x0.125mm)	90-056-25100447	1
8	Cardboard (580x370mm)	94-004-01801454	1

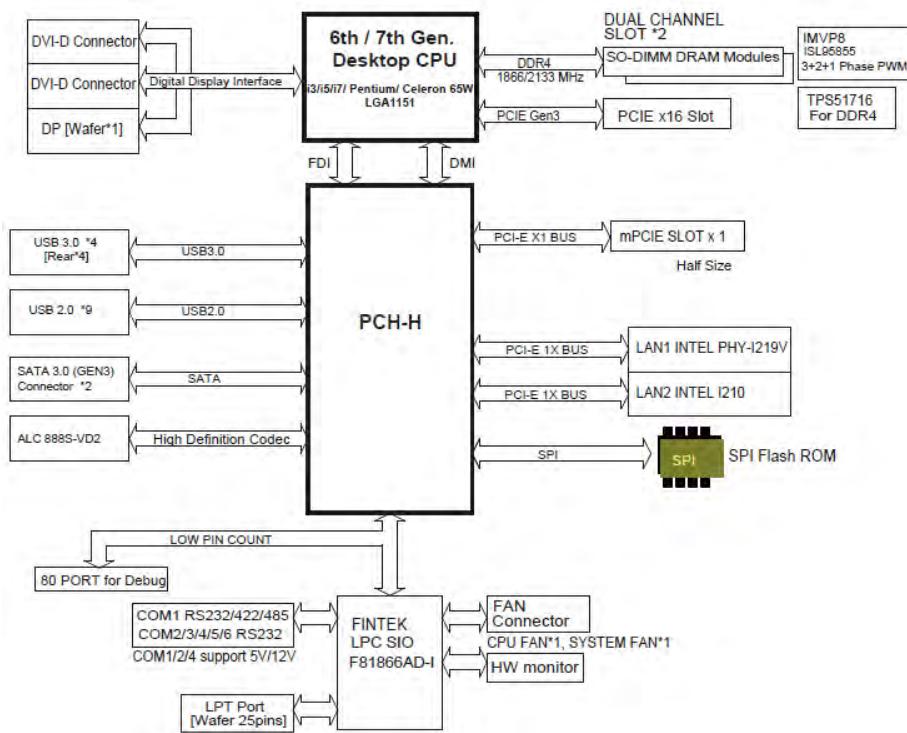
Appendix B Technical Summary

This appendix will give you a brief introduction of the allocation maps for the system resources.

The following topics are included:

- System Block Diagram
- Interrupt Map
- I/O Map
- Memory Map
- Configuring WatchDog Timer
- Flash BIOS Update

Main Board Block Diagram



Interrupt Map

IRQ	ASSIGNMENT
IRQ 0	System timer
IRQ 3	Communications Port (COM2)
IRQ 4	Communications Port (COM1)
IRQ 6	Communications Port (COM5)
IRQ 7	Communications Port (COM3)
IRQ 8	System CMOS/real time clock
IRQ 10	Communications Port (COM4)
IRQ 11	Communications Port (COM6)
IRQ 11	Intel(R) 100 Series/C230 Series Chipset Family SMBus - A123
IRQ 11	Intel(R) 100 Series/C230 Series Chipset Family Thermal subsystem - A131
IRQ 13	Numeric data processor
IRQ 14	Motherboard resources
IRQ 16	High Definition Audio Controller
IRQ 19	Intel(R) Active Management Technology - SOL (COM7)
IRQ 54	Microsoft ACPI-Compliant System
IRQ 55	Microsoft ACPI-Compliant System
IRQ 56	Microsoft ACPI-Compliant System
IRQ 57	Microsoft ACPI-Compliant System
IRQ 58	Microsoft ACPI-Compliant System
IRQ 59	Microsoft ACPI-Compliant System
IRQ 60	Microsoft ACPI-Compliant System
IRQ 61	Microsoft ACPI-Compliant System
IRQ 62	Microsoft ACPI-Compliant System
IRQ 63	Microsoft ACPI-Compliant System
IRQ 64	Microsoft ACPI-Compliant System
IRQ 65	Microsoft ACPI-Compliant System
IRQ 66	Microsoft ACPI-Compliant System
IRQ 67	Microsoft ACPI-Compliant System
IRQ 68	Microsoft ACPI-Compliant System
IRQ 69	Microsoft ACPI-Compliant System
IRQ 70	Microsoft ACPI-Compliant System
IRQ 71	Microsoft ACPI-Compliant System
IRQ 72	Microsoft ACPI-Compliant System
IRQ 73	Microsoft ACPI-Compliant System
IRQ 74	Microsoft ACPI-Compliant System

IRQ	ASSIGNMENT
IRQ 75	Microsoft ACPI-Compliant System
IRQ 76	Microsoft ACPI-Compliant System
IRQ 77	Microsoft ACPI-Compliant System
IRQ 78	Microsoft ACPI-Compliant System
IRQ 79	Microsoft ACPI-Compliant System
IRQ 80	Microsoft ACPI-Compliant System
IRQ 81	Microsoft ACPI-Compliant System
IRQ 82	Microsoft ACPI-Compliant System
IRQ 83	Microsoft ACPI-Compliant System
IRQ 84	Microsoft ACPI-Compliant System
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IRQ 97	Microsoft ACPI-Compliant System
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IRQ 102	Microsoft ACPI-Compliant System
IRQ 103	Microsoft ACPI-Compliant System
IRQ 104	Microsoft ACPI-Compliant System
IRQ 105	Microsoft ACPI-Compliant System
IRQ 106	Microsoft ACPI-Compliant System
IRQ 107	Microsoft ACPI-Compliant System
IRQ 108	Microsoft ACPI-Compliant System
IRQ 109	Microsoft ACPI-Compliant System
IRQ 110	Microsoft ACPI-Compliant System
IRQ 111	Microsoft ACPI-Compliant System
IRQ 112	Microsoft ACPI-Compliant System
IRQ 113	Microsoft ACPI-Compliant System

IRQ	ASSIGNMENT
IRQ 114	Microsoft ACPI-Compliant System
IRQ 115	Microsoft ACPI-Compliant System
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IRQ	ASSIGNMENT
IRQ 153	Microsoft ACPI-Compliant System
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IRQ	ASSIGNMENT
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IRQ	ASSIGNMENT
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IRQ	ASSIGNMENT
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IRQ	ASSIGNMENT
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IRQ	ASSIGNMENT
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IRQ	ASSIGNMENT
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IRQ	ASSIGNMENT
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IRQ 510	Microsoft ACPI-Compliant System
IRQ 511	Microsoft ACPI-Compliant System
IRQ 4294967283	Intel(R) Management Engine Interface
IRQ 4294967284	Intel(R) I210 Gigabit Network Connection
IRQ 4294967285	Intel(R) I210 Gigabit Network Connection
IRQ 4294967286	Intel(R) I210 Gigabit Network Connection

Appendix B Technical Summary

IRQ	ASSIGNMENT
IRQ 4294967287	Intel(R) I210 Gigabit Network Connection
IRQ 4294967288	Intel(R) I210 Gigabit Network Connection
IRQ 4294967289	Intel(R) I210 Gigabit Network Connection
IRQ 4294967290	Intel(R) USB 3.0 eXtensible Host Controller - 1.0 (Microsoft)
IRQ 4294967291	Intel(R) HD Graphics 630
IRQ 4294967292	Intel(R) Ethernet Connection (2) I219-LM
IRQ 4294967293	Intel(R) 100 Series/C230 Chipset Family SATA AHCI Controller
IRQ 4294967294	Intel(R) 100 Series/C230 Series Chipset Family PCI Express Root Port #6 - A115

I/O MAP

I/O	ASSIGNMENT
0x00000000-0x00000CF7	PCI Express Root Complex
0x00000020-0x00000021	Programmable interrupt controller
0x00000024-0x00000025	Programmable interrupt controller
0x00000028-0x00000029	Programmable interrupt controller
0x0000002C-0x0000002D	Programmable interrupt controller
0x0000002E-0x0000002F	Motherboard resources
0x00000030-0x00000031	Programmable interrupt controller
0x00000034-0x00000035	Programmable interrupt controller
0x00000038-0x00000039	Programmable interrupt controller
0x0000003C-0x0000003D	Programmable interrupt controller
0x00000040-0x00000043	System timer
0x0000004E-0x0000004F	Motherboard resources
0x00000050-0x00000053	System timer
0x00000061-0x00000061	Motherboard resources
0x00000063-0x00000063	Motherboard resources
0x00000065-0x00000065	Motherboard resources
0x00000067-0x00000067	Motherboard resources
0x00000070-0x00000070	Motherboard resources
0x00000070-0x00000070	System CMOS/real time clock
0x00000080-0x00000080	Motherboard resources
0x00000092-0x00000092	Motherboard resources
0x000000A0-0x000000A1	Programmable interrupt controller
0x000000A4-0x000000A5	Programmable interrupt controller
0x000000A8-0x000000A9	Programmable interrupt controller
0x000000AC-0x000000AD	Programmable interrupt controller
0x000000B0-0x000000B1	Programmable interrupt controller
0x000000B2-0x000000B3	Motherboard resources
0x000000B4-0x000000B5	Programmable interrupt controller
0x000000B8-0x000000B9	Programmable interrupt controller
0x000000BC-0x000000BD	Programmable interrupt controller
0x000000F0-0x000000F0	Numeric data processor
0x000002E0-0x000002E7	Communications Port (COM6)
0x000002E8-0x000002EF	Communications Port (COM4)
0x000002F0-0x000002F7	Communications Port (COM5)
0x000002F8-0x000002FF	Communications Port (COM2)
0x00000378-0x0000037F	Printer Port (LPT1)
0x000003B0-0x000003BB	Intel(R) HD Graphics 630

Appendix B Technical Summary

I/O	ASSIGNMENT
0x000003C0-0x000003DF	Intel(R) HD Graphics 630
0x000003E8-0x000003EF	Communications Port (COM3)
0x000003F8-0x000003FF	Communications Port (COM1)
0x000004D0-0x000004D1	Programmable interrupt controller
0x00000680-0x0000069F	Motherboard resources
0x00000800-0x0000087F	Motherboard resources
0x00000A00-0x00000A0F	Motherboard resources
0x00000A10-0x00000A1F	Motherboard resources
0x00000A20-0x00000A2F	Motherboard resources
0x00000D00-0x0000FFFF	PCI Express Root Complex
0x0000164E-0x0000164F	Motherboard resources
0x00001800-0x000018FE	Motherboard resources
0x00001854-0x00001857	Motherboard resources
0x0000E000-0x0000EFFF	Intel(R) 100 Series/C230 Series Chipset Family PCI Express Root Port #6 - A115
0x0000F000-0x0000F03F	Intel(R) HD Graphics 630
0x0000F040-0x0000F05F	Intel(R) 100 Series/C230 Series Chipset Family SMBus - A123
0x0000F060-0x0000F07F	Intel(R) 100 Series/C230 Chipset Family SATA AHCI Controller
0x0000F080-0x0000F083	Intel(R) 100 Series/C230 Chipset Family SATA AHCI Controller
0x0000F090-0x0000F097	Intel(R) 100 Series/C230 Chipset Family SATA AHCI Controller
0x0000F0A0-0x0000F0A7	Intel(R) Active Management Technology - SOL (COM7)
0x0000FF00-0x0000FFE	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources

Memory Map

MEMORY MAP	ASSIGNMENT
0xFED10000-0xFED17FFF	Motherboard resources
0xFED18000-0xFED18FFF	Motherboard resources
0xFED19000-0xFED19FFF	Motherboard resources
0xE0000000-0xFFFFFFFF	Motherboard resources
0xFED20000-0xFED3FFFF	Motherboard resources
0xFED90000-0xFED93FFF	Motherboard resources
0xFED45000-0xFED8FFFF	Motherboard resources
0xFF000000-0xFFFFFFFF	Motherboard resources
0xFF000000-0xFFFFFFFF	Legacy device
0xFEE00000-0xFEEFFFFFF	Motherboard resources
0xDFFE0000-0xDFFFFFFF	Motherboard resources
0xFDAF0000-0xFDAFFFFFF	Motherboard resources
0xFDAE0000-0xFDAEFFFF	Motherboard resources
0xFDAC0000-0xFDACFFFF	Motherboard resources
0xDE000000-0xDEFFFFFF	Intel(R) HD Graphics 630
0xC0000000-0xCFFFFFFF	Intel(R) HD Graphics 630
0xDF100000-0xDF11FFFF	Intel(R) Ethernet Connection (2) I219-LM
0xDF000000-0xDF0FFFFF	Intel(R) 100 Series/C230 Series Chipset Family PCI Express Root Port #6 - A115
0xDF000000-0xDF0FFFFF	Intel(R) I210 Gigabit Network Connection
0xFED00000-0xFED003FF	High precision event timer
0xDF148000-0xDF149FFF	Intel(R) 100 Series/C230 Chipset Family SATA AHCI Controller
0xDF14C000-0xDF14C0FF	Intel(R) 100 Series/C230 Chipset Family SATA AHCI Controller
0xDF14B000-0xDF14B7FF	Intel(R) 100 Series/C230 Chipset Family SATA AHCI Controller
0xFD000000-0xFDABFFFF	Motherboard resources
0xFD000000-0xFDABFFFF	PCI Express Root Complex
0xFDAD0000-0xFDADFFFF	Motherboard resources
0xFDB00000-0xFDFFFFFF	Motherboard resources
0xFE000000-0xFE01FFFF	Motherboard resources
0xFE036000-0xFE03BFFF	Motherboard resources
0xFE03D000-0xFE3FFFFFF	Motherboard resources

MEMORY MAP	ASSIGNMENT
0xFE410000-0xFE7FFFFF	Motherboard resources
0xDF080000-0xDF083FFF	Intel(R) I210 Gigabit Network Connection
0xDF144000-0xDF147FFF	Intel(R) 100 Series/C230 Series Chipset Family PMC - A121
0x90000000-0xFFFFFFFF	PCI Express Root Complex
0xDF14D000-0xDF14DFFF	Intel(R) Active Management Technology - SOL (COM7)
0xFE40F000-0xFE40FFFF	Intel(R) Management Engine Interface
0xDF130000-0xDF13FFFF	Intel(R) USB 3.0 eXtensible Host Controller - 1.0 (Microsoft)
0xDF14A000-0xDF14A0FF	Intel(R) 100 Series/C230 Series Chipset Family SMBus - A123
0xDF14F000-0xDF14FFFF	Intel(R) 100 Series/C230 Series Chipset Family Thermal subsystem - A131
0xDF140000-0xDF143FFF	High Definition Audio Controller
0xDF120000-0xDF12FFFF	High Definition Audio Controller
0xA0000-0xBFFFF	Intel(R) HD Graphics 630
0xA0000-0xBFFFF	PCI Express Root Complex

DMA Map

MEMORY MAP	ASSIGNMENT
Channel 3	Printer Port (LPT1)

Configuring WatchDog Timer

The I/O port address of the watchdog timer is 2E (hex) and 2F (hex). 2E (hex) is the address port. 2F (hex) is the data port. User must first assign the address of register by writing address value into address port 2E (hex), then write/read data to/from the assigned register through data port 2F (hex).

Configuration Sequence

To program F81866 configuration registers, the following configuration sequence must be followed:

(1) Enter the extended function mode

To place the chip into the Extended Function Mode, two successive writes of 0x87 must be applied to Extended Function Enable Registers (EFERs, i.e. 2Eh or 4Eh).

(2) Configure the configuration registers

The chip selects the Logical Device and activates the desired Logical Devices through Extended Function Index Register (EFIR) and Extended Function Data Register (EFDR). The EFIR is located at the same address as the EFER, and the EFDR is located at address (EFIR+1). First, write the Logical Device Number (i.e. 0x07) to the EFIR and then write the number of the desired Logical Device to the EFDR. If accessing the Chip (Global) Control Registers, this step is not required. Secondly, write the address of the desired configuration register within the Logical Device to the EFIR and then write (or read) the desired configuration register through the EFDR.

(3) Exit the extended function mode

To exit the Extended Function Mode, writing 0xAA to the EFER is required. Once the chip exits the Extended Function Mode, it is in the normal running mode and is ready to enter the configuration mode.

Code example for watch dog timer

Enable watchdog timer and set timeout interval to 30 seconds.

```
; ----- Enter to extended function mode -----
mov      dx,  2eh
mov      al,  87h
out      dx,  al
out      dx,  al

; ----- Select Logical Device 7 of watchdog timer -----
mov      al,  07h
out      dx,  al
inc      dx
mov      al,  07h
out      dx,  al

;----- Enable Watch dog feature -----
mov      al,  030h
out      dx,  al
inc      dx
mov      al,  01h
out      dx,  al

;----- Set timeout interval as 30 seconds -----
dec      dx
mov      al,  0F6h
out      dx,  al
inc      dx
mov      al,  1Eh
out      dx,  al

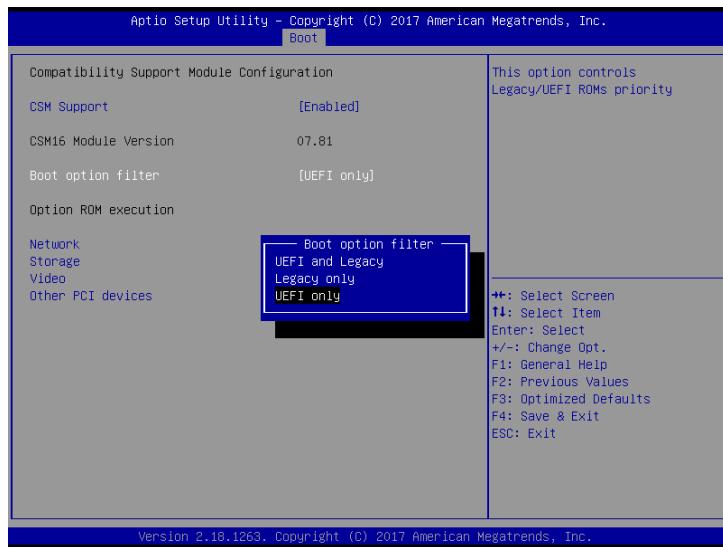
;----- Enable Watch PME-----
dec      dx
mov      al,  0FAh
out      dx,  al
inc      dx
in       al,  dx
```

```
or      al,  51h
out     dx,  al
;----- Set second as counting unit and start counting -----
dec    dx
mov    al,  0F5h
out    dx,  al
inc    dx
in     al,  dx
and    al,  0F7h
or     al,  20h
out    dx,  al
;----- Exit the extended function mode -----
dec    dx
mov    al,  0AAh
out    dx,  al
```

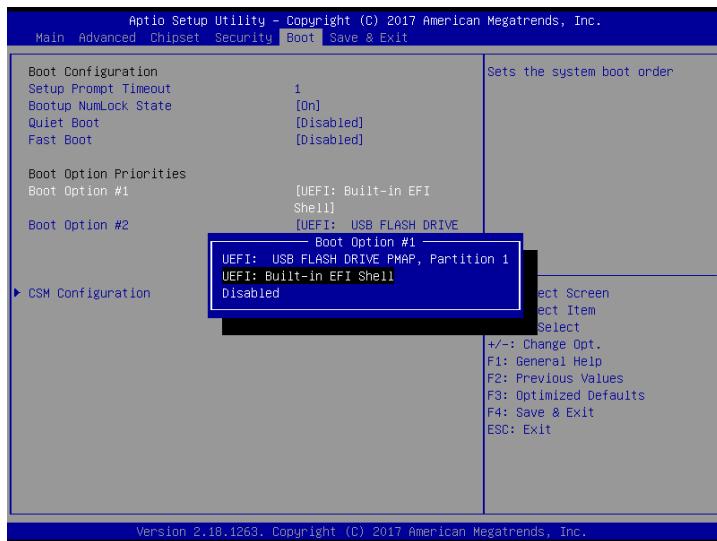
Flash BIOS Update

I. Prerequisites

- 1** Prepare a USB storage device which can save the required files for BIOS update.
- 2** Download and save the BIOS file (e.g. S2140PX1.bin) to the storage device.
- 3** Copy AMI flash utility – AFUEFIx64.exe (v5.09.01) into the storage device. The utility and BIOS file should be saved to the same path.
- 4** Make sure the target system can first boot to the EFI shell environment.
 - (1) Connect the USB storage device.
 - (2) Turn on the computer and press <ESC> or key during boot to enter BIOS Setup.
 - (3) Select [**Boot**] menu and enter into [**CSM Configuration**] menu.
 - (4) Set [**Boot option filter**] to [**UEFI Only**] and press <**F4**> key to save configuration and restart the system.



- (5) Press <ESC> or to enter into BIOS setup menu again.
- (6) Select [**Boot**] menu and set [**UEFI: Built-in EFI Shell**] to be the 1st boot device.
- (7) Press <**F4**> key to save configuration and restart the system to boot into EFI Shell environment.



II.AFUEFIx64 command for system BIOS update

AFUEFIx64.efi is the AMI firmware update utility; the command line is shown as below:

AFUEFIx64 <ROM File Name> [option1] [option2]....

Users can type “**AFUEFIx64 /?**” to see the definition of each control option. The recommended options for BIOS ROM update include the following parameters:

/P: Program main BIOS image.

/B: Program Boot Block.

/N: Program NVRAM.

/X: Don’t check ROM ID.

II. BIOS Update Procedure

- 1** Boot into EFI Shell, change to the path where you put BIOS image and AFUEFIx64.

```
Shell> fs0:  
fs0:\> cd afuefix64
```

- 2** Type " **AFUEFIx64 Sxxxx.bin /p /b /n /x**" and press Enter to start the flash procedure.

(Note that xxxx means the BIOS revision part, e.g. 0PM1...)

- 3** During the BIOS update procedure, you will see the BIOS update process status and its execution percentage. Beware! Do not turn off the system power or reset your computer when the entire update procedure are not complete; otherwise, the BIOS ROM may be crashed and the system will be unable to boot up next time.
- 4** After the BIOS update procedure is completed, the following messages will be shown:

```
fs0:\afuefix64> afuefix64 S2X40PQ1.bin /p /b /n /x
+-----+
---+
|       AMI Firmware Update Utility  v5.09.01.1317
|
| Copyright (C) 2016 American Megatrends Inc. All Rights Reserved.
+-----+
---+
Reading flash ..... done
- ME Data Size Checking - OK
- FFS checksums ..... OK.
Erasing Boot Block ..... done
Updating Boot Block ..... done
Verifying Boot Block ..... done
Erasing Main Block ..... done
Updating Main Block ..... done
Verifying Main Block ..... done
Erasing NVRAM Block ..... done
Updating NVRAM Block ..... done
Verifying NVRAM Block ..... done

fs0:\afuefix64
```

- 5 Restart the system and boot up with the new BIOS configurations.
- 6 The BIOS Update is completed after the system is restarted.
- 7 Reboot the system and verify if the BIOS version shown on the initialization screen has been updated.

