USER'S MANUAL

SP-6120/6122/6128

Intel® N2800/D2550
10.4"/12.1"/8.4" Fanless Panel PC
With DVI/Audio/2LAN

SP-6120/6122/6128 M2

SP-6120/6122/6128
Intel® N2800/D2550
10.4”/12.1”/8.4” Fanless Panel PC
With DVI/Audio/2LAN

COPYRIGHT NOTICE

DISCLAIMER

This user’s manual is meant to assist you in installing and setting up the system. The information contained in this document is subject to change without any notice.

CE NOTICE

This is a class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

FCC NOTICE

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

You are cautioned that any change or modifications to the equipment not expressly approve by the party responsible for compliance could void your authority to operate such equipment.

CAUTION! Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.
WARNING! Some internal parts of the system may have high electrical voltage. And therefore we strongly recommend that qualified engineers can open and disassemble the system. The LCD and touch screen are easily breakable, please handle them with extra care.

TABLE OF CONTENTS

CHAPTER 1 INTRODUCTION

CHAPTER 2 SYSTEM CONFIGURATION

CHAPTER 3 SOFTWARE UTILITIES

CHAPTER

1

INTRODUCTION

This chapter gives you the information for SP-6120/6122/6128. It also outlines the System specification.

Section includes:

- About This Manual
- System Specifications
- Safety Precautions

Experienced users can skip to chapter 2 on page 2-1 for Quick Start.

1-1. ABOUT THIS MANUAL

Thank you for purchasing our SP-6120/6122/6128 Intel® N2800/D2550 10.4"/ 12.1"/ 8.4" Fanless Panel PC with DVI/Audio/2LAN. SP-6120/6122/6128 provides faster processing speed, greater expandability and can handle more task than before. This manual is designed to assist you how to install and set up the system. It contains four chapters. The user can apply this manual for configuration according to the following chapters:

Chapter 1 Introduction

This chapter introduces you to the background of this manual, and the specifications for this system. The final page of this chapter will indicate how to avoid damaging this board.

Chapter 2 Hardware Configuration

This chapter outlines the component locations and their functions. In the end of this chapter, you will learn how to set jumper and how to configure this card to meet your own needs.

Chapter 3 Software Utilities

This chapter contains helpful information for proper installations of the VGA utility, LAN utility, and Sound utility.

Chapter 4 AMI BIOS Setup

This chapter indicates you how to set up the BIOS configurations.

Appendix A System Diagrams

This appendix gives you the exploded diagrams and part numbers of the SP-6120/ 6122/6128.

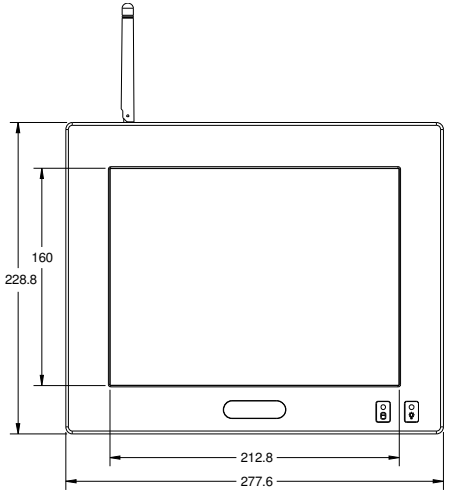
Appendix B Technical Summary

This appendix gives you the information about the Technical maps, Watchdog-timer configuration, and Flash BIOS Update.

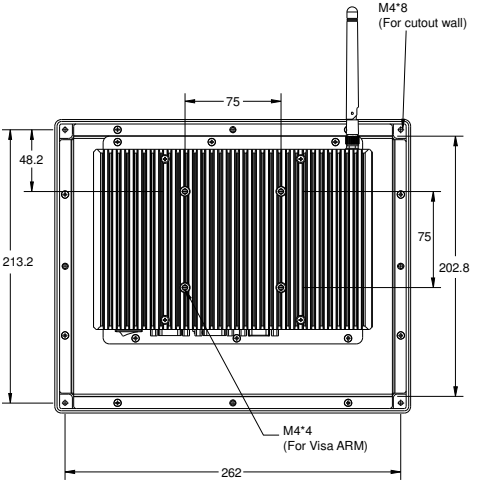
1-2. SYSTEM ILLUSTRATION

SP-6120 & SP-6120 CPT

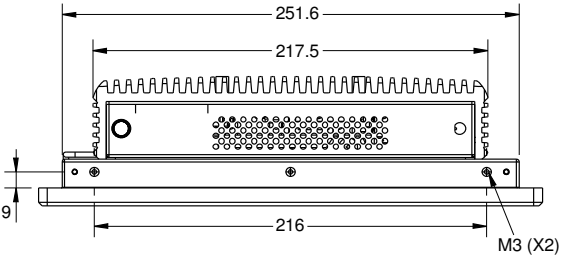
Front View



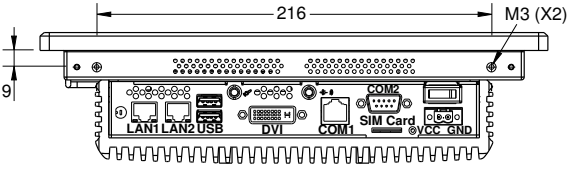
Rear View



Top View

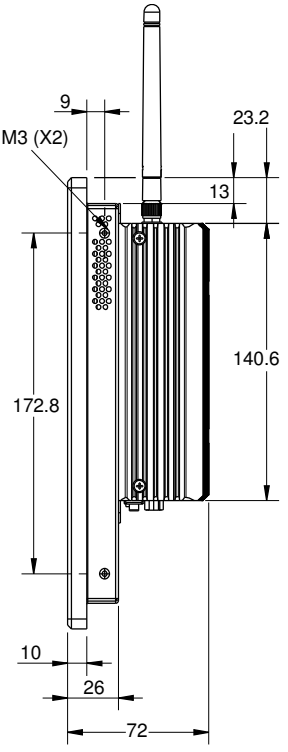


I/O View

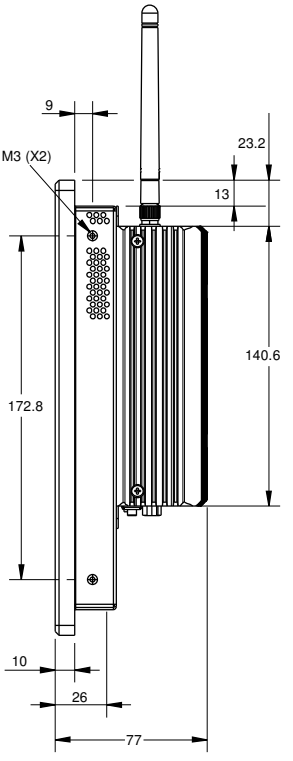


Unit: mm

Side View - SP-6120



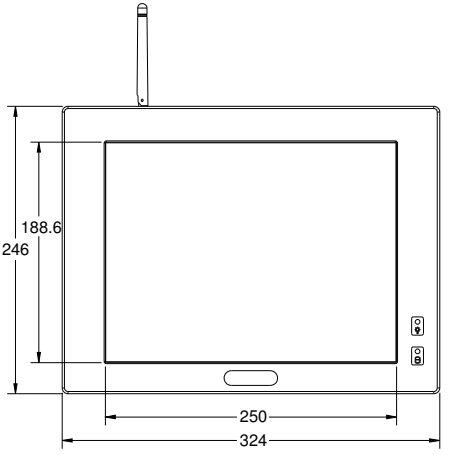
Side View - SP-6120 CPT



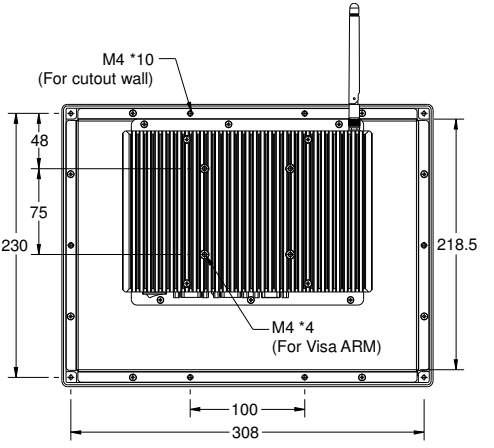
Unit: mm

SP-6122

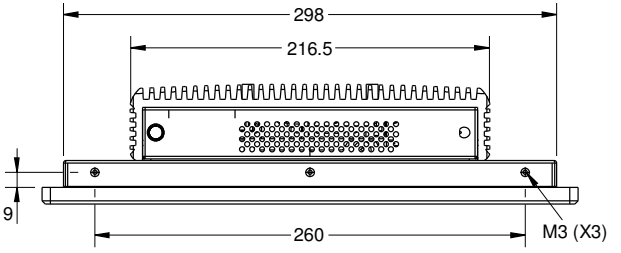
Front View



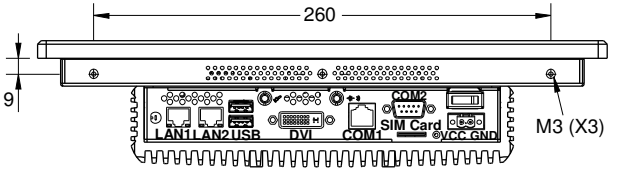
Rear View



Top View

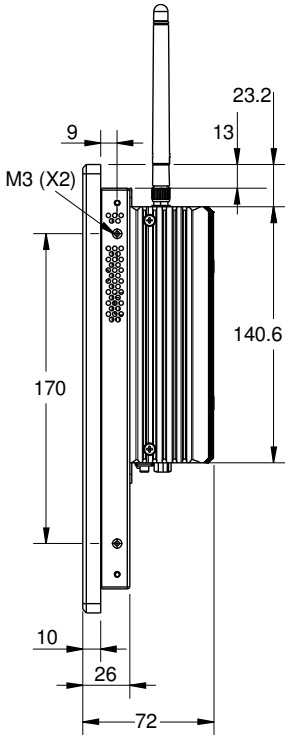


I/O View



Unit: mm

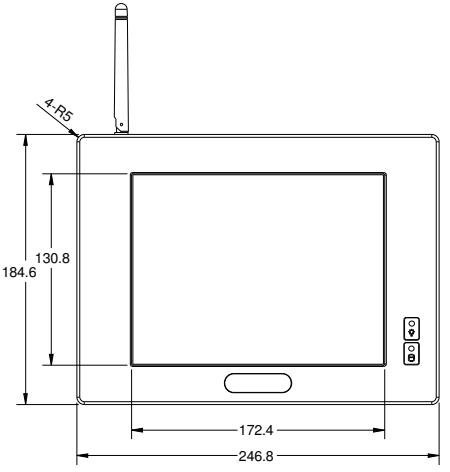
Side View



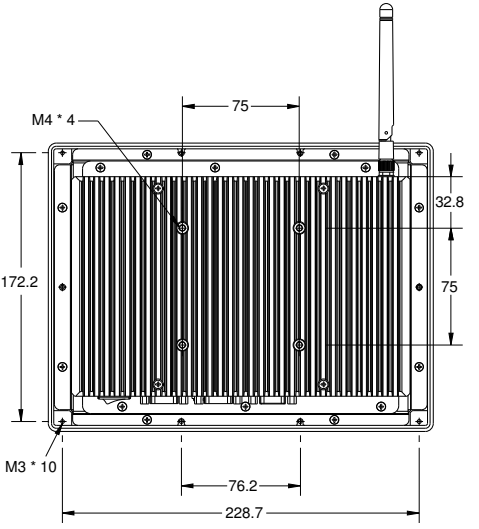
Unit: mm

SP-6128

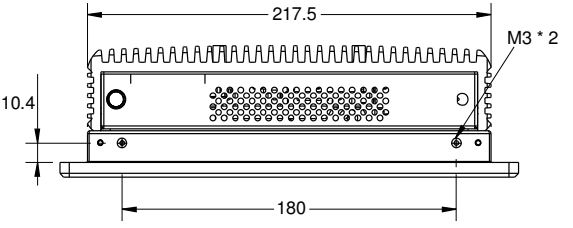
Front View



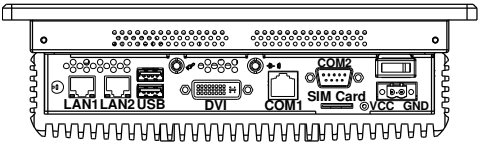
Rear View



Top View

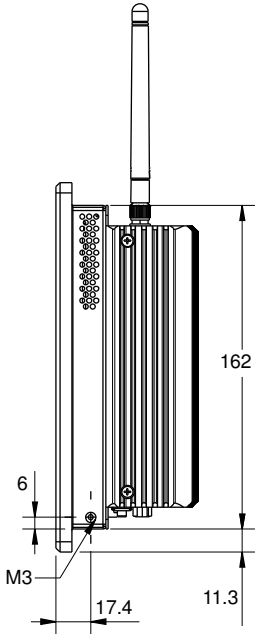


I/O View



Unit: mm

Side View



Unit: mm

1-3. SYSTEM SPECIFICATION

System

CPU Support	Intel® N2800/D2550 processor on board
Chipset	Intel® NM10
OS Support	Windows 7
Memory Support	1 x DDR3 SO-DIMM Socket (up to 4GB)
Drive Bay	1 x 2.5" SATA HDD
Power Requirement	DC-in 9~36V
Front Bezel	Aluminum
IP65	Front panel only
Wall Mount Type	VESA 75
Net Weight	▪ **SP-6120:** 3.5 kg ▪ **SP-6122**: 4.1 kg ▪ **SP-6128:** 2.9 kg
Dimension (W x H x D)	▪ **SP-6120:** 277.6 x 228.8 x 72 mm ▪ **SP-6122**: 324 x 246 x 72 mm ▪ **SP-6128:** 246.8 x 184.6 x 71 mm
Certificate	FCC/CE

I/O Ports

Serial Port	2 ports (5V/12V/RI selectable) COM2 is RS-232/422/485 selectable.
USB	2 x USB 2.0
DVI	1 x DVI
LAN	2 x LAN (10/100/1000 Mbps)
Audio	▪ 1 x Line-out ▪ 1 x MIC-in
Expansion slot	▪ 1 x Mini-PCIe slot ▪ 1 x CFast card slot

Display

LCD Panel Size	▪ **SP-6120:** 10.4” ▪ **SP-6122**: 12.1” ▪ **SP-6128:** 8.4”
Resolution (Brighness)	▪ **SP-6120:** SVGA or XGA ▪ **SP-6122**: XGA ▪ **SP-6128:** SVGA
Touch Panel Type	(ELO) 5W Analog resistive (USB interface)

Environment

Operation Temp.	HDD: 0 ~ 45°C (32 ~ 113°F) CFast card: 0 ~ 55°C (32 ~ 131°F)
Storage Temp.	-20 ~ 60°C (-4 ~ 140°F)
Humidity	20 ~ 90%

1-4. SAFETY PRECAUTIONS

Follow the messages below to avoid your systems from damage:

1. Keep your system away from static electricity on all occasions.

2. Prevent electric shock. Don't touch any components of this card when the card is power-on. Always disconnect power when the system is not in use.

3. Disconnect power when you change any hardware devices. For instance, when you connect a jumper or install any cards, a surge of power may damage the electronic components or the whole system.

CHAPTER

2

HARDWARE CONFIGURATION

*** QUICK START ***

Helpful information describes the jumper & connector settings, and component locations.

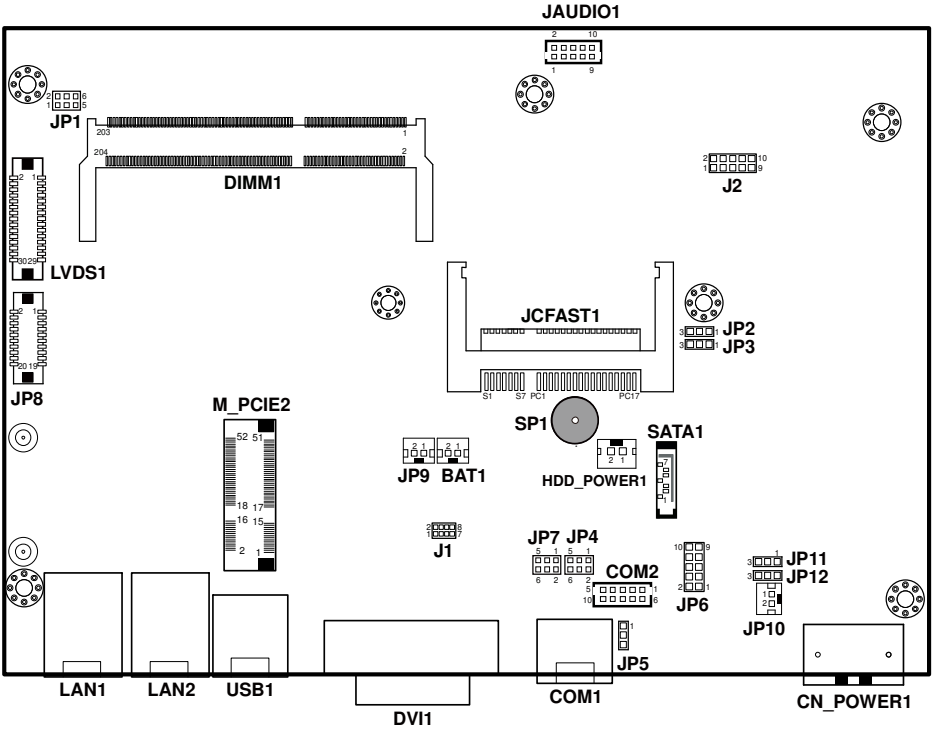
Section includes:

- Jumper & Connector Quick Reference Table
- Component Locations
- Configuration and Jumper settings
- Connector's Pin Assignments

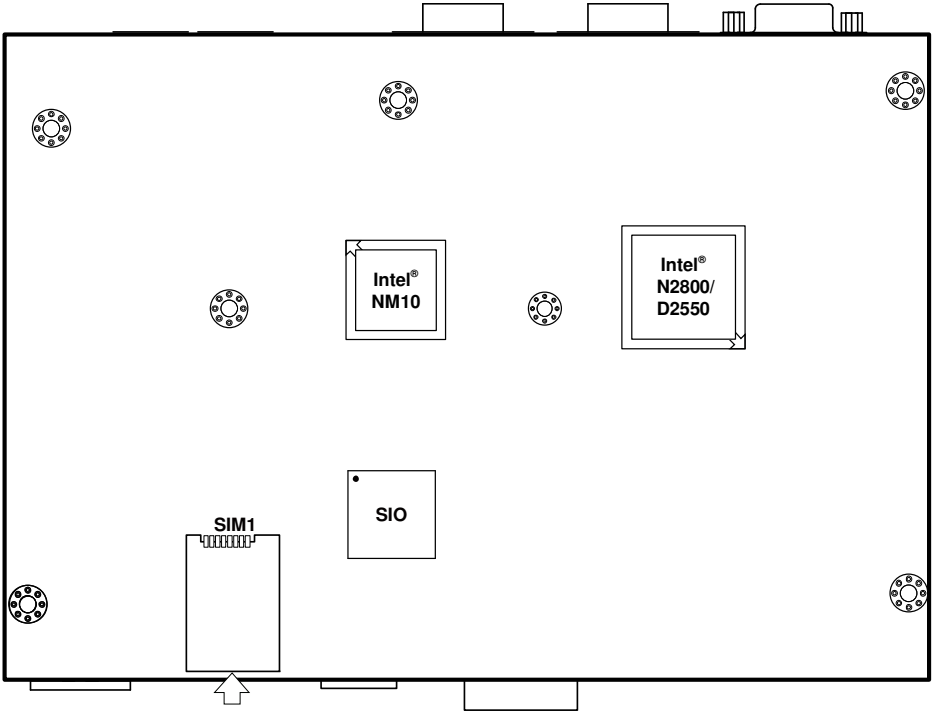
2-1. JUMPER & CONNECTOR QUICK REFERENCE TABLE

JUMPER / CONNECTOR	NAME
COM Port Connector	COM1, COM2
COM Port Configuration Connector	JP4, JP7
COM2 RS-232/422/485 Selection	JP6
COM2 Auto Detect Selection	JP5
Front Panel Connector	JP8
USB Port	USB1
LAN Port	LAN1, LAN2
DVI-I Port	DVI1
PWR IN Connector	CN_POWER1
SATA & SATA Power Connector	SATA1, HDD_POWER1
Audio Connector	JAUDIO1
Clear CMOS Data Selection	JP2
System Power Connector	JP11, JP12
LVDS Connector	LVDS1
LVDS Panel Power Connector	JP1
CFast Card Slot	JCFAST1
CFast Card Power Connector	JP3

2-2. COMPONENT LOCATIONS



SP-6120/6122/6128 Connectors, Jumpers and Components Locations - Front



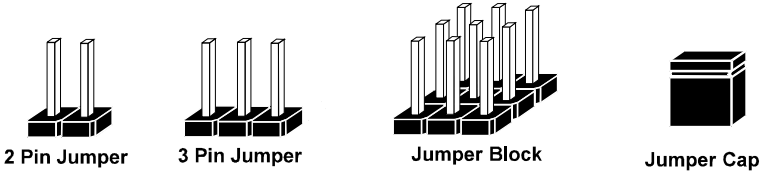
SP-6120/6122/6128 Connectors, Jumpers and Components Locations - Rear

2-3. HOW TO SET THE JUMPERS

You can configure your board by setting jumpers. Jumper is consists of two or three metal pins with a plastic base mounted on the card, and by using a small plastic "cap", Also known as the jumper cap (with a metal contact inside), you are able to connect the pins. So you can set-up your hardware configuration by "open" or "close" pins.

The jumper can be combined into sets that called jumper blocks. When the jumpers are all in the block, you have to put them together to set up the hardware configuration. The figure below shows how this looks like.

JUMPERS AND CAPS

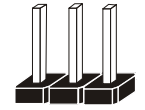


If a jumper has three pins (for examples, labelled PIN1, PIN2, and PIN3), You can connect PIN1 & PIN2 to create one setting by shorting. You can either connect PIN2 & PIN3 to create another setting. The same jumper diagrams are applied all through this manual. The figure below shows what the manual diagrams look and what they represent.

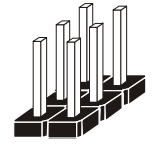
JUMPER DIAGRAMS

Jumper Cap
looks like this

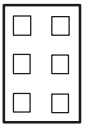
2 pin Jumper
looks like this



3 pin Jumper
looks like this



Jumper Block
looks like this

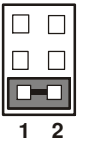


JUMPER SETTINGS

2 pin Jumper close(enabled)
Looks like this

3 pin Jumper
2-3 pin close(enabled)
Looks like this

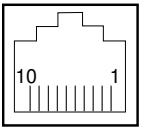
Jumper Block
1-2 pin close(enabled)
Looks like this



2-4. COM PORT & CONNECTOR

COM1: COM1 Connectors
The pin assignments are as follows:

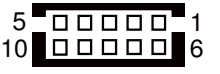
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DCD1	6	DSR1
2	RXD1	7	RTS1
3	TXD1	8	CTS1
4	DTR1	9	RI selectable
5	GND	10	NC



COM1

COM2: COM2 Connector
The pin assignments are as follows:

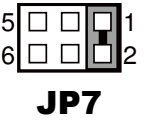
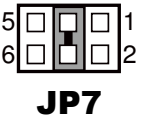
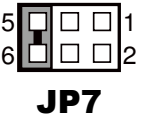
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DCD2	6	DSR2
2	RXD2	7	RTS2
3	TXD2	8	CTS2
	DTR2	9	RI selectable
5	GND	10	NC



COM2

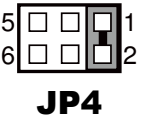
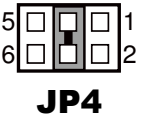
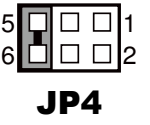
2-5. COM PORT CONFIGURATION CONNECTOR

JP7: COM1 Port Configuration Connector
The selections are as follows:

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
RI	1-2	
12V	3-4	
5V	5-6	

Note: Manufacturing Default is RI.

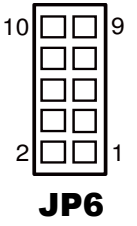
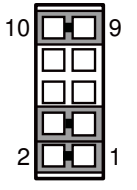
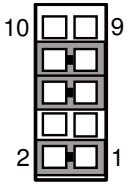
JP4: COM2 Port Configuration Connector
The selections are as follows:

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
RI	1-2	
12V	3-4	
5V	5-6	

Note: Manufacturing Default is RI.

2-6. RS-232/422/485 (COM2) SELECTION

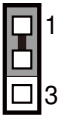
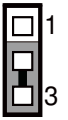
JP6: RS-232/422/485 (COM2) Selection
The selections are as follows:

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
RS-232	All open	 **JP6**
RS-422	1-2, 3-4, 9-10	 **JP6**
RS-485	1-2, 5-6, 7-8	 **JP6**

Note: Manufacturing Default is RS-232.

2-7. COM2 AUTO DETECT SELECTION

JP5: COM2 Auto Detect Selection
The selections are as follows:

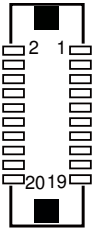
SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
Normal	1-2	 **JP5**
Auto	2-3	 **JP5**

Note: Manufacturing Default is Auto.

2-8. FRONT PANEL CONNECTOR

JP8: Front Panel Connector
The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	VCC5_SB	11	NC
2	VCC5_SB	12	VCC12
3	USB7-	13	NC
4	VCC12	14	HDD_LED
5	USB7+	15	GND
6	VCC12	16	POWER_LED+
7	GND	17	GND
8	VCC12	18	NC
9	PANEL_BKLTEN	19	VCC5
10	VCC12	20	USB7_OCJ



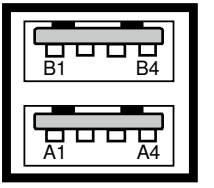
JP8

2-9. USB PORT

USB1: USB Port
The pin assignments are as follows:

USB1:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
A1	VCC5	B1	VCC5
A2	USB0-	B2	USB1-
A3	USB0+	B3	USB1+
A4	GND	B4	GND

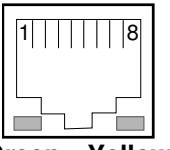


USB1

2-10. LAN PORT

LAN1, LAN2: LAN Connectors
The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	MDI_0P	5	MDI_2P
2	MDI_0N	6	MDI_2N
3	MDI_1P	7	MDI_3P
4	MDI_1N	8	MDI_3N



Green Yellow

LAN1/ LAN2

LAN LED Indicator:

Left Side LED

Green Color Blinking	LAN Message Active
Off	No LAN Message Active

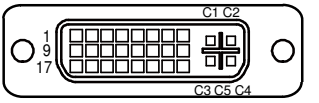
Right Side LED

Yellow Color On	10/100 LAN Speed Indicator
Orange Color on	Giga LAN Speed Indicator
Off	No LAN switch/ hub connected.

2-11. DVI PORT

DVI1: DVI Connector
The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	HDMI_2-	16	HDMI_HPD
2	HDMI_2+	17	HDMI_0-
3	GND	18	HDMI_0+
4	NC	19	GND
5	NC	20	NC
6	HDMI_clock	21	NC
7	HDMI_data	22	GND
8	CRT_VSYNC	23	HDMI_Clock+
9	HDMI_1-	24	HDMI_Clock-
10	HDMI_1+	C1	CRT_RED
11	GND	C2	CRT_GREEN
12	NC	C3	CRT_BLUE
13	NC	C4	CRT_HSYNC
14	VCC	C5	GND
15	GND		

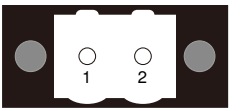


DVI1

2-12. PWR-IN CONNECTOR

CN_POWER1: PWR-in Connector
The pin assignments are as follows:

PIN	ASSIGNMENT
1	PWR-I
2	GND



CN_POWER1

2-13. SATA & SATA POWER CONNECTOR

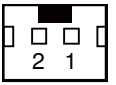
SATA1: SATA Connector
The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	G1	5	RX-
2	TX+	6	RX+
3	TX-	7	G3
4	G2		

SATA1

HDD_POWER1: SATA Power Connector
The pin assignments are as follows:

PIN	ASSIGNMENT
1	VCC
2	GND

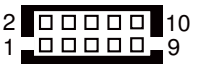


HDD_POWER1

2-14. AUDIO CONNECTOR

JAUDIO1: Audio Connectors
The pin assignments are as follows:

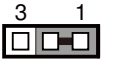
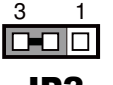
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	MIC1L	6	LINEINR
2	MIC1R	7	GND
3	GND	8	GND
4	GND	9	LINEOUTL
5	LINEINL	10	LINEOUTR



JAUDIO1

2-15. CLEAR CMOS DATA SELECTION

JP2 : Clear CMOS Data Selection
The jumper setting is as follows:

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
Normal	1-2	 **JP2**
Clear CMOS*	2-3	 **JP2**

Note: Manufacturing Default is Normal.

*To clear CMOS data, user must power-off the computer and set the jumper to "Clear CMOS" as illustrated above. After five to six seconds, set the jumper back to "Normal" and power-on the computer.

2-16. SYSTEM POWER SELECTION

JP11, JP12 : System Power Selection
The jumper setting is as follows:

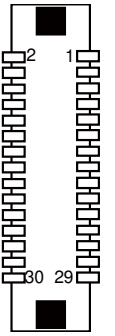
SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION	
Normal	1-2	 **JP11**	 **JP12**

Note: Manufacturing Default is Normal.

2-17. LVDS CONNECTOR

LVDS1: LVDS Connector
The pin assignments are as follows:

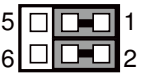
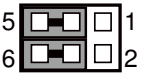
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	LVDS_VCC	16	LVDS0_CLK+(Odd)
2	GND	17	LVDS0_CLK-(Odd)
3	NC	18	GND
4	NC	19	LVDS0_D2+(Odd)
5	GND	20	LVDS0_D2-(Odd)
6	NC	21	GND
7	NC	22	LVDS0_D1+(Odd)
8	GND	23	LVDS0_D1-(Odd)
9	NC	24	GND
10	NC	25	LVDS0_D0+(Odd)
11	NC	26	LVDS0_D0-(Odd)
12	NC	27	LVDS0_D3+(Odd)
13	NC	28	LVDS0_D3-(Odd)
14	NC	29	LVDS_VCC
15	GND	30	LVDS_VCC



LVDS1

2-18. LVDS PANEL POWER CONNECTOR

JP1 : LVDS Panel Power Connector
The jumper setting is as follows:

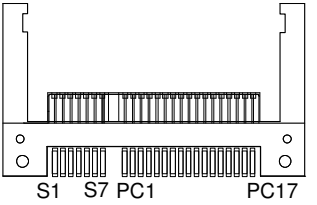
SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
3.3V	1-3, 2-4	 JP1
5V	3-5, 4-6	 JP1

Note: Manufacturing default is 3.3V.

2-19. CFAST CARD SLOT

JCFAST1: CFAST Card Slot
The pin assignments are as follows:

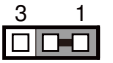
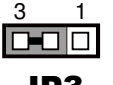
PIN	ASSIGNMENT	PIN	ASSIGNMENT
S1	GND	PC6	NC
S2	SATA_TXP0	PC7	GND
S3	SATA_TXN0	PC8	NC
S4	GND	PC9	NC
S5	SATA_RXN0	PC10	NC
S6	SATA_RXP0	PC11	NC
S7	GND	PC12	NC
PC1	NC	PC13	3.3V/5V
PC2	GND	PC14	3.3V/5V
PC3	NC	PC15	GND
PC4	NC	PC16	GND
PC5	NC	PC17	NC



JCFAST1

2-20. CFAST CARD POWER CONNECTOR

JP3 : CFAST Card Power Connector
The jumper setting is as follows:

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
3.3V	1-2	 **JP3**
5V	2-3	 **JP3**

Note: Manufacturing default is 3.3V.

SOFTWARE UTILITIES

CHAPTER

3

This chapter comprises the detailed information of VGA driver, LAN driver, and Sound driver.

Section includes:

- Introduction
- Intel® Chipset Software Installation Utility
- VGA Driver Utility
- LAN Driver Utility
- Sound Driver Utility
- Touchscreen Driver Utility

3-1. INTRODUCTION

Enclosed with our SP-6120/6122/6128 package, you will find a CD ROM disk containing all types of drivers we have. As a SP-6120/6122/6128 user, you will only need some of files contained in the CD ROM disk, please take note of the following chart:

FILE NAME (Assume that CD ROM drive is D:)	**PURPOSE**
D:\Driver\UTILITY	Intel® Chipset Software Installation Utility
D:\Driver\VGA	Intel® Graphics Media Accelerator 3600 Series for VGA driver installation
D:\Driver\ LAN	Realtek RTL8111F for LAN driver installation
D:\Driver\Sound	Realtek ALC888S High Definition Audio for sound driver installation
D:\Driver\Touch	eGalax USB Touchscreen Controller for Windows installation
D:\Driver\Flash	AMI BIOS Update Utility

Note: Be sure to install the Utility right after the OS is fully installed.

3-2. INTEL® CHIPSET SOFTWARE INSTALLATION UTILITY

3-2-1. Introduction

The Intel® Chipset Device Software installs Windows *.INF files to the target system. These files outline to the operating system how to configure the Intel® chipset components in order to ensure that the following features function properly:

- PCIe Support
- SATA Storage Support
- USB Support
- Identification of Intel® Chipset Components in the Device Manager

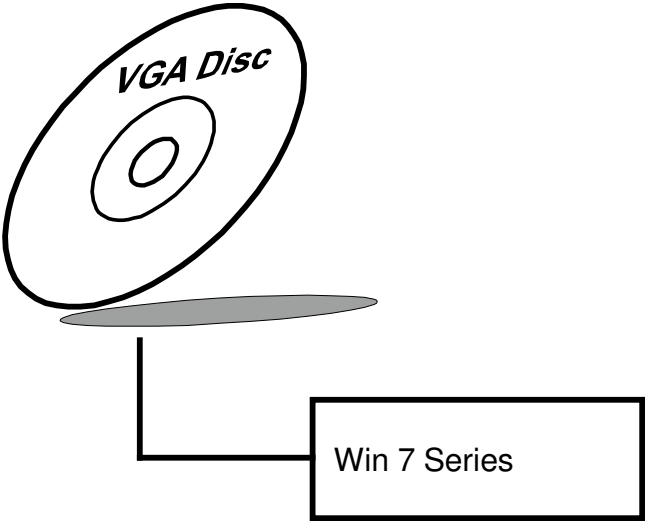
3-2-2. Installation of Utility for Windows 7

The Utility Pack is made only for Windows 7. It should be installed right after the OS installation; kindly follow the following steps:

1. Place insert the Utility Disk into Floppy Disk Drive A/B or CD ROM drive.
2. Under Windows system, go to the directory where Utility Disc is located.
   e.g.: D:\Driver\Platform\(OS)\Utility\infinst_autol.exe
3. Click infinst_autol.exe file for utility installation.
4. Follow the instructions on the screen to complete the installation.
5. Once installation is completed, shut down the system and restart in order for the changes to take effect.

3-3. VGA DRIVER UTILITY

The VGA interface is embedded with our SP-6120/6122/6128 system to support CRT display. The following illustration briefly shows you the content of VGA driver.



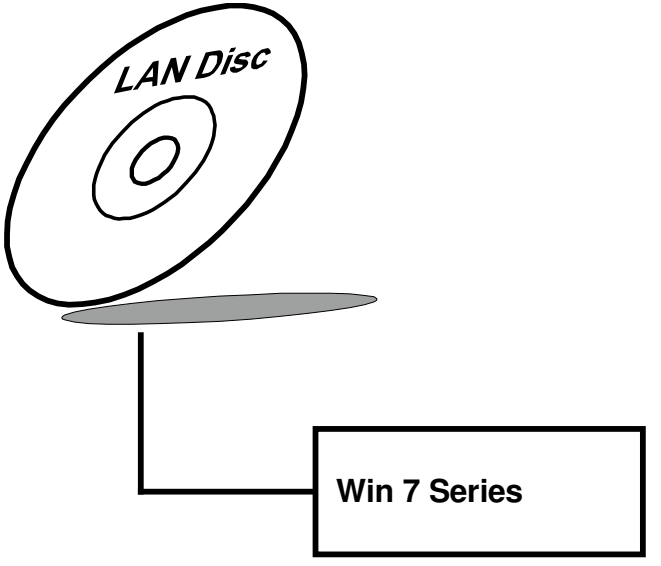
3-3-1. Installation of VGA Driver for Windows 7

1. Start the computer.
2. Insert the Utility Disk into the CD ROM drive or drive A/B.
3. Open the VGA folder for your system to choose an appropriate folder, and double-click "*.exe" file to install.
   e.g. D:\Driver\Platform\(OS)\Graphics\Your system\ ***.exe
   (If D is not your CD-ROM drive, substitute D with the correct drive letter.)
4. Follow the Wizard's on-screen instructions to complete the installation.

3-4. LAN DRIVER UTILITY

3-4-1. Introduction

The SP-6120/6122/6128 is enhanced with LAN function that can support various network adapters. The content of the LAN driver is found as follows:

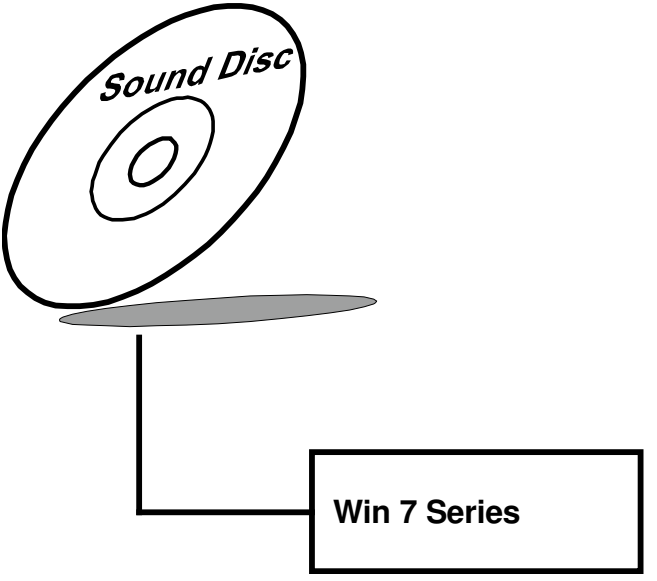


For more details on Installation procedure, please refer to Readme.txt file found on LAN DRIVER UTILITY.

3-5. SOUND DRIVER UTILITY

3-5-1. Introduction

The Audio chip enhanced in this system is fully compatible with Windows 7. Below, you will find the content of the Sound driver:

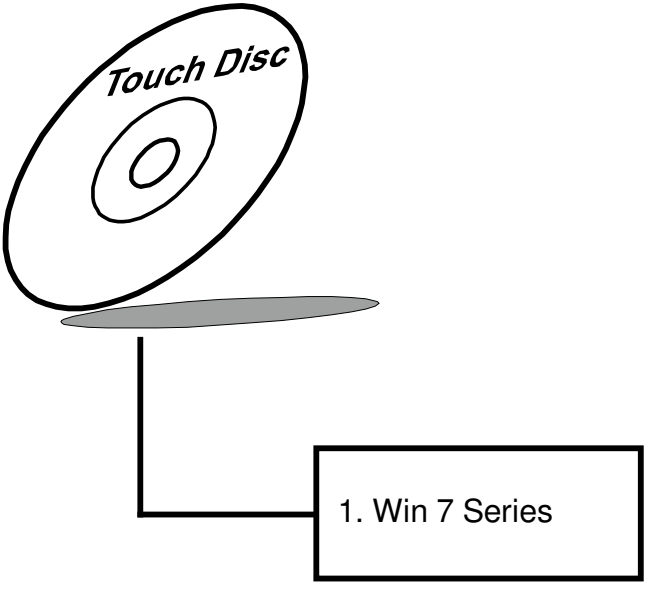


3-5-2. Installation Procedure for Windows 7

1. Open the “Sound” folder. For your system to choose an appropriate folder, and Run the setup.exe program to start the installation.
   e.g.: D:\Driver\Platform\(OS)\ SOUND\Your system\setup.exe
   (If D is not your CD-ROM drive, substitute D with the correct drive letter.)
2. Click on [Next] to continue the procedure. If the Windows popup "Windows can't verify the publisher of this driver software" message, press "Install this driver software anyway" to continue the installation.
3. Finally, select to restart the system and press [Finish] to complete the installation.

3-6. TOUCHSCREEN DRIVER UTILITY

The touch screen driver utility can only be installed on Windows 7, and it should be installed right after the OS installation.



3-6-1. Installation of Touchscreen Driver

To install the touchscreen driver, follow the steps below:

1. Open the "Device/Touchscreen" folder where the touchscreen driver is located.
2. Click **Setup.exe** file for driver installation.
3. Follow the on-screen instructions to complete the installation.
4. Once installation is completed, shut down the system and restart for the changes to take effect.

AMI BIOS SETUP

CHAPTER

4

This chapter shows how to set up the AMI BIOS.

Section includes:

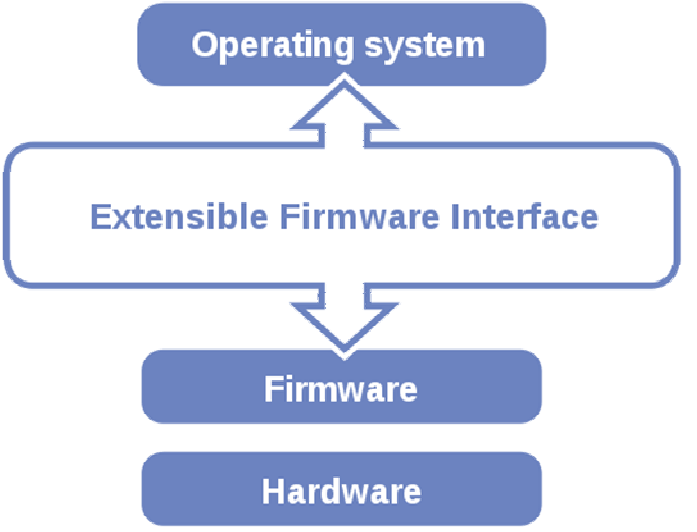
- Introduction
- Entering Setup
- Main
- Advanced
- Chipset
- Boot
- Security
- Save & Exit

4-1. INTRODUCTION

The system SP-6120/6122/6128 uses an AMI Aptio BIOS that is stored in the Serial Peripheral Interface Flash Memory (SPI Flash) and can be updated. The SPI Flash contains the BIOS setup menu, Power-on Self-test (POST), the PCI auto-configuration utility, LAN EEPROM information, and Plug and Play support.

Aptio is AMI's BIOS firmware based on the UEFI (Unified Extensible Firmware Interface) specifications and the Intel Platform Innovation Framework for EFI. The UEFI specification defines an interface between an operating system and platform firmware. The interface consists of data tables that contain platform-related information, boot service calls, and runtime service calls that are available to the operating system and its loader. These provide standard environment for booting an operating system and running pre-boot applications.

Following illustration shows Extensible Firmware Interface's position in the software stack.



EFI BIOS provides an user interface allow users the ability to modify hardware configuration, e.g. change system date and time, enable or disable a system component, decide bootable device priorities, setup personal password, etc., which is convenient for modifications and customization of the computer system and allows technicians another method for finding solutions if hardware has any problems.

The BIOS setup menu can be used to view and change the BIOS settings for the computer. The BIOS setup menu is accessible by pressing the <Del> or <ESC> key on keyboard during the POST stage, right before the operating system is loading. All the settings are described in chapter to be followed.

4-2. ENTERING SETUP

When the system is powered on, the BIOS will enter the Power-on Self-test (POST) routines and the following message will appear on the lower screen:

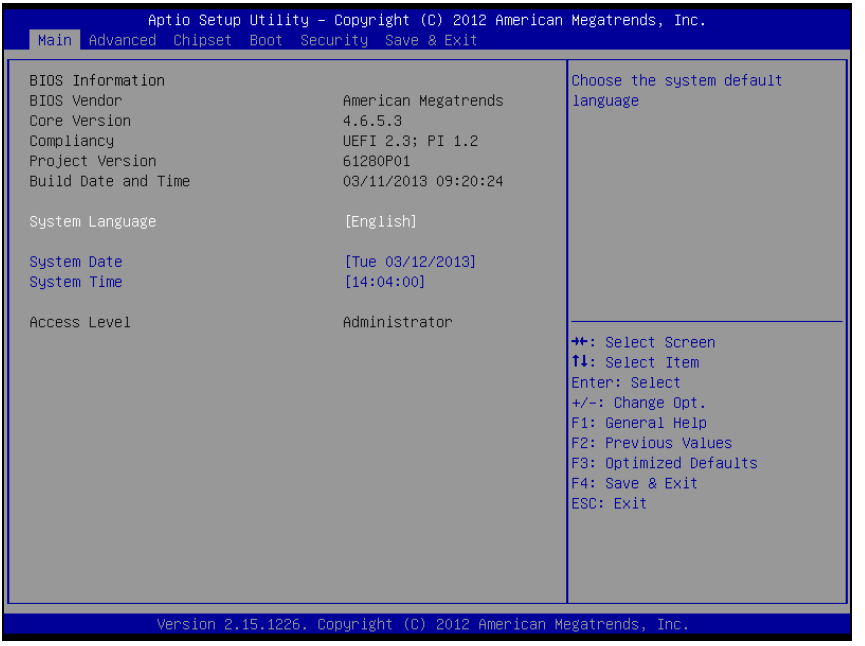
POST screen

As long as this message is present on the screen you may press the <ESC> or <Del> key (the one that shares the decimal point at the bottom of the number keypad) to access the setup menu. In a moment, the main menu of the Aptio Setup Utility will appear on the screen:

BIOS setup program initial screen

You may move the cursor by up/down keys to highlight the individual menu items. As you highlight each item, a brief description of the highlighted selection will appear at the bottom of the screen.

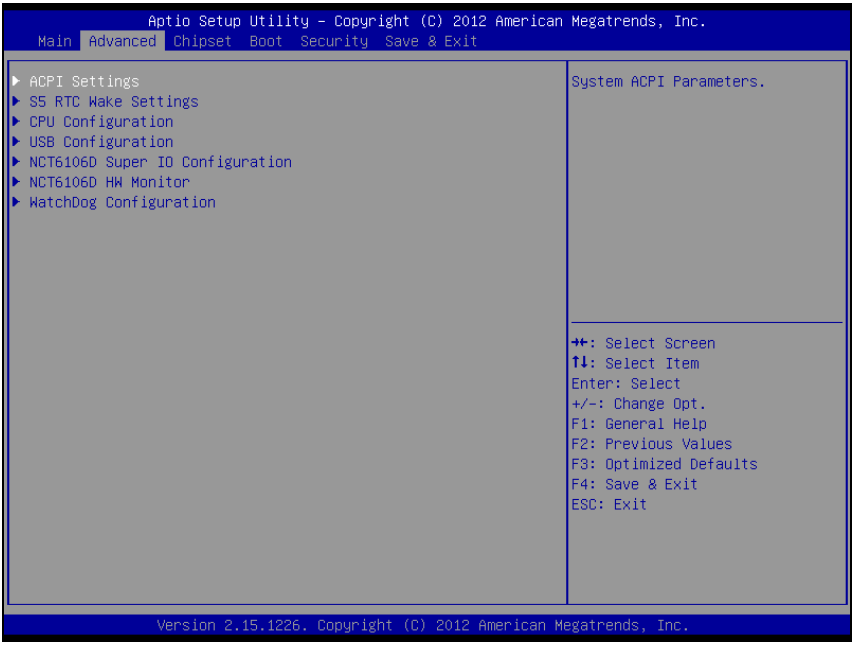
4-3. MAIN



Main Screen

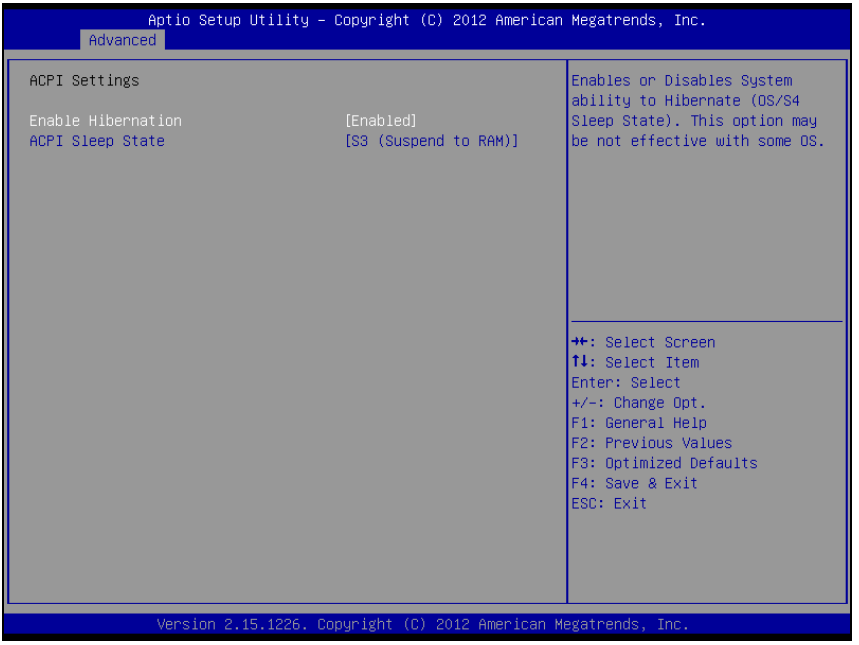
BIOS Setting	Options	Description/Purpose
BIOS Vendor	No changeable options	Displays the BIOS vendor.
Core Version	No changeable options	Displays the current BIOS core version.
Project Version	No changeable options	Displays the version of the BIOS currently installed on the platform.
Build Date and Time	No changeable options	Displays the date of current BIOS version.
System Date	Month, day, year	Specifies the current date.
System Time	Hour, minute, second	Specifies the current time.

4-4. ADVANCED



Advanced screen

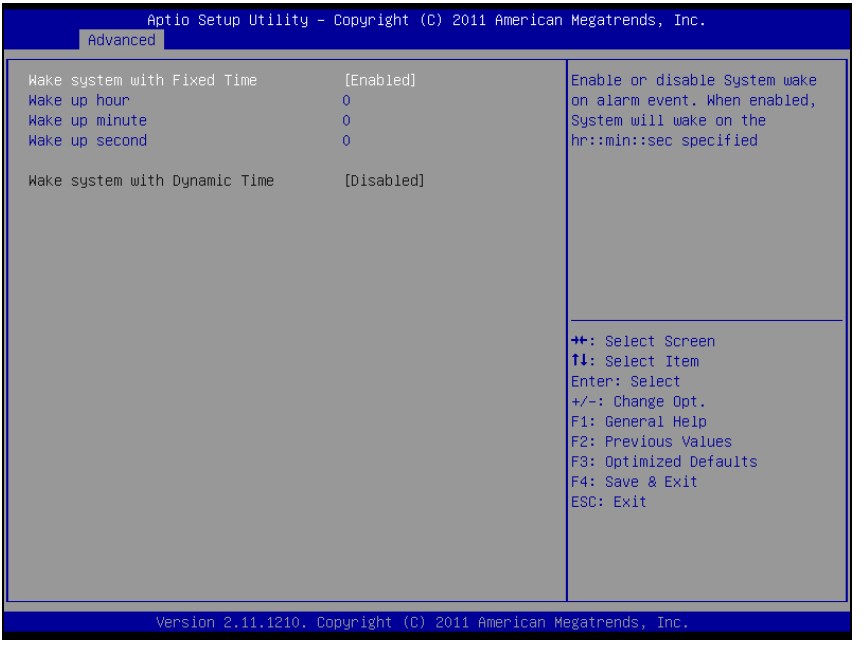
4-4-1. Advanced – ACPI Settings



ACPI Settings screen

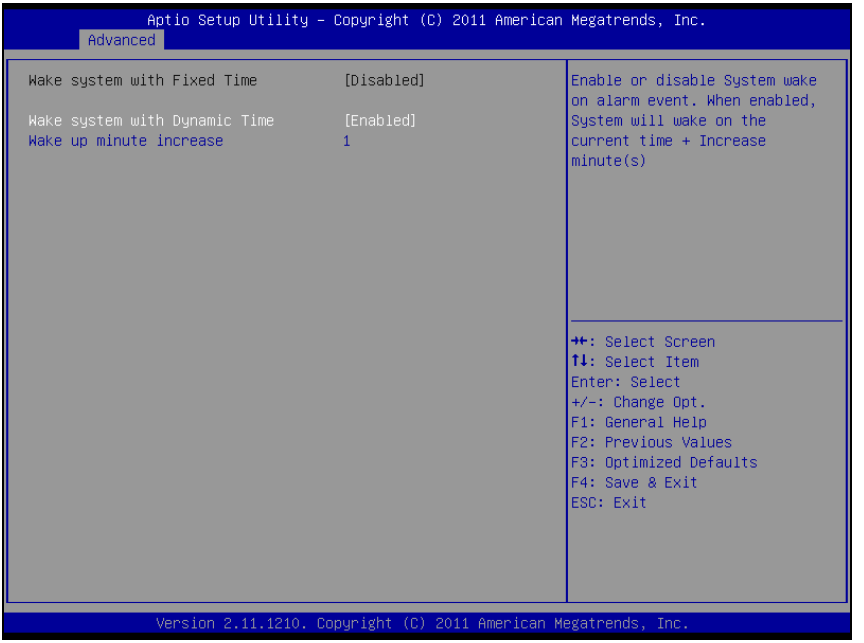
BIOS Setting	Options	Description/Purpose
Enable Hibernation	-Disabled -Enabled	Enables or disables system ability to hibernate (OS/S4 sleep state).
ACPI Sleep state	-Suspend Disabled -S1 (CPU Stop Clock) -S3(Suspend to RAM)	Select the highest ACPI sleep state the system will enter when the suspend button is pressed.

4-4-2. Advanced – S5 RTC Wake Setting



S5 RTC Wake Setting screen

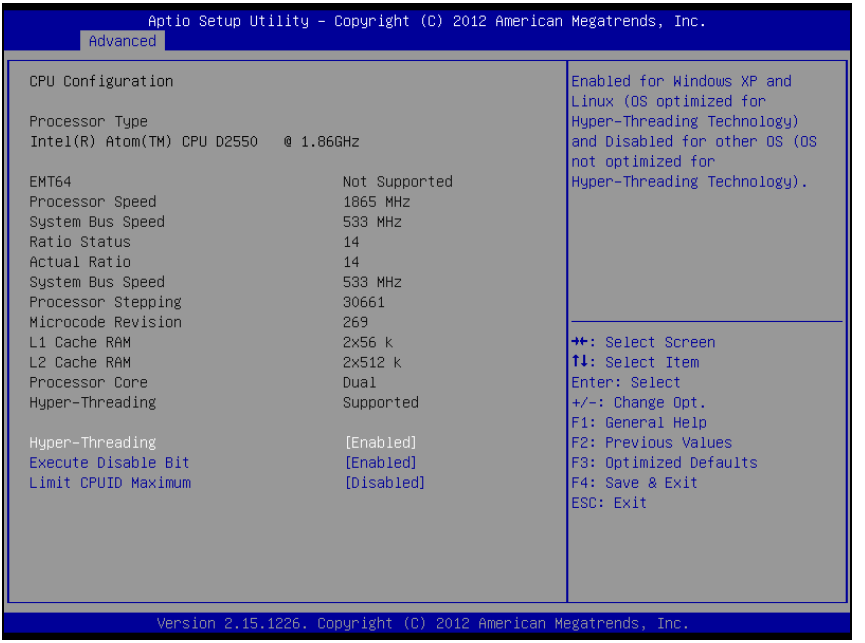
BIOS Setting	Options	Description/Purpose
Wake up with fixed time	-Disabled -Enabled	Enable wake up feature with fixed time.
Wake up hour	Multiple options ranging from 0 to 23	Sets the hour for wake up.
Wake up minute	Multiple options ranging from 0 to 59	Sets the minute for wake up.
Wake up second	Multiple options ranging from 0 to 59	Sets the second for wake up.



S5 RTC Wake Setting screen

BIOS Setting	Options	Description/Purpose
Wake system with dynamic time	-Disabled -Enabled	Enable wake up feature with dynamic time.
Wake up minute increase	Multiple options ranging from 1 to 5	Sets the minute for wake up.

4-4-3. Advanced - CPU Configuration

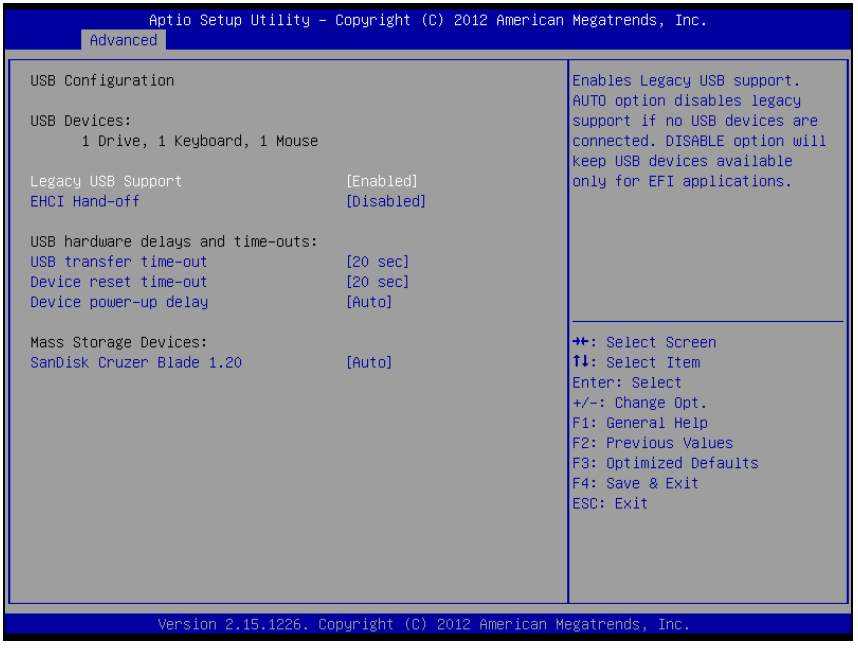


CPU Configuration screen

BIOS Setting	Options	Description/Purpose
Processor Type	No changeable options	Displays the processor brand string obtained from the CPUID instruction.
Processor Speed	No changeable options	Displays the maximum processor speed at current settings.
System Bus Speed	No changeable options	Displays the System bus frequency.
Actual Ratio	No changeable options	This is a read-only item, which displays the ratio actual value of this motherboard.
Processor Stepping	No changeable options	Displays the processor family, mode and stepping.
Macrocode Revision	No changeable options	Displays processor's microcode update revision.

BIOS Setting	Options	Description/Purpose
L1 cache RAM	No changeable options	Displays amount of Level 1 cache.
L2 cache RAM	No changeable options	Displays amount of Level 2 cache.
Processor Cores	No changeable options	Displays information about number of physical cores in processor.
Hyper-threading	No changeable options	Reports if Intel Hyper-Threading Technology is supported by processor.
Hyper-threading	-Disabled -Enabled	It is enabled for Windows and Linux (OS optimized for Hyper Threading Technology) and disabled for other OS (OS not optimized for Hyper Threading Technology).
Execute Disable Bit	-Disabled -Enabled	Enable to implement Execute Disable Technology.
Limit CPUID Maximum	-Disabled -Enabled	This feature allows the user to set the maximum CPU ID value. Enable this function to boot the legacy operating systems that cannot support processors with extended CPUID functions. The options are Enabled and Disabled (for the Windows OS.).

4-4-4. USB Configuration

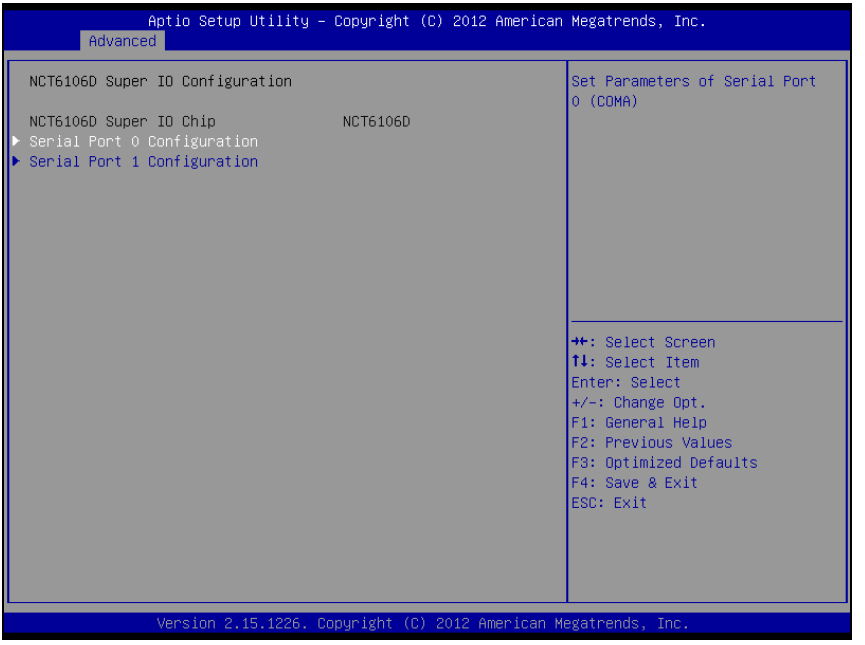


USB Configuration screen

BIOS Setting	Options	Description/Purpose
USB Devices	No changeable options	Displays number of available USB devices.
Legacy USB Support	-Disabled -Enabled -Auto	Enables support for legacy USB.
EHCI Hand-off	-Disabled -Enabled	When enabled it allows BIOS support control of the EHCI controller and the OS handoff synchronization capability.
USB transfer time-out	-1 sec -5 sec -10 sec -20 sec	The time-out value for Control, Bulk, and Interrupt transfers.

BIOS Setting	Options	Description/Purpose
Device reset time-out	-10 sec -20 sec -30 sec -40 sec	USB mass storage device Start Unit command time-out.
Device power-up delay	-Auto -Manual	Device power-up delay Maximum time the device will take before it properly reports itself to the Host Controller.

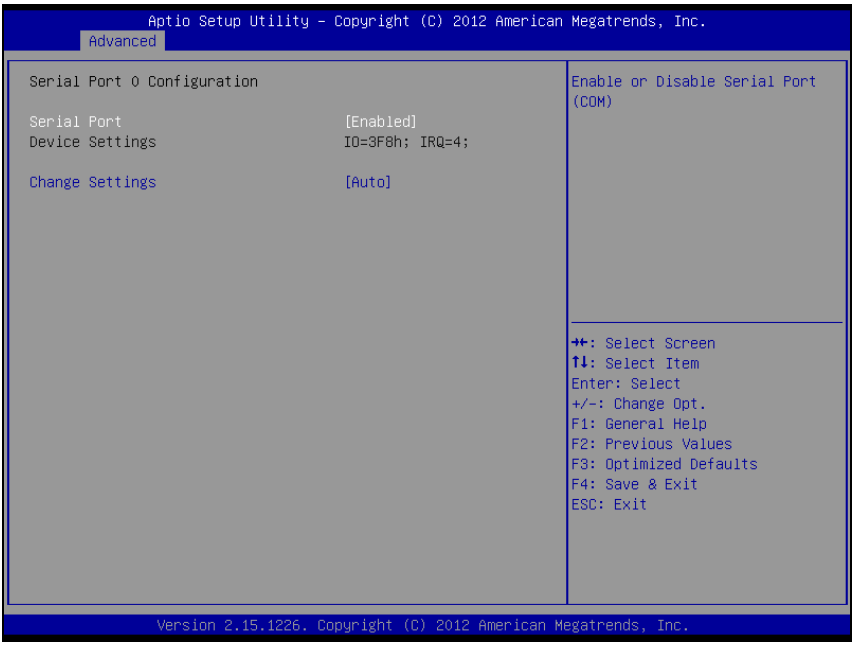
4-4-5. Advanced – NCT6106D Super IO Configuration



NCT6106D Super IO Configuration screen

BIOS Setting	Options	Description/Purpose
Super IO Chip	No changeable options	Displays the super IO chip model and its manufacturer.

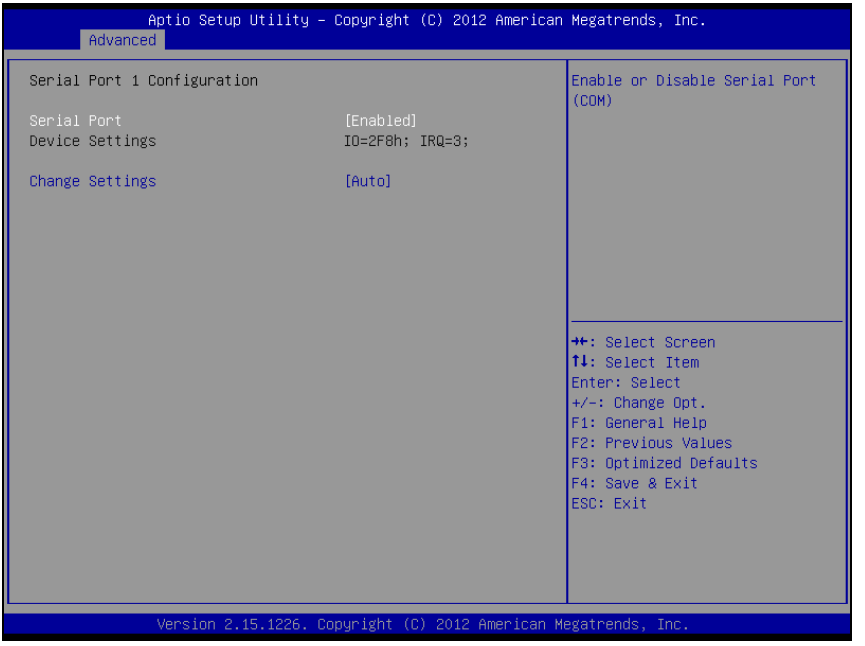
4-4-5-1. NCT6106D Super IO Configuration – Serial Port 0 Configuration



Serial Port 0 Configuration screen

BIOS Setting	Options	Description/Purpose
Serial Port	-Disabled -Enabled	Configures the serial port 0.
Device Settings	No changeable options	Reports the current serial port 0 setting.
Change Settings	-Auto -IO=3F8h; IRQ=4 -IO=3F8h; IRQ=3,4,5,6,7,10,11,12 -IO=2F8h; IRQ=3,4,5,6,7,10,11,12 -IO=3E8h; IRQ=3,4,5,6,7,10,11,12 -IO=2E8h; IRQ=3,4,5,6,7,10,11,12	Specifies the base I/O address and interrupt request for the serial port 0 if enabled.

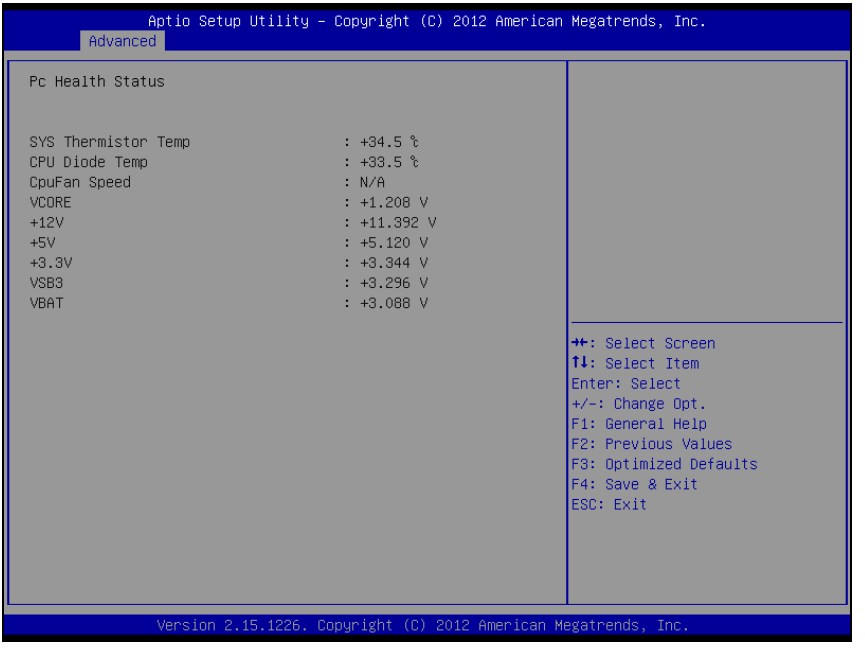
4-4-5-2 NCT6106D Super IO Configuration – Serial Port 1Configuration



Serial Port 1 Configuration screen

BIOS Setting	Options	Description/Purpose
Serial Port	-Disabled -Enabled	Configures the serial port 1.
Device Settings	No changeable options	Reports the current serial port 1 setting.
Change Settings	-Auto -IO=2F8h; IRQ=3 -IO=3F8h; IRQ=3,4,5,6,7,10,11,12 -IO=2F8h; IRQ=3,4,5,6,7,10,11,12 -IO=3E8h; IRQ=3,4,5,6,7,10,11,12 -IO=2E8h; IRQ=3,4,5,6,7,10,11,12	Specifies the base I/O address and interrupt request for the serial port 1 if enabled.

4-4-6. Advanced – NCT6106D HW Monitor

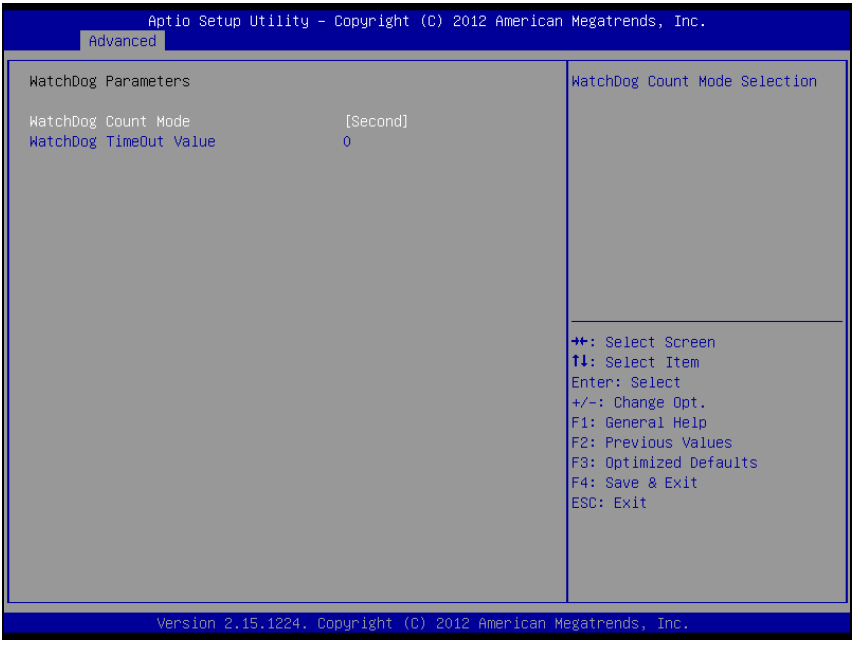


NCT6106D HW Monitor screen

BIOS Setting	Options	Description/Purpose
SYS Thermistor Temp	No changeable options	Displays system's temperature.
CPU Diode Temp	No changeable options	Displays processor's temperature.
CPU Fan Speed	No changeable options	Displays fan speed of the CPU fan.
VCORE	No changeable options	Displays voltage level of the +VCORE in supply.
+12V	No changeable options	Displays voltage level of the +12V in supply.
+5V	No changeable options	Displays voltage level of the +5V in supply.
+3.3V	No changeable options	Displays voltage level of the +3.3V in supply.

BIOS Setting	Options	Description/Purpose
VSB3	No changeable options	Displays voltage level of the +3.3VSB in supply.
VBAT	No changeable options	Displays voltage level of the backup CMOS battery.

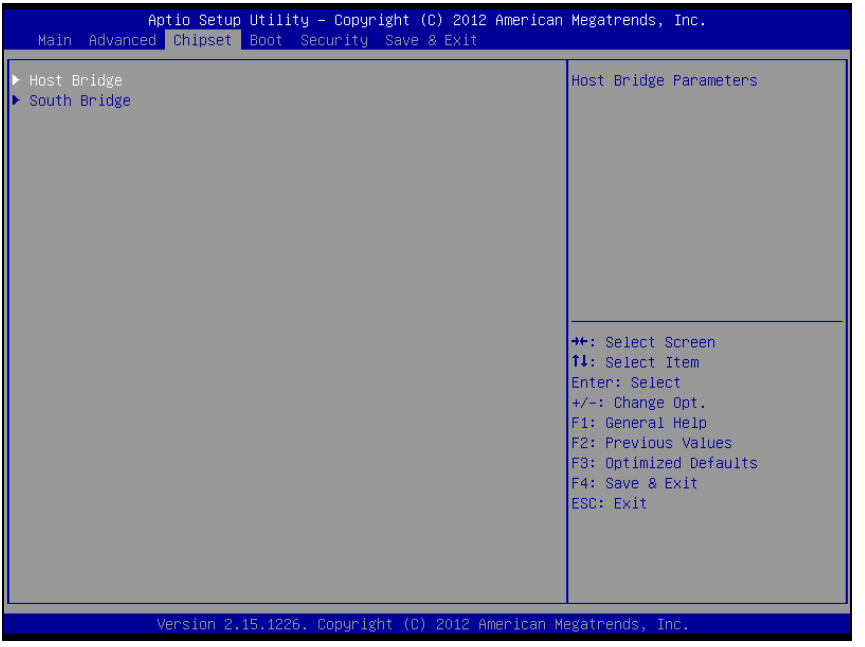
4-4-7. Advanced – Watchdog Configuration



Watchdog Configuration screen

BIOS Setting	Options	Description/Purpose
Watchdog count mode	-Second -Minute	Selects unit for watchdog timer.
Watchdog timeout value	Multiple options ranging from 0 to 255	Sets the desired value for watchdog timer. 0 means disabled.

4-5. Chipset



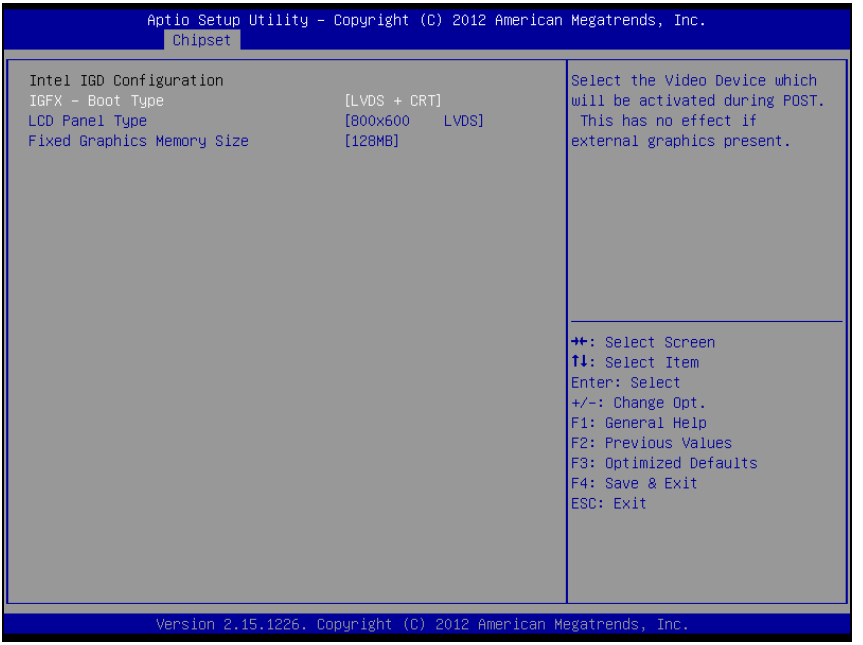
Chipset screen

4-5-1. Chipset – Host Bridge

Host Bridge screen

BIOS Setting	Options	Description/Purpose
Memory Frequency	No changeable options	Display the speed of your memory.
Total Memory	No changeable options	Displays the total amount of RAM.
DIMM#1	No changeable options	Display the amount of RAM installed in first memory slot.

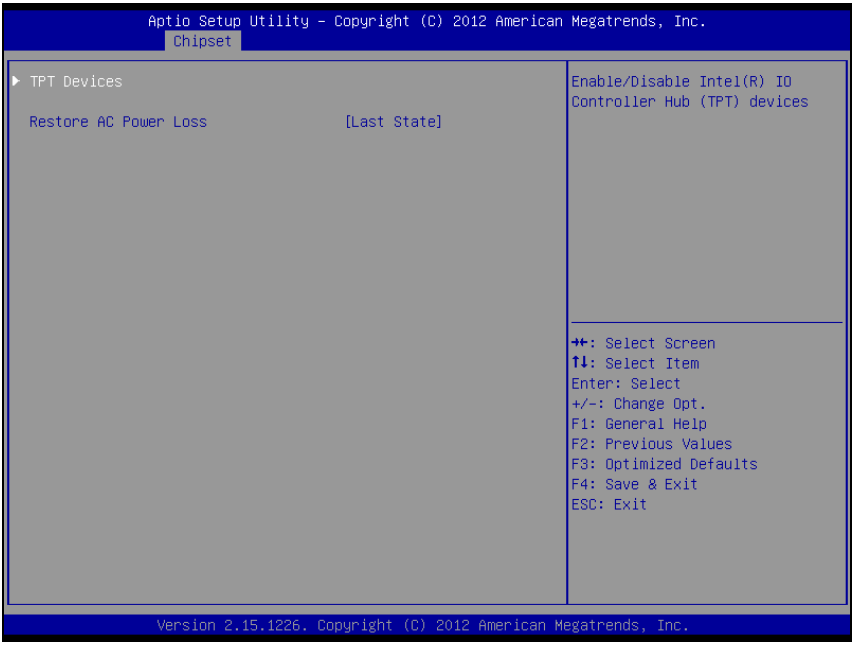
4-5-1-1. Host Bridge – Intel® IGD Configuration



Intel® IGD Configuration screen

BIOS Setting	Options	Description/Purpose
IGFX – Boot Type	- LVDS + CRT - CRT - LVDS	Select the video device which will be activated during POST.
LCD Panel Type	-800x600 LVDS -1024x768 LVDS	Select LCD Panel used by Internal Graphics Device by selecting the appropriate setup item.
Fixed graphic Memory Size	-128MB -256MB	Configure fixed graphics memory size.

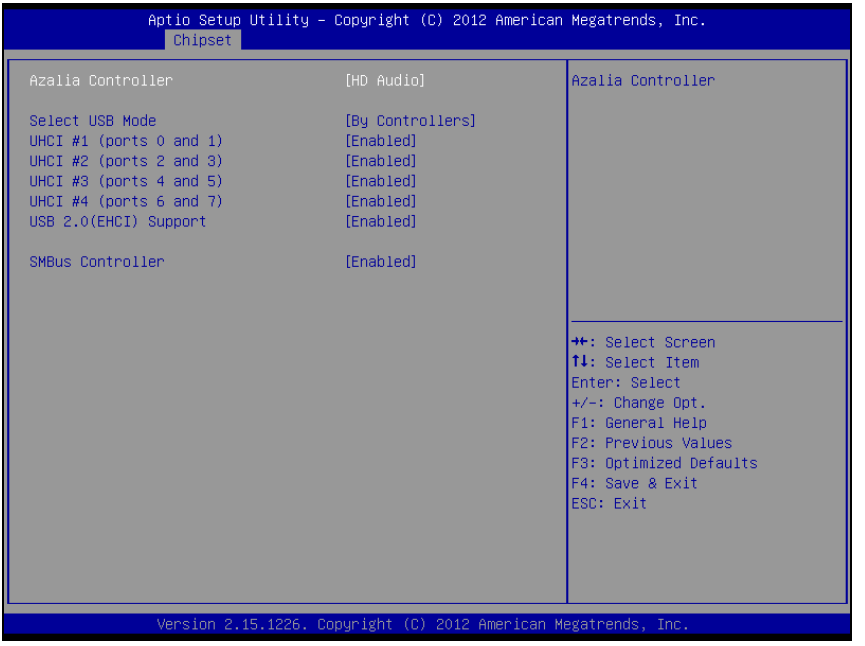
4-5-2. Chipset – South Bridge



South Bridge screen

BIOS Setting	Options	Description/Purpose
Restore AC Power Loss	-Power Off -Power On -Last State	Determines the mode of operation in case of power loss. ▪ **Power Off** keeps the power off till the power button is pressed. ▪ **Power On** restores power to the computer. ▪ **Last State** restores the previous power state before power loss happened.

4-5-2-1. South Bridge – TPT Devices

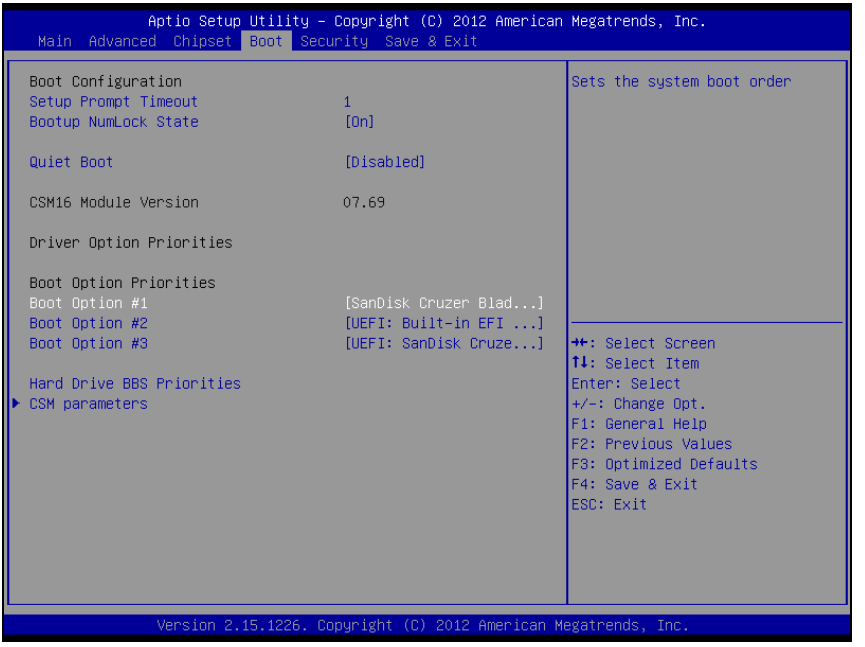


TPT Devices screen

BIOS Setting	Options	Description/Purpose
Azalia Controller	-Disabled -HD Audio	Azalia Controller
Select USB Mode	- By Ports - By Controllers	Select USB mode to control USB ports.
UHCI #1 (ports 0 and 1)	-Disabled -Enabled	Control the USB UHCI (USB 1.1) functions. Disable from highest to lowest Controller.
UHCI #2 (ports 2 and 3)	-Disabled -Enabled	Control the USB UHCI (USB 1.1) functions. Disable from highest to lowest Controller.
UHCI #3 (ports 4 and 5)	-Disabled -Enabled	Control the USB UHCI (USB 1.1) functions. Disable from highest to lowest Controller.

BIOS Setting	Options	Description/Purpose
UHCI #4 (ports 6 and 7)	-Disabled -Enabled	Control the USB UHCI (USB 1.1) functions. Disable from highest to lowest Controller.
USB 2.0(EHCI) Support	-Disabled -Enabled	Enable or Disable USB 2.0 (EHCI) Support
SMBus controller	-Disabled -Enabled	Enable or Disable OnChip SMBus Controller.

4-6. Boot

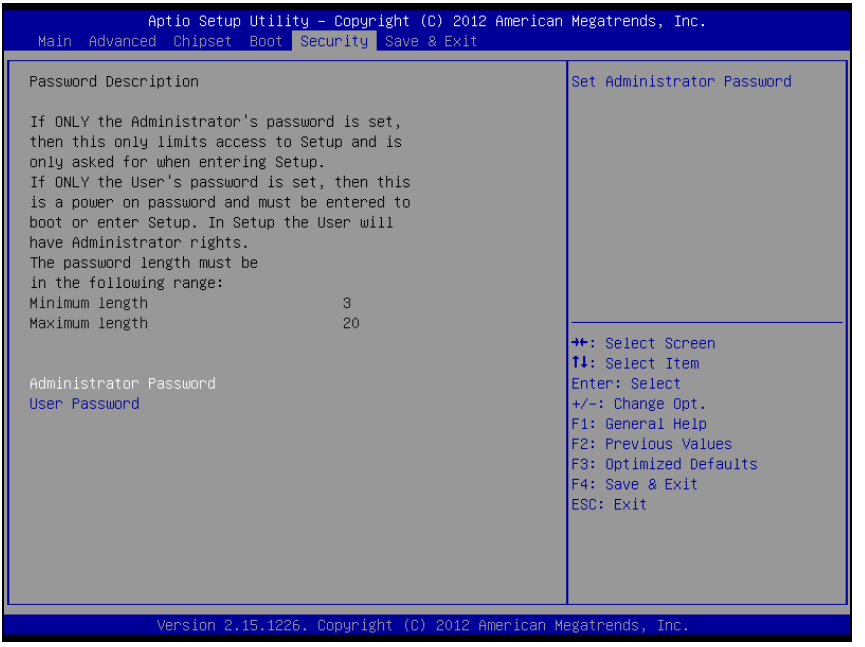


Boot screen

BIOS Setting	Options	Description/Purpose
Setup Prompt Timeout	Multiple options ranging from 1 to 65535	Specifies number of seconds to wait for setup activation key (value 65535 results in indefinite waiting).
Bootup NumLock Status	-On -Off	Specifies the power-on state of the numlock feature on the numeric keypad of keyboard.
Quiet Boot	-Disabled-Enabled	When quiet boot is enabled, it displays OEM logo instead of POST messages during boot.
CSM16 Module Version	No changeable options	Displays the current Compatibility Support Module version.

BIOS Setting	Options	Description/Purpose
Option ROM Messages	-Force BIOS -Keep Current	Allows the POST screen to display Option ROM messages.
UEFI Boot	-Enabled -Disabled	▪ **Enabled**:enables all UEFI boot options. ▪ **Disabled**:disables all UEFI boot options.
Boot Option #1	-[drive(s)] -Disabled	Allows setting boot option listed in Hard Drive BBS Priorities.

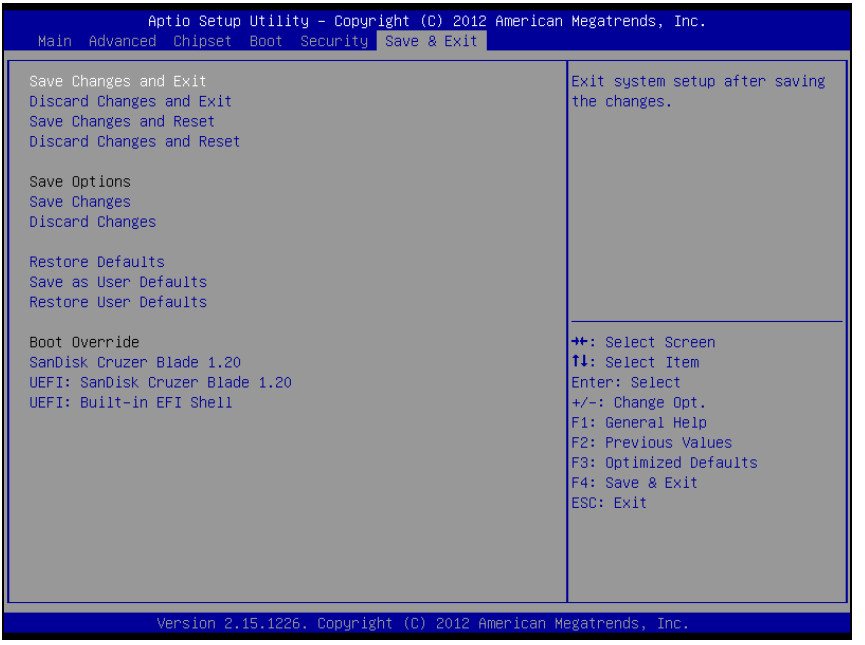
4-7. Security



Security screen

BIOS Setting	Options	Description/Purpose
Administrator Password	Password can be up to 20 alphanumeric characters.	Specifies the administrator password.
User Password	Password can be up to 20 alphanumeric characters.	Specifies the user password.

4-8. Save & Exit



Save & Exit screen

BIOS Setting	Options	Description/Purpose
Save Changes and Exit	No changeable options	Exits and saves the changes in CMOS SRAM.
Discard Changes and Exit	No changeable options	Exits without saving any changes made in BIOS settings.
Save Changes and Reset	No changeable options	Saves the changes in CMOS SRAM and resets.
Discard Changes and Reset	No changeable options	Resets without saving any changes made in BIOS settings.
Save Changes	No changeable options	Saves the changes done in BIOS settings so far.
Discard Changes	No changeable options	Discards the changes done in BIOS settings so far.

BIOS Setting	Options	Description/Purpose
Restore Defaults	No changeable options	Loads the optimized defaults for BIOS settings.
Save as User Defaults	No changeable options	Saves the current values as user defaults.
Restore User Defaults	No changeable options	Loads the user defaults for BIOS settings.
Boot Override	-[drive(s)]	Forces to boot from selected [drive(s)].

APPENDIX

A

SYSTEM ASSEMBLY

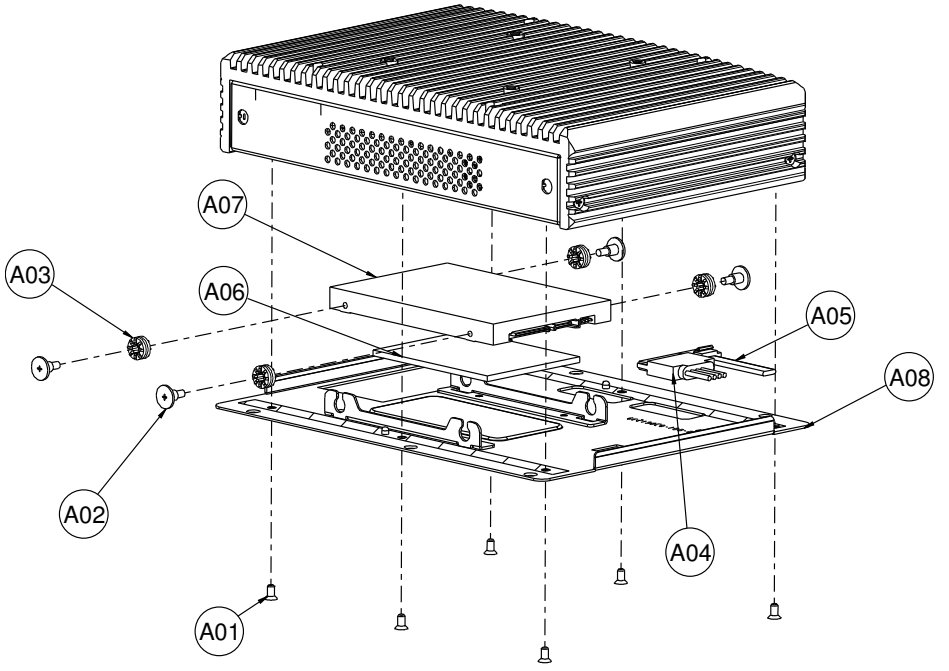
This appendix contains the exploded diagram of the system.

Section includes:

- Exploded Diagram for SP-6120 (CPT)/6122/6128 System
- Exploded Diagram for SP-6120 System
- Exploded Diagram for SP-6120 CPT System
- Exploded Diagram for SP-6122 System
- Exploded Diagram for SP-6128 System

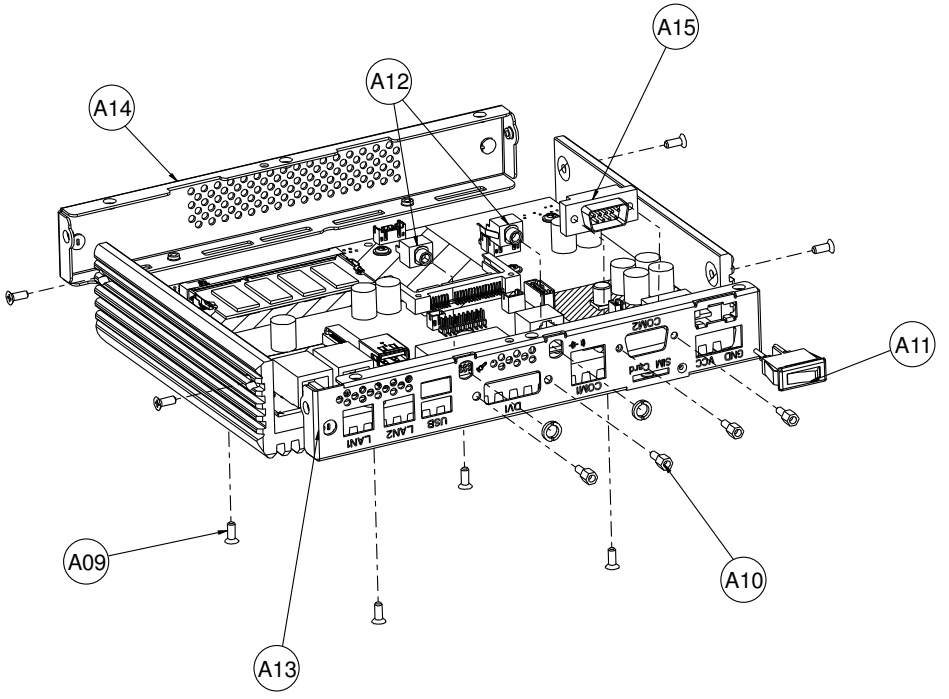
EXPLODED DIAGRAM FOR SP-6120 (CPT)/6122/6128 SYSTEM

SATA HDD



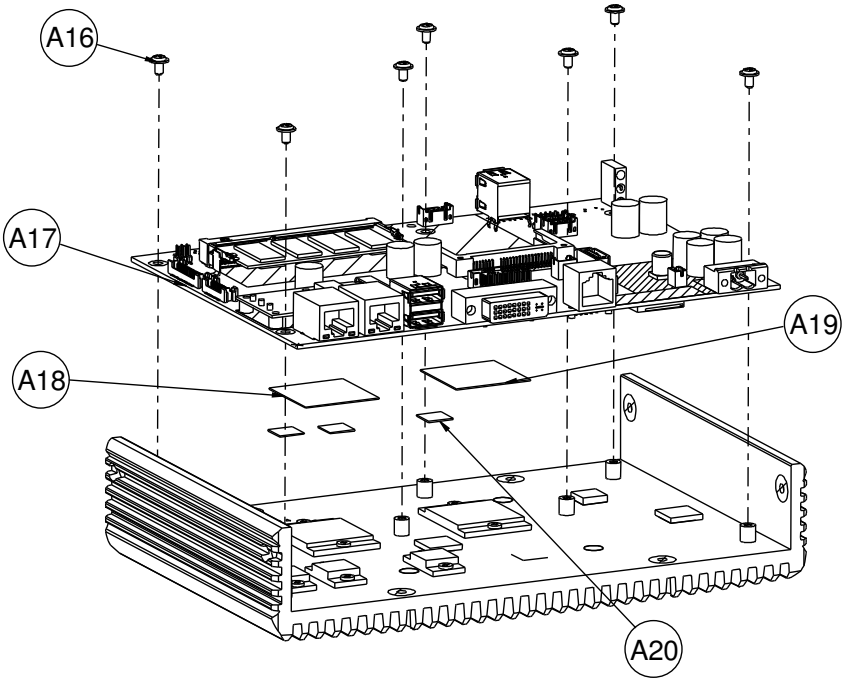
No.	Name	P/N No.	Qt'y
A01	M3_L6_F_B	22-215-30060011	6
A02	HDD Screw	82-272-30005013	4
A03	HDD Rubber	23-680-39580963	4
A04	sata power cable	27-008-24902071	1
A05	sata data cable	27-008-20902031	1
A06	Thermal Pad	21-006-07055001	1
A07	sata hdd	By order	1
A08	611x_bot_case	20-001-03061239	1

Rear case



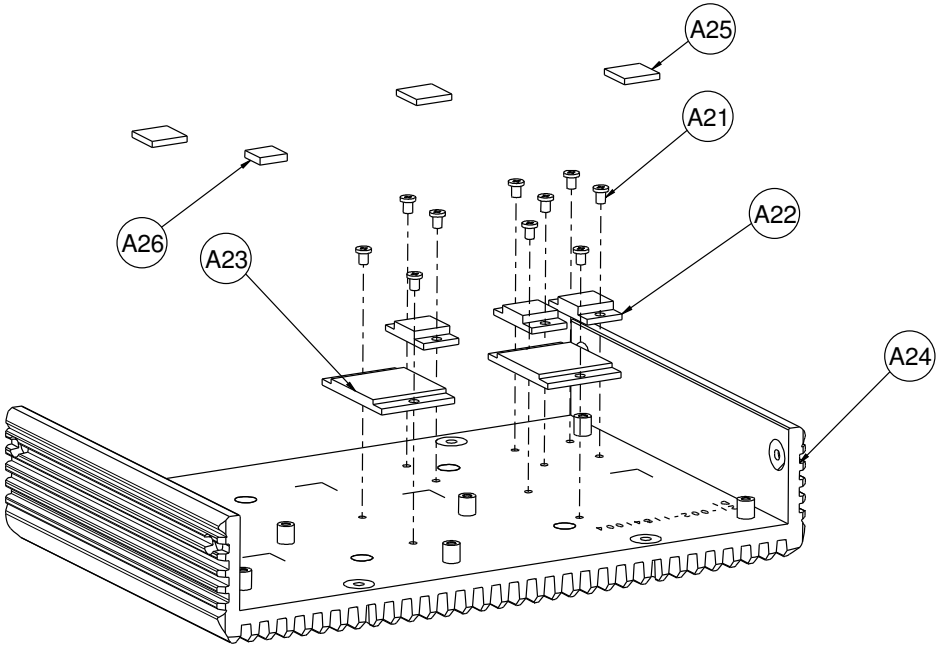
No.	Name	P/N No.	Qt'y
A09	M3_L6_F_B	22-215-30060011	8
A10	No.4 BOSS	22-692-40048051	4
A11	switch cable	27-019-26301071	1
A12	LINE OUT CABLE	27-028-24906111	1
A13	SP-612X_BACK_CASE	20-001-03061250	1
A14	SP-610X_FRONT_CASE	20-001-03062239	1
A15	COM Cable	27-024-24902031	1

Main board



No.	Name	P/N No.	Qt'y
A16	M3_L5_Washer_Ni	22-242-30005311	7
A17	SB-8122	--	1
A18	Thermal Pad Big	81-006-83030003	1
A19	Thermal Pad Big 2	81-006-83030004	1
A20	Thermal Pad small	81-006-81010003	3

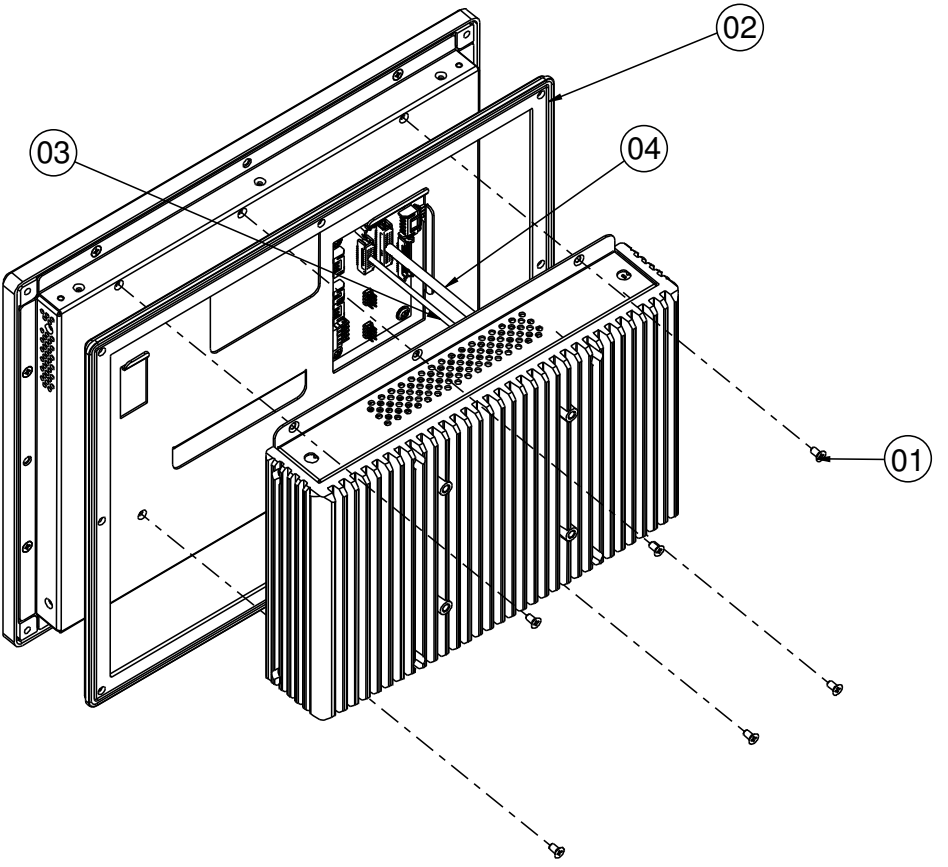
Heat sink



No.	Name	P/N No.	Qt'y
A21	M3_L5_1	22-272-30049015	10
A22	Heatsink Block small	21-002-12513001	3
A23	Heatsink Block Big	21-002-13927001	2
A24	611X Heatsink	21-002-11841003	1
A25	Thermal Pad 1	81-006-81313002	3
A26	thermal pad 2	81-006-81010002	1

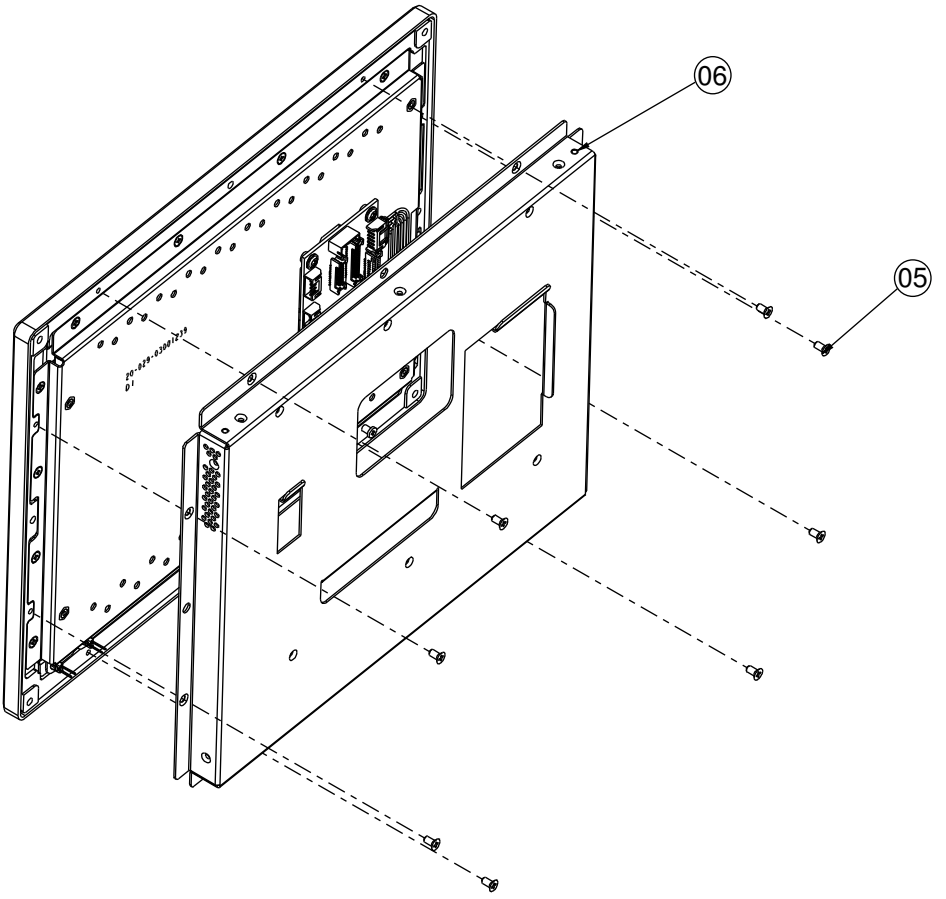
EXPLODED DIAGRAM FOR SP-6120 SYSTEM

Open & close



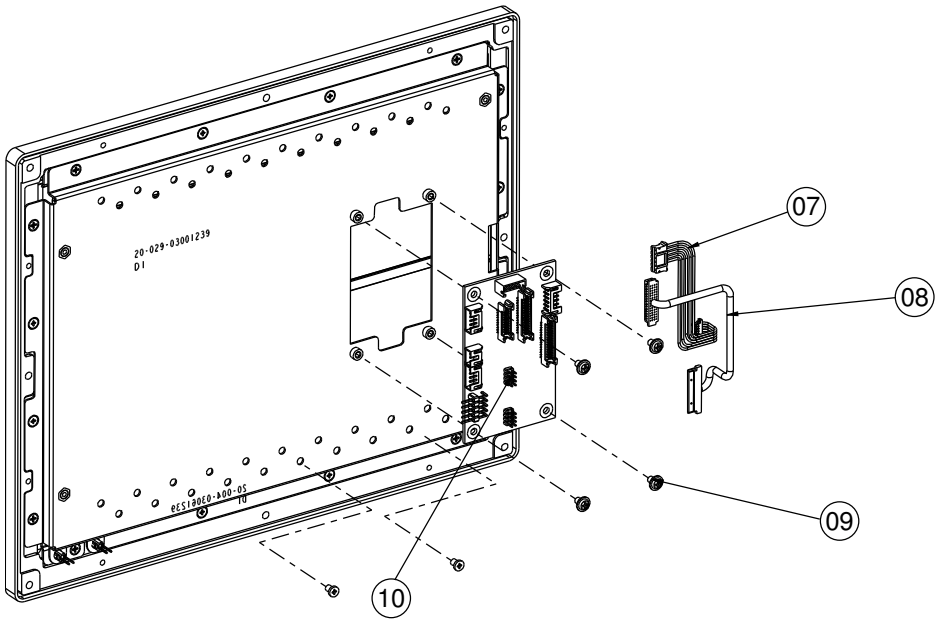
No.	Name	P/N No.	Qt'y
1	M3_L6_F_B	22-215-30060011	6
2	outside rubber	30-013-01100239	1
3	Link_cable data	27-055-26303111	1
4	Link cable lvds	27-020-26304111	1

LCD cover



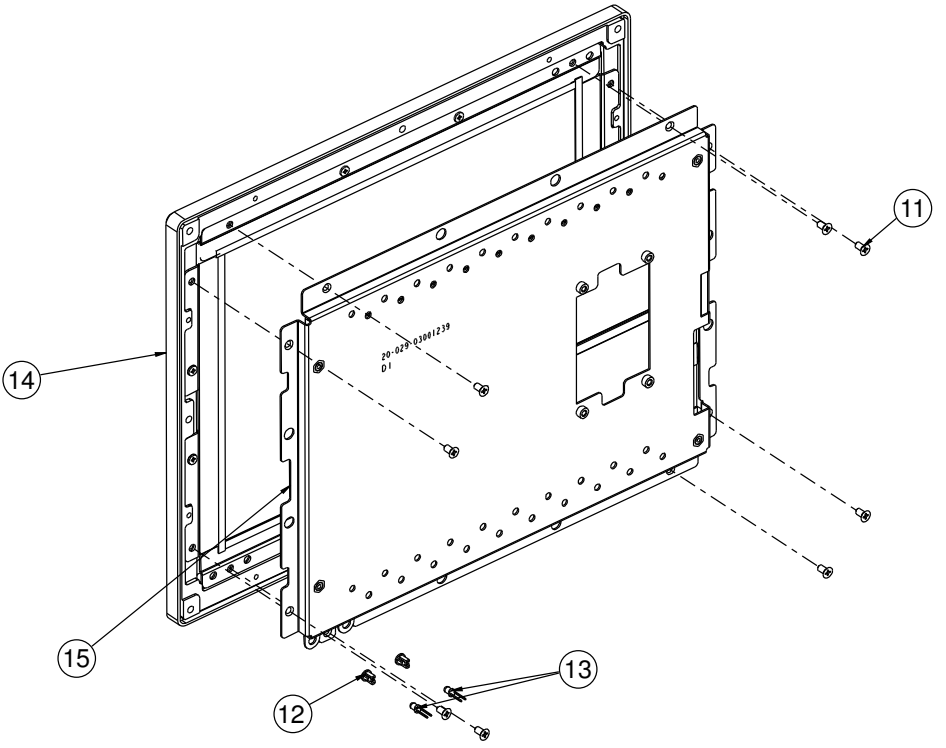
No.	Name	P/N No.	Qt'y
5	M3_L6_F_B	22-215-30060011	8
6	LCD cover	20-004-03061239	1

Daughter board



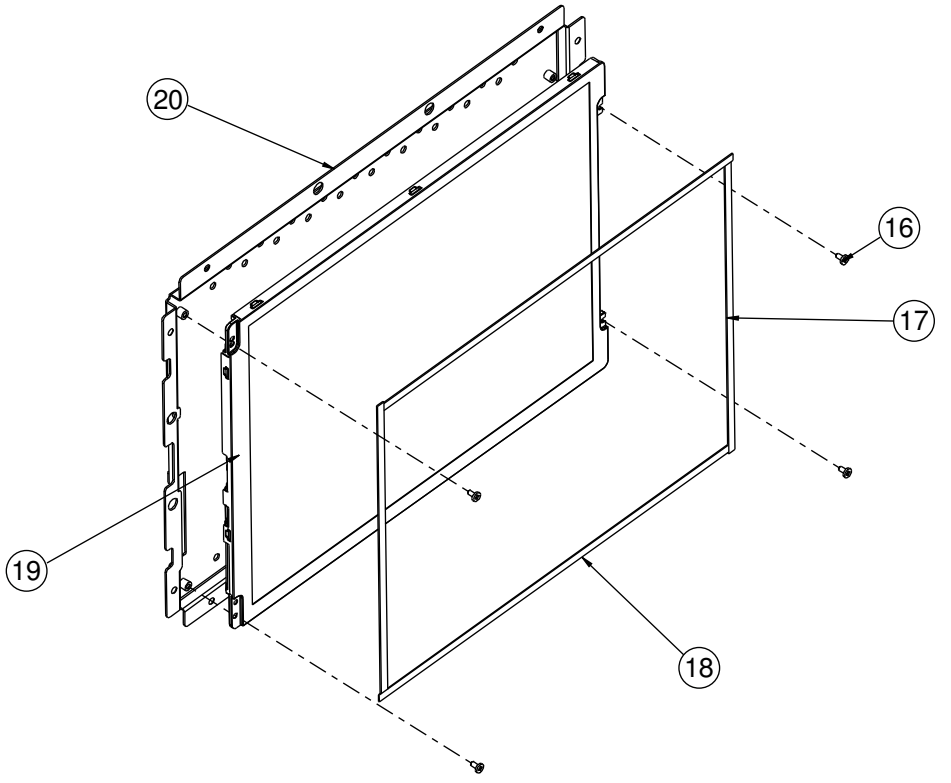
No.	Name	P/N No.	Qt'y
7	LCD_LED cable	27-055-25002071	1
8	LVDS Cable	27-020-25003111	1
9	M3_L5_Washer_Ni	22-242-30005311	4
10	SR-6100RA-D4N	--	1

LCD holder



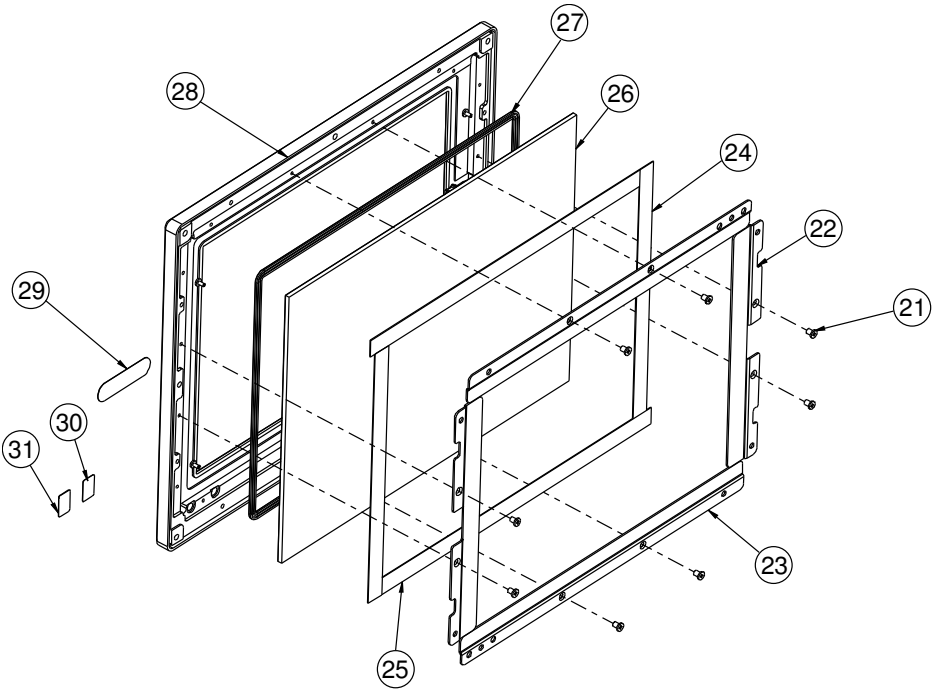
No.	Name	P/N No.	Qt'y
11	M3_L5_F_B	22-215-30005011	8
12	D3mm LED HOUSING	30-014-04100165	2
13	power+hdd led cable	27-018-25005111	1
14	Panel_Assembly	--	1
15	LCD Holder Assembly	--	1

LCD Panel



No.	Name	P/N No.	Qt'y
16	M2_L4_I_Ni	22-272-20004011	4
17	PORON(167X4X0.5T)	30-013-24700000	2
18	PORON(220X4X0.5T)	30-013-24600000	2
19	Panel	52-351-01104302	1
20	LCD_holder	20-029-03001239	1

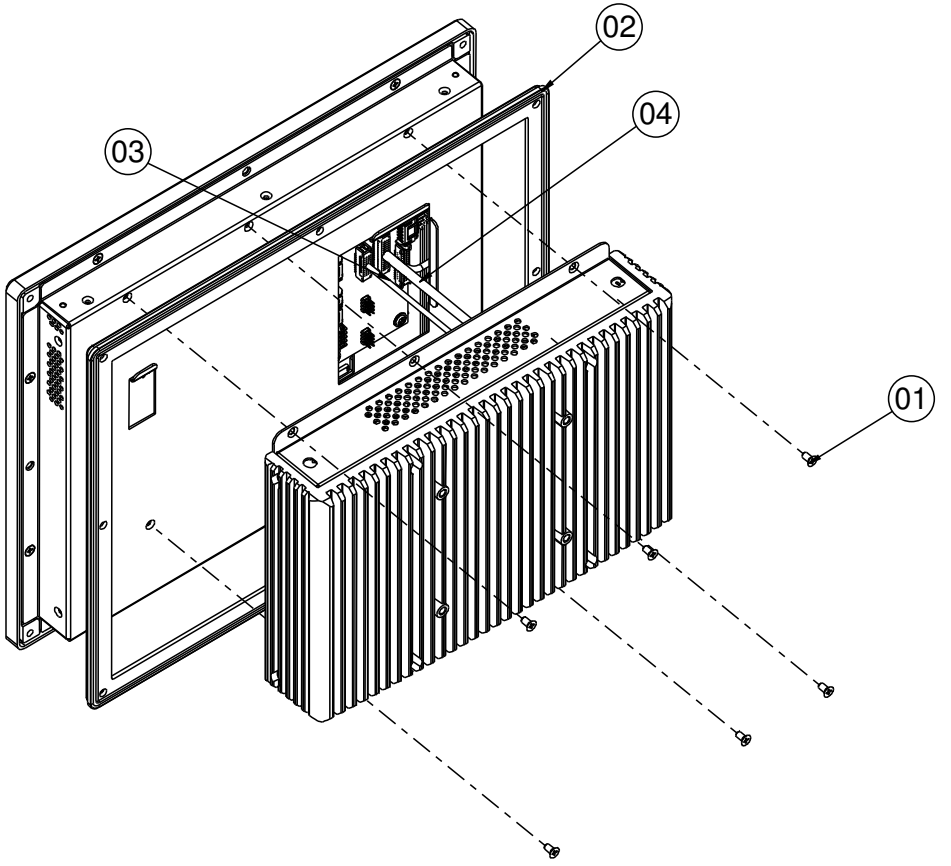
Touch panel & front panel



No.	Name	P/N No.	Qt'y
21	M3_L4_I_Ni	82-272-30004018	8
22	6110-TOUCH-SUPPORT_LR	20-006-03001239	2
23	6110-TOUCH-SUPPORT_TB	20-006-03002239	2
24	PORON_175x11.6x0.5T	90-013-24100000	2
25	PORON_233X11.6X0.5T	90-013-24200000	2
26	ELO Touch	52-380-01510401	1
27	LCD_RUBBER	30-013-01100045	1
28	6110 Front PANEL	20-003-01091239	1
29	Protech Label	34-017-02104009	1
30	HDD Label	34-017-02101009	1
31	Power Label	34-017-02103009	1

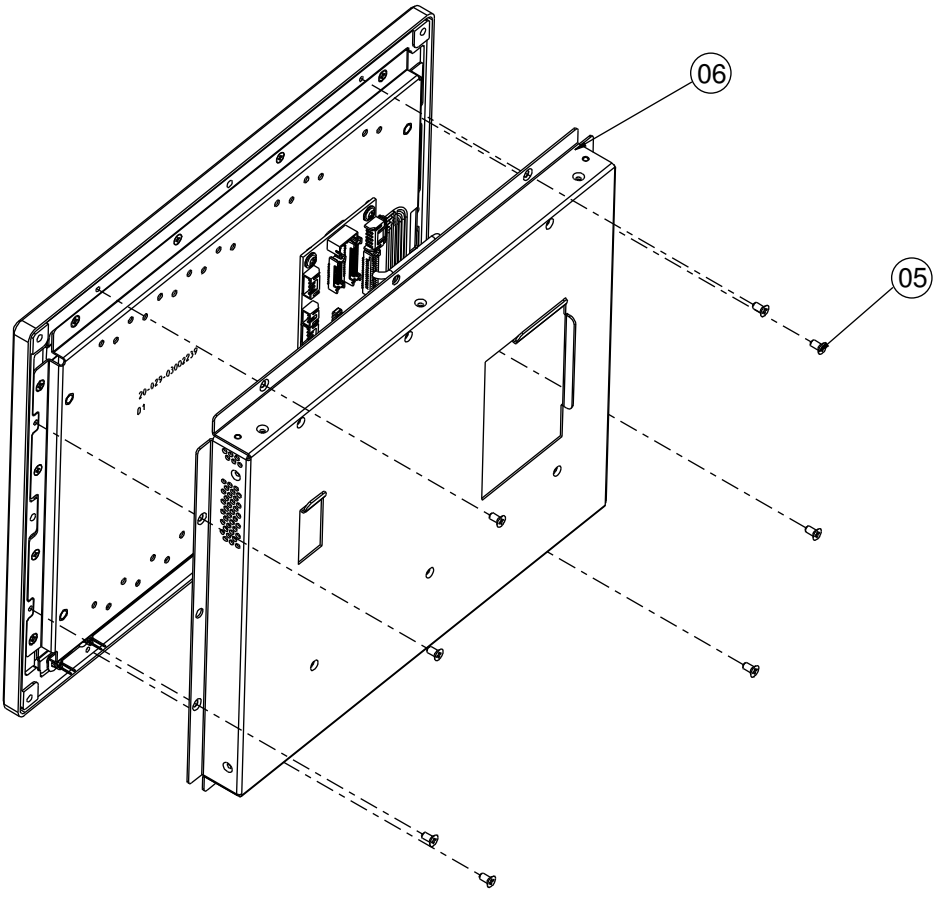
EXPLODED DIAGRAM FOR SP-6120 CPT SYSTEM

Open & close



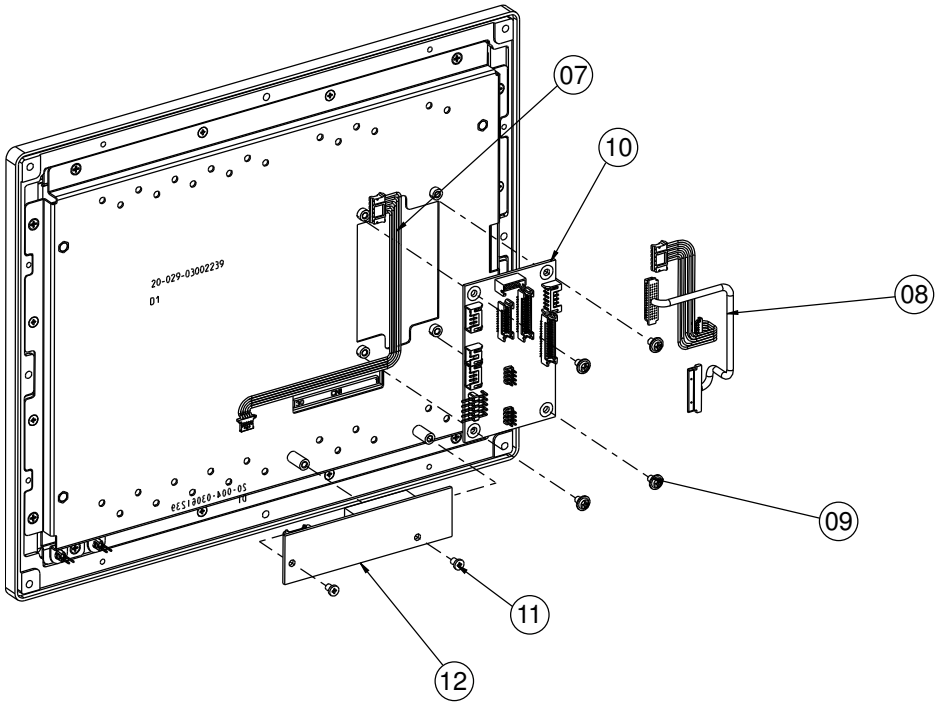
No.	Name	P/N No.	Qt'y
1	M3_L6_F_B	22-215-30060011	6
2	outside rubber	30-013-01100239	1
3	Link_cable data	27-055-26303111	1
4	Link cable lvds	27-020-26304111	1

LCD cover



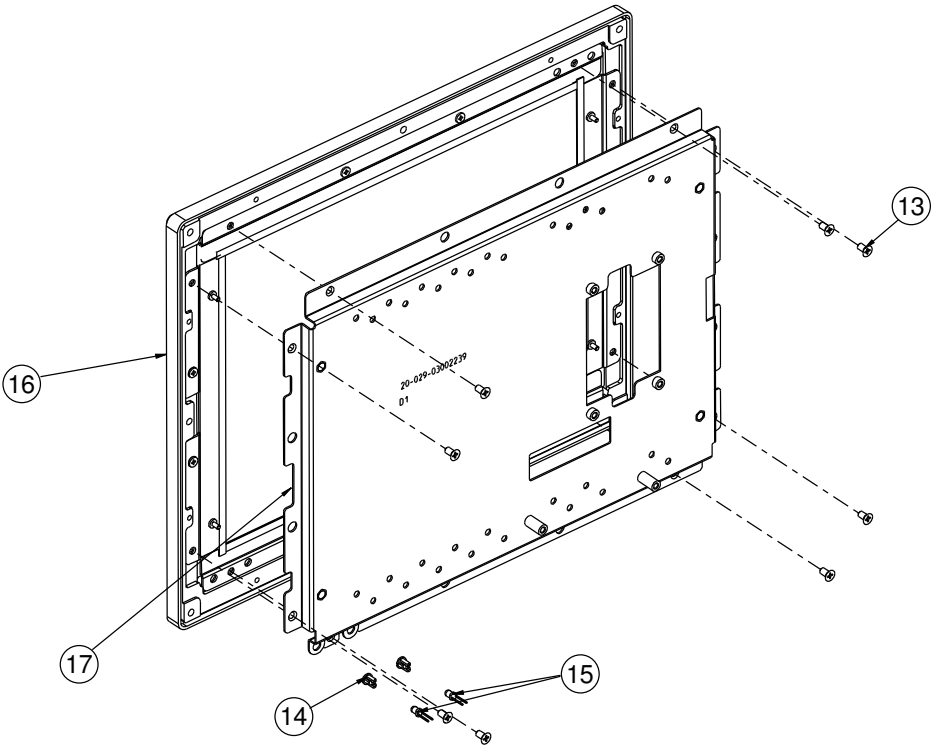
No.	Name	P/N No.	Qt'y
5	M3_L6_F_B	22-215-30060011	8
6	SP-6110 CPT LCD COVER	20-004-03062239	1

Daughter board



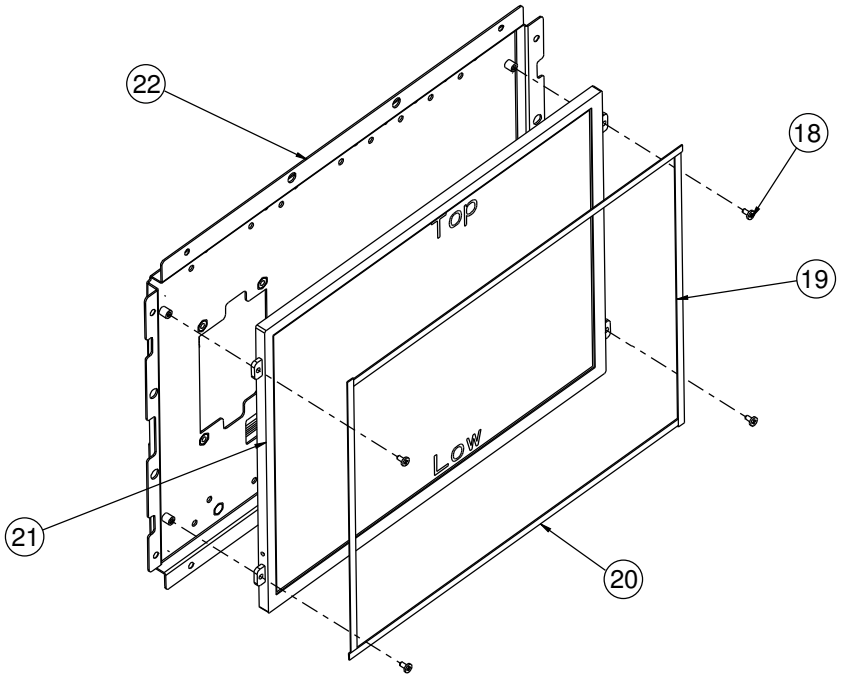
No.	Name	P/N No.	Qt'y
7	INVERTER CABLE	27-055-23904111	1
8	LVDS Cable	27-020-23903111	1
9	M3_L5_Washer_Ni	22-242-30005311	4
10	SR-6100RA-D4N	--	1
11	M3_L4_1_Ni	82-272-30004018	2
12	INVERTER	52-101-08010203	1

LCD holder



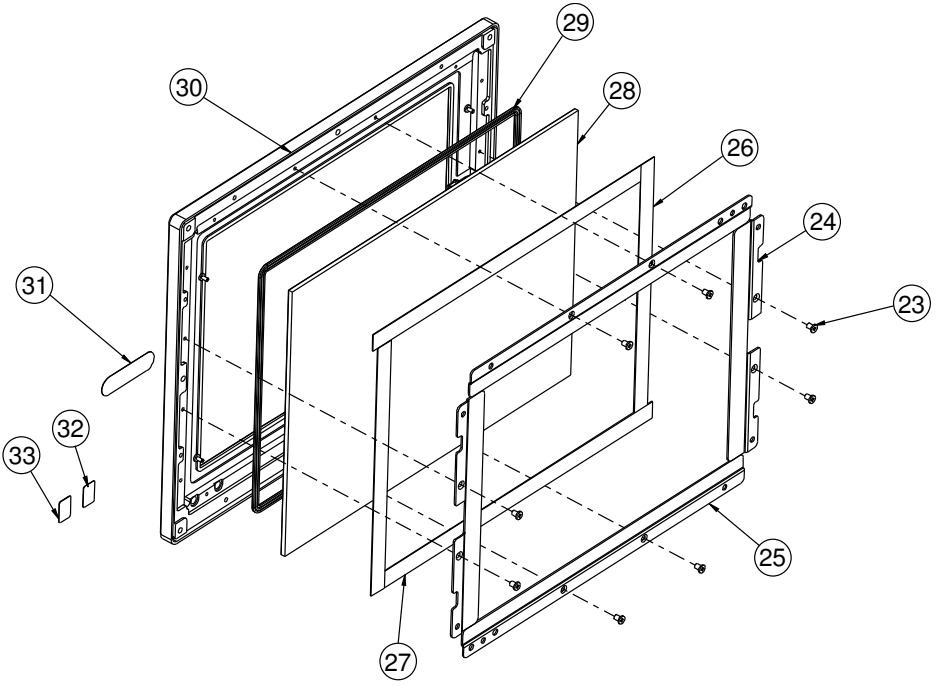
No.	Name	P/N No.	Qt'y
13	M3_L5_F_B	22-215-30005011	8
14	D3mm LED HOUSING	30-014-04100165	2
15	power+hdd led cable	27-018-25005111	1
16	Panel_Assembly	--	1
17	LCD Holder Assembly	--	1

LCD panel



No.	Name	P/N No.	Qt'y
18	M2_L4_1_Ni	22-272-20004011	4
19	PORON(167X4X0.5T)	30-013-24700000	2
20	PORON(220X4X0.5T)	30-013-24600000	2
21	Panel	52-351-01104019	1
22	CPT LCD_holder	20-029-03002239	1

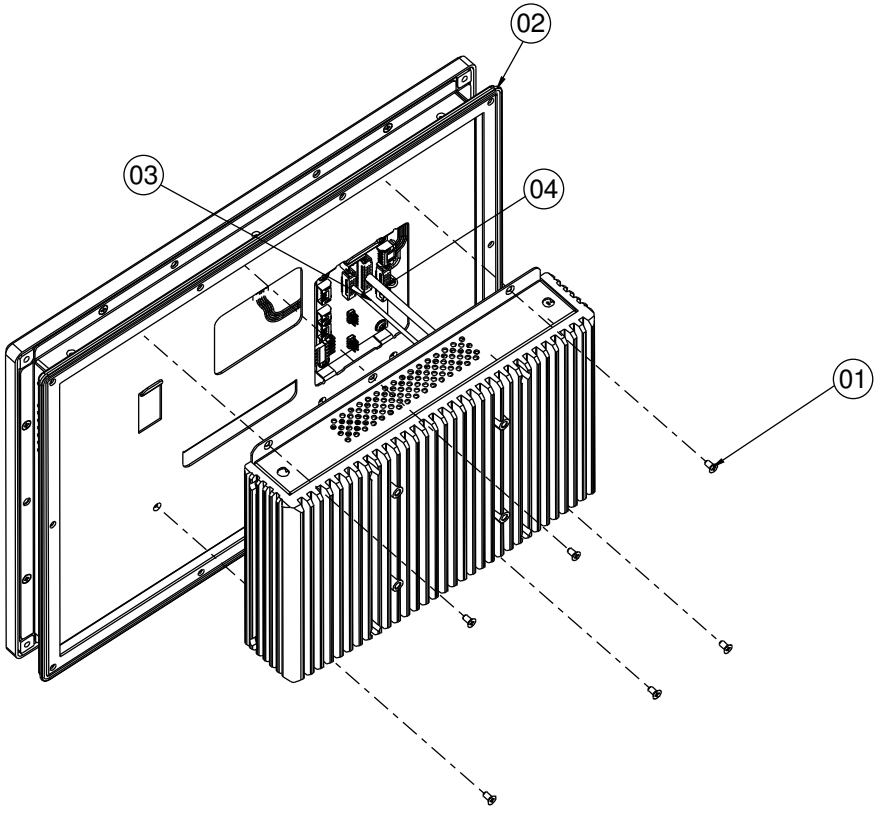
Touch panel & front panel



No.	Name	P/N No.	Qt'y
23	M3_L4_I_Ni	82-272-30004018	8
24	6110-TOUCH-SUPPORT_LR	20-006-03001239	2
25	6110-TOUCH-SUPPORT_TB	20-006-03002239	2
26	PORON_175x11.6x0.5T	90-013-24100000	2
27	PORON_233X11.6X0.5T	90-013-24200000	2
28	ELO Touch	52-380-01510401	1
29	LCD_RUBBER	30-013-01100045	1
30	6110 Front PANEL	20-003-01091239	1
31	Protech Label	34-017-02104009	1
32	HDD Label	34-017-02101009	1
33	Power Label	34-017-02103009	1

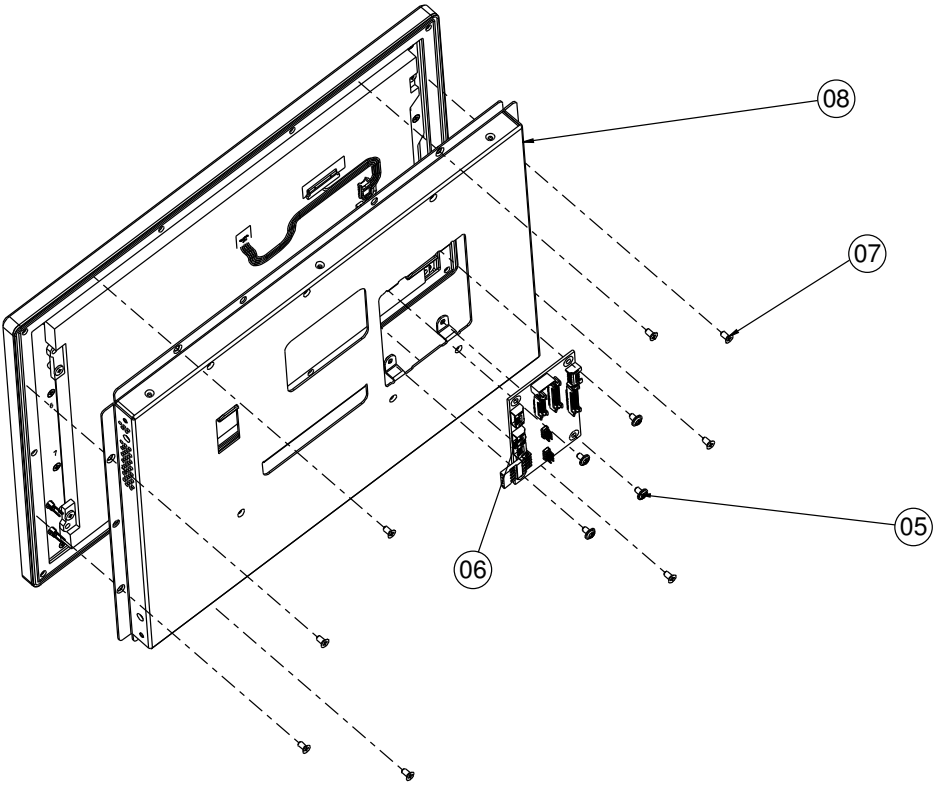
EXPLODED DIAGRAM FOR SP-6122 SYSTEM

Open & close



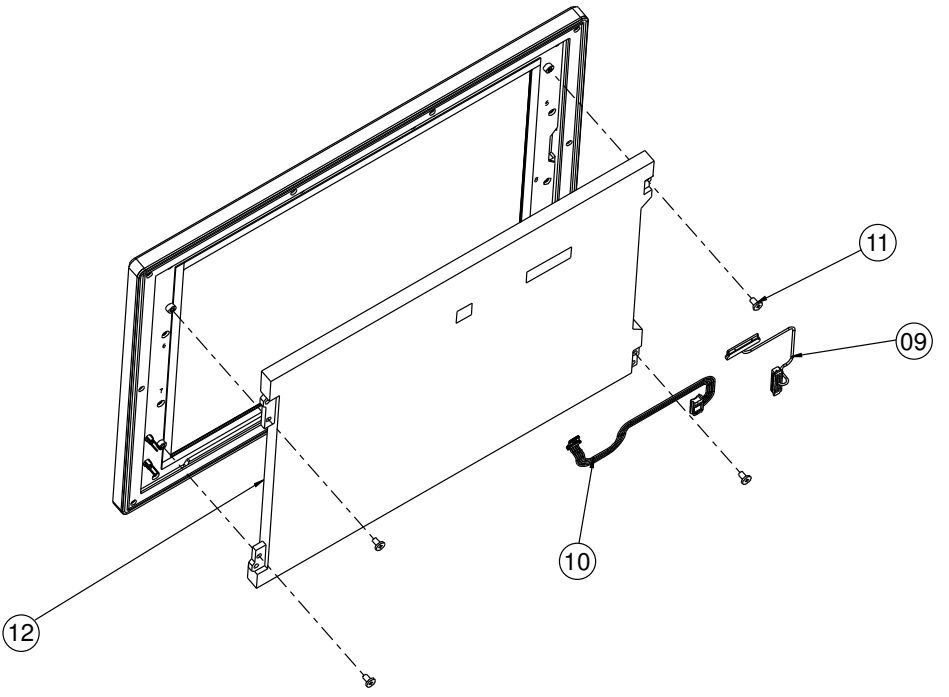
No.	Name	P/N No.	Qt'y
1	M3_L6_F_B	22-215-30060011	6
2	6112_outside rubber	30-013-01200240	1
3	Link_cable data	27-055-26303111	1
4	LInk cable lvds	27-020-26304111	1

LCD cover & daughter board



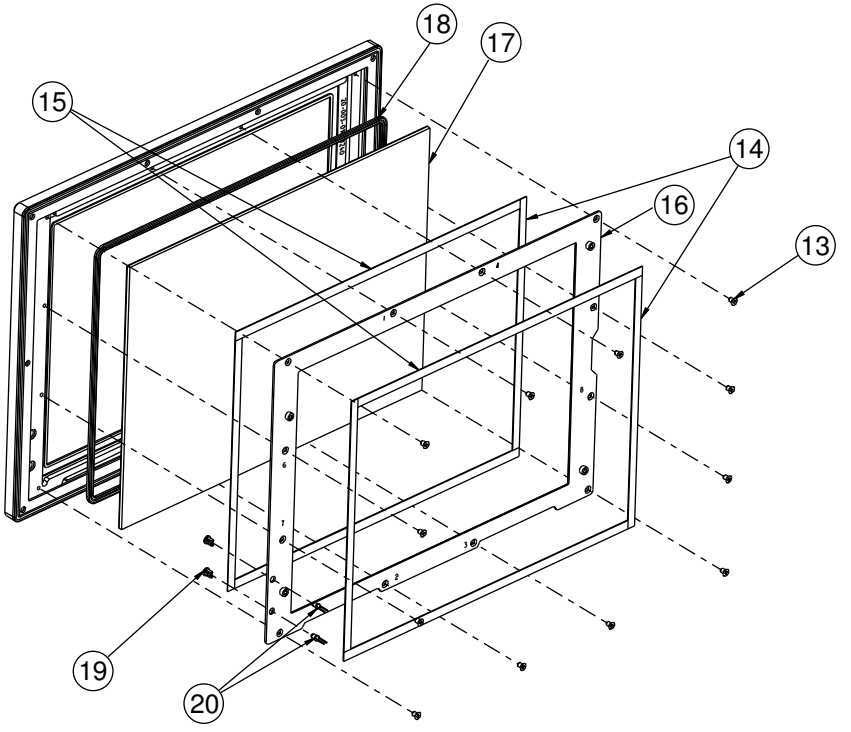
No.	Name	P/N No.	Qt'y
5	M3_L5_Washer_Ni	22-242-30005311	4
6	SR-6100RA-D4N	--	1
7	M3_L6_F_B	22-215-30060011	8
8	6112 LCD cover	20-004-03061240	1

LCD panel



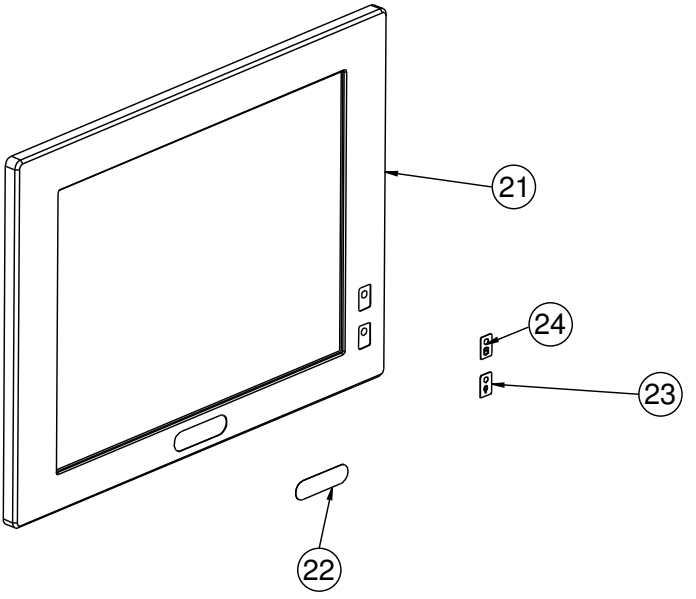
No.	Name	P/N No.	Qt'y
09	LCD_LED cable	27-055-26203071	1
10	LVDS Cable	27-020-26203111	1
11	M3_L5_1_Ni	22-272-30049015	4
12	12" panel	52-351-02121002	1

Touch panel



No.	Name	P/N No.	Qt'y
13	M3_L4_F_Ni	22-215-30005011	12
14	197X6X0.5T_PORON	90-013-24300264	4
15	257X7X0.5T_PORON	90-013-24400264	4
16	12" touch holser	20-029-03002240	1
17	12" Touch	52-351-00011814	1
18	12" Rubber	30-013-01100240	1
19	D3mm LED HOUSING	30-014-04100165	2
20	power+hdd led cable	27-018-26206111	1

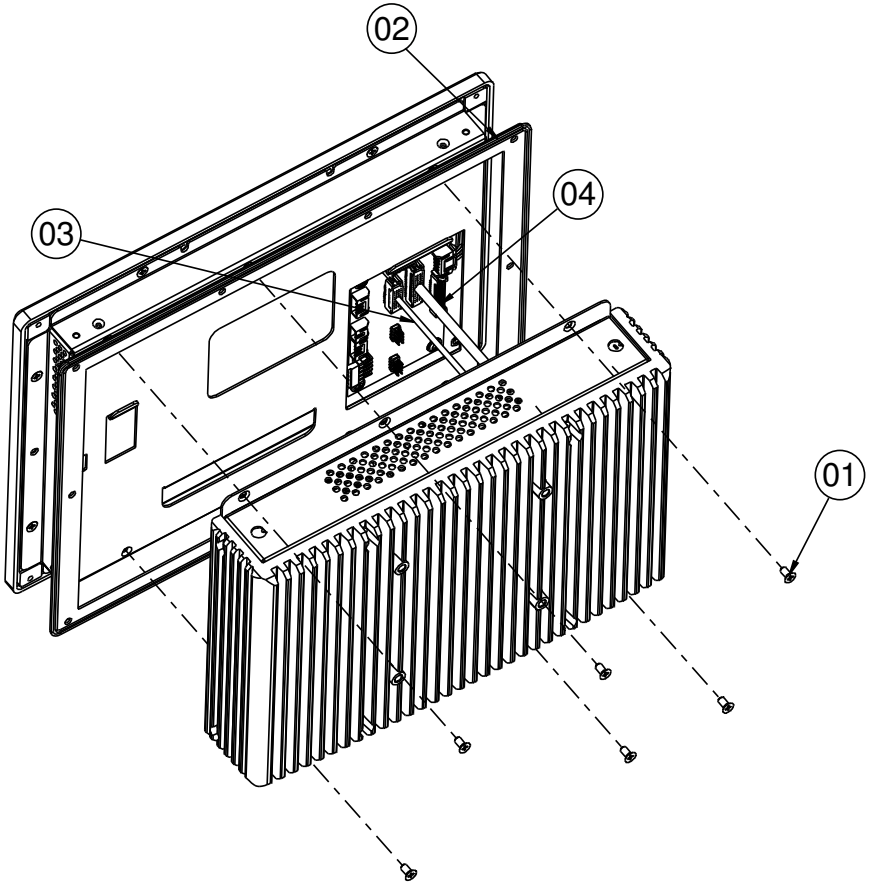
Front panel



No.	Name	P/N No.	Qt'y
21	6112 Front PANEL	20-003-01091240	1
22	Protech Label	34-017-02104009	1
23	HDD Label	34-017-02101009	1
24	Power Label	34-017-02103009	1

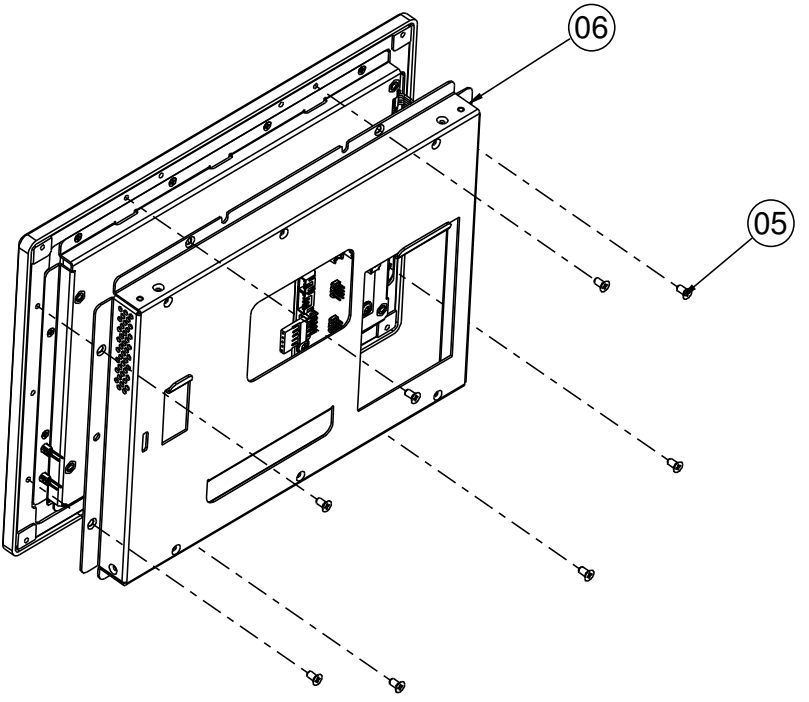
EXPLODED DIAGRAM FOR SP-6128 SYSTEM

Open & close



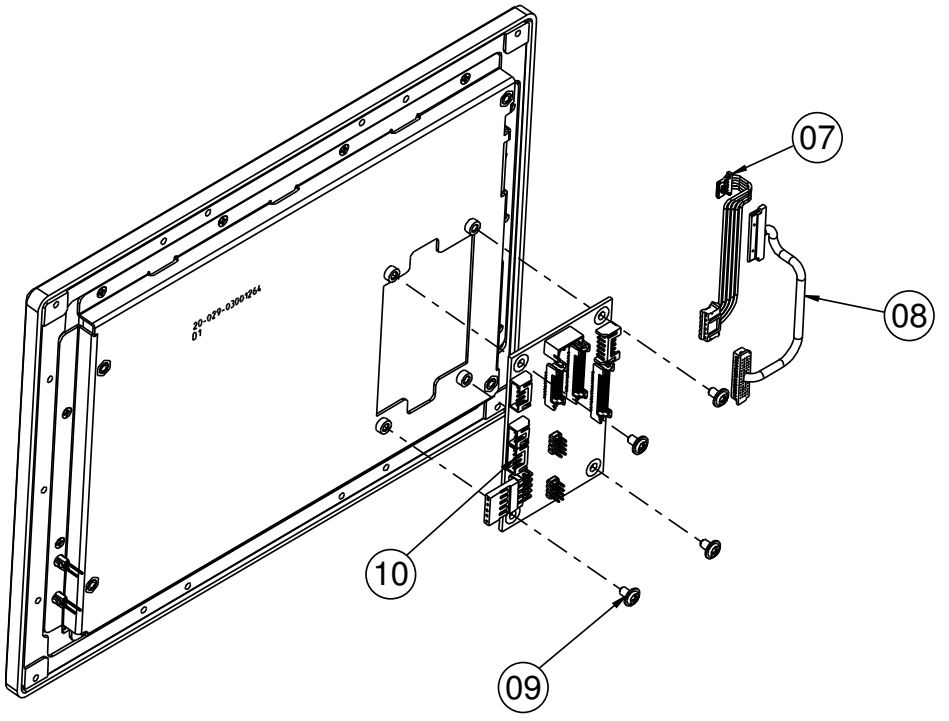
No.	Name	P/N No.	Qt'y
1	M3_L6_F_B	22-215-3006001I	6
2	6118_outside rubber	30-013-0120003I	1
3	Link_cable data	27-055-26303III	1
4	LInk cable lvds	27-020-26304III	1

LCD cover



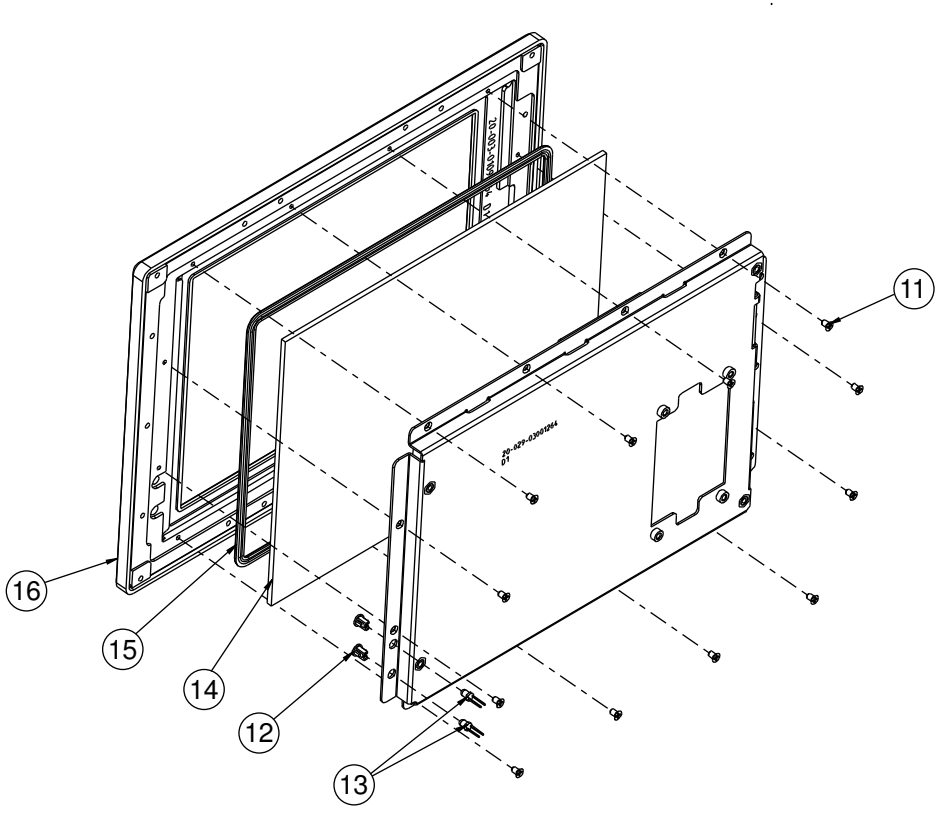
No.	Name	P/N No.	Qt'y
5	M3_L6_F_B	22-215-30060011	8
6	6118_LCD cover	20-004-03061264	1

Daughter board



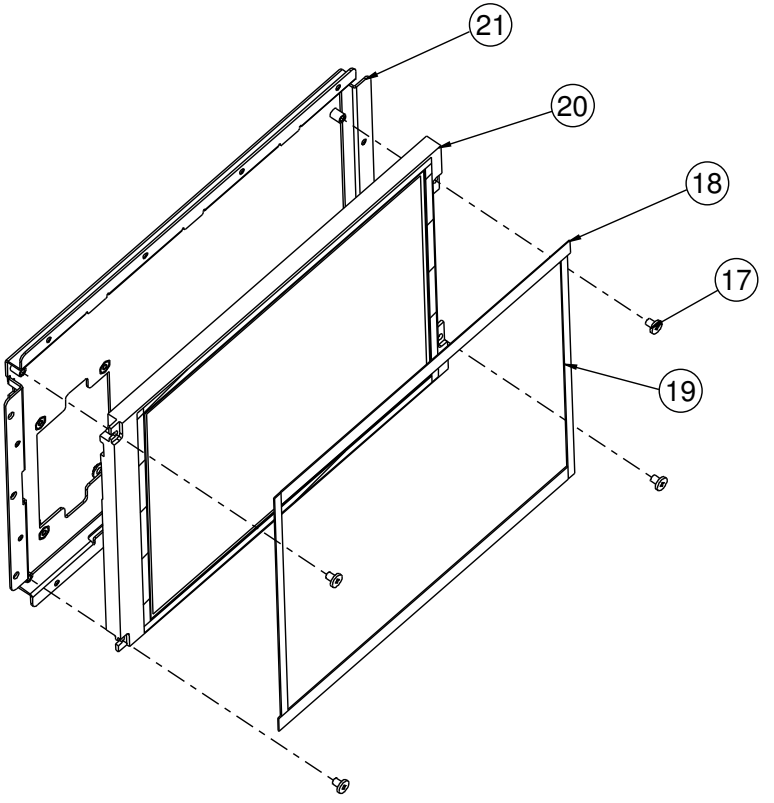
No.	Name	P/N No.	Qt'y
7	LCD_LED cable	27-069-26302071	1
8	LVDS Cable	27-020-26303111	1
9	M3_L5_Washer_Ni	22-242-30005311	4
10	SR-6100RA-D4N	--	1

LCD panel



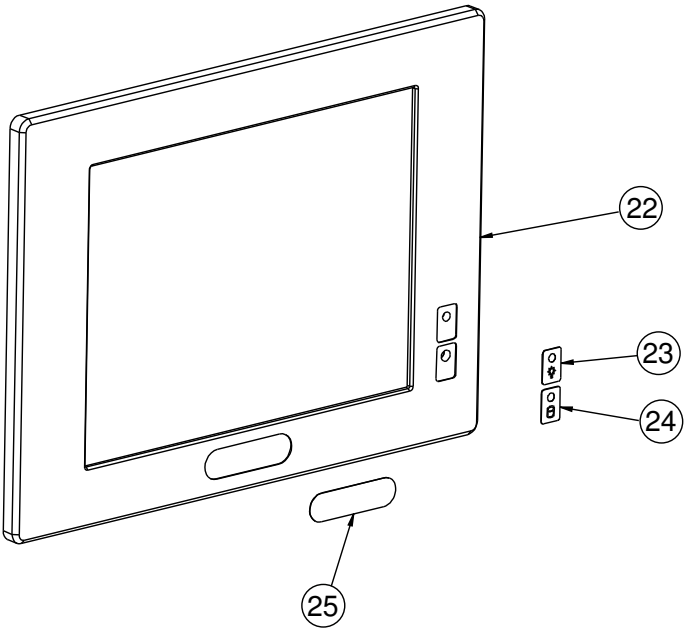
No.	Name	P/N No.	Qt'y
11	M2.5_L4_F_Ni	22-212-25004011	12
12	D3mm LED HOUSING	30-014-04100165	2
13	power+hdd led cable	27-018-26304111	1
14	8" ELO Touch	52-351-00494714	1
15	8: Lcd Rubber	30-013-01300031	1
16	Front Assembly	--	1

Touch panel



No.	Name	P/N No.	Qt'y
17	M3_L4_I_Ni	22-272-30049015	4
18	180X8X0.5T_PORON	90-013-24200264	2
19	139X4X0.5T_PORON	90-013-24100264	2
20	8" LCD	52-351-00084902	1
21	6118_LCD_Holder	20-029-03001264	1

Front panel



No.	Name	P/N No.	Qt'y
22	6118 Front PANEL	20-003-01091264	1
23	Protech Label	34-017-02104009	1
24	HDD Label	34-017-02101009	1
25	Power Label	34-017-02103009	1

TECHNICAL SUMMARY

APPENDIX

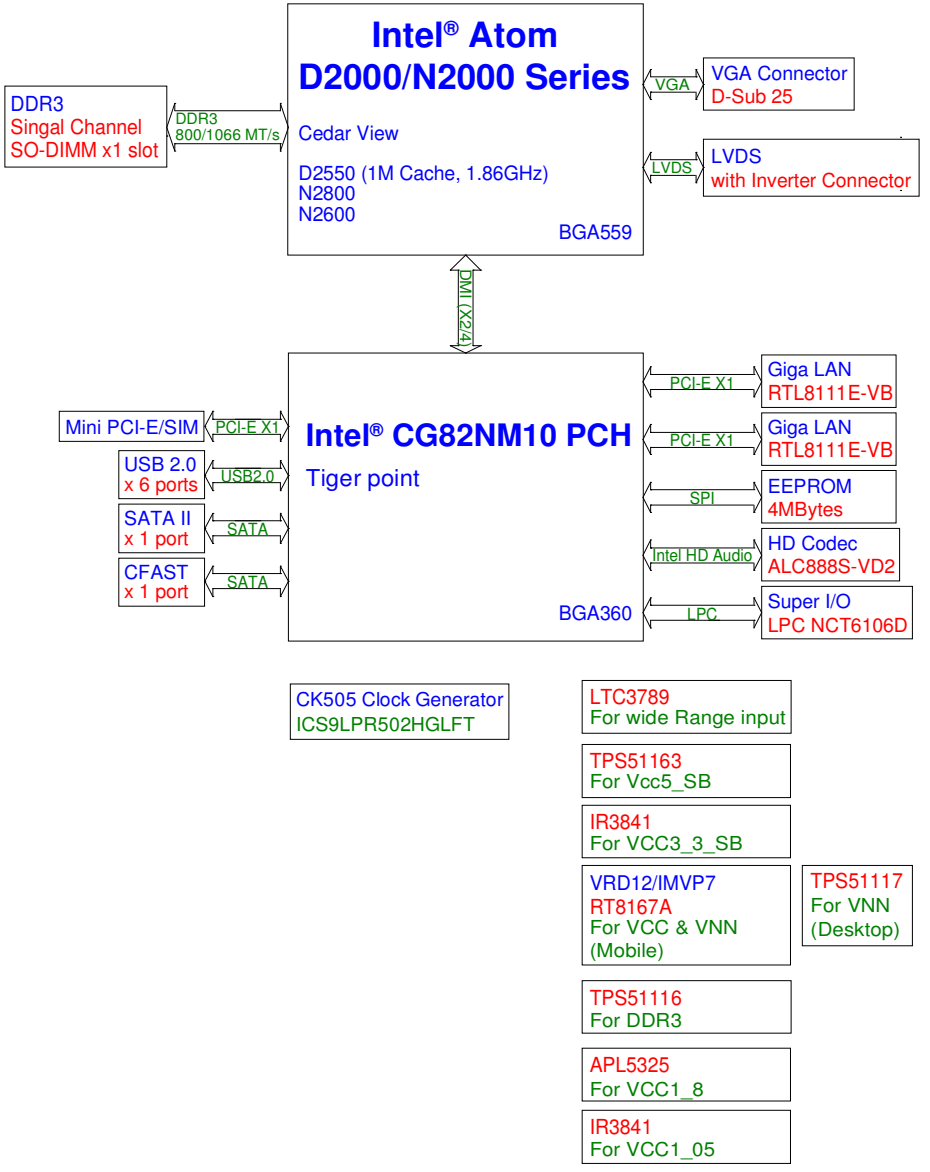
B

This section introduces you the maps concisely.

Section includes:

- Block Diagram
- Interrupt Map
- DMA Channels Map
- I/O Map
- Memory Map
- Watchdog Timer Configuration
- Flash BIOS Update

BLOCK DIAGRAM



INTERRUPT MAP

IRQ	ASSIGNMENT
0	System timer
3	Communications Port (COM2)
4	Communications Port (COM1)
6	Communications Port (COM3)
7	Communications Port (COM4)
8	System CMOS/real time clock
10	Intel® N10/ICH7 Family SMBus Controller - 27DA
13	Numeric data processor
16	Intel® N10/ICH7 Family USB Universal Host Controller - 27CB
16	Intel® N10/ICH7 Family PCI Express Root Port - 27D0
17	Intel® N10/ICH7 Family PCI Express Root Port - 27D2
18	Intel® N10/ICH7 Family USB Universal Host Controller - 27CA
19	Intel® N10/ICH7 Family USB Universal Host Controller - 27C9
19	Standard AHCI 1.0 Serial ATA Controller
22	High Definition Audio Controller
23	Intel® N10/ICH7 Family USB Universal Host Controller - 27C8
23	Intel® N10/ICH7 Family USB2 Enhanced Host Controller - 27CC
81	Microsoft ACPI-Compliant System
82	Microsoft ACPI-Compliant System
83	Microsoft ACPI-Compliant System
84	Microsoft ACPI-Compliant System
85	Microsoft ACPI-Compliant System
86	Microsoft ACPI-Compliant System
87	Microsoft ACPI-Compliant System
88	Microsoft ACPI-Compliant System
89	Microsoft ACPI-Compliant System
90	Microsoft ACPI-Compliant System
91	Microsoft ACPI-Compliant System
92	Microsoft ACPI-Compliant System
93	Microsoft ACPI-Compliant System

IRQ	ASSIGNMENT
94	Microsoft ACPI-Compliant System
95	Microsoft ACPI-Compliant System
96	Microsoft ACPI-Compliant System
97	Microsoft ACPI-Compliant System
98	Microsoft ACPI-Compliant System
99	Microsoft ACPI-Compliant System
100	Microsoft ACPI-Compliant System
101	Microsoft ACPI-Compliant System
102	Microsoft ACPI-Compliant System
103	Microsoft ACPI-Compliant System
104	Microsoft ACPI-Compliant System
105	Microsoft ACPI-Compliant System
106	Microsoft ACPI-Compliant System
107	Microsoft ACPI-Compliant System
108	Microsoft ACPI-Compliant System
109	Microsoft ACPI-Compliant System
110	Microsoft ACPI-Compliant System
111	Microsoft ACPI-Compliant System
112	Microsoft ACPI-Compliant System
113	Microsoft ACPI-Compliant System
114	Microsoft ACPI-Compliant System
115	Microsoft ACPI-Compliant System
116	Microsoft ACPI-Compliant System
117	Microsoft ACPI-Compliant System
118	Microsoft ACPI-Compliant System
119	Microsoft ACPI-Compliant System
120	Microsoft ACPI-Compliant System
121	Microsoft ACPI-Compliant System
122	Microsoft ACPI-Compliant System
123	Microsoft ACPI-Compliant System
124	Microsoft ACPI-Compliant System
125	Microsoft ACPI-Compliant System

IRQ	ASSIGNMENT
126	Microsoft ACPI-Compliant System
127	Microsoft ACPI-Compliant System
128	Microsoft ACPI-Compliant System
129	Microsoft ACPI-Compliant System
130	Microsoft ACPI-Compliant System
131	Microsoft ACPI-Compliant System
132	Microsoft ACPI-Compliant System
133	Microsoft ACPI-Compliant System
134	Microsoft ACPI-Compliant System
135	Microsoft ACPI-Compliant System
136	Microsoft ACPI-Compliant System
137	Microsoft ACPI-Compliant System
138	Microsoft ACPI-Compliant System
139	Microsoft ACPI-Compliant System
140	Microsoft ACPI-Compliant System
141	Microsoft ACPI-Compliant System
142	Microsoft ACPI-Compliant System
143	Microsoft ACPI-Compliant System
144	Microsoft ACPI-Compliant System
145	Microsoft ACPI-Compliant System
146	Microsoft ACPI-Compliant System
147	Microsoft ACPI-Compliant System
148	Microsoft ACPI-Compliant System
149	Microsoft ACPI-Compliant System
150	Microsoft ACPI-Compliant System
151	Microsoft ACPI-Compliant System
152	Microsoft ACPI-Compliant System
153	Microsoft ACPI-Compliant System
154	Microsoft ACPI-Compliant System
155	Microsoft ACPI-Compliant System
156	Microsoft ACPI-Compliant System
157	Microsoft ACPI-Compliant System

IRQ	ASSIGNMENT
158	Microsoft ACPI-Compliant System
159	Microsoft ACPI-Compliant System
160	Microsoft ACPI-Compliant System
161	Microsoft ACPI-Compliant System
162	Microsoft ACPI-Compliant System
163	Microsoft ACPI-Compliant System
164	Microsoft ACPI-Compliant System
165	Microsoft ACPI-Compliant System
166	Microsoft ACPI-Compliant System
167	Microsoft ACPI-Compliant System
168	Microsoft ACPI-Compliant System
169	Microsoft ACPI-Compliant System
170	Microsoft ACPI-Compliant System
171	Microsoft ACPI-Compliant System
172	Microsoft ACPI-Compliant System
173	Microsoft ACPI-Compliant System
174	Microsoft ACPI-Compliant System
175	Microsoft ACPI-Compliant System
176	Microsoft ACPI-Compliant System
177	Microsoft ACPI-Compliant System
178	Microsoft ACPI-Compliant System
179	Microsoft ACPI-Compliant System
180	Microsoft ACPI-Compliant System
181	Microsoft ACPI-Compliant System
182	Microsoft ACPI-Compliant System
183	Microsoft ACPI-Compliant System
184	Microsoft ACPI-Compliant System
185	Microsoft ACPI-Compliant System
186	Microsoft ACPI-Compliant System
187	Microsoft ACPI-Compliant System
188	Microsoft ACPI-Compliant System
189	Microsoft ACPI-Compliant System

IRQ	ASSIGNMENT
190	Microsoft ACPI-Compliant System
4294967292	Realtek PCIe GBE Family Controller #2
4294967293	Realtek PCIe GBE Family Controller
4294967294	Intel® Graphics Media Accelerator 3600 Series

Note: The resource information is gathered by Windows 7 (the IRQ could be assigned differently depending on your OS).

DMA CHANNELS MAP

TIMER CHANNEL	ASSIGNMENT
Channel 4	Direct memory access controller

I/O MAP

I/O MAP	ASSIGNMENT
0x00000000-0x0000001F	Direct memory access controller
0x00000000-0x0000001F	PCI bus
0x00000010-0x0000001F	Motherboard resources
0x00000020-0x00000021	Programmable interrupt controller
0x00000022-0x0000003F	Motherboard resources
0x00000024-0x00000025	Programmable interrupt controller
0x00000028-0x00000029	Programmable interrupt controller
0x0000002C-0x0000002D	Programmable interrupt controller
0x0000002E-0x0000002F	Motherboard resources
0x00000030-0x00000031	Programmable interrupt controller
0x00000034-0x00000035	Programmable interrupt controller
0x00000038-0x00000039	Programmable interrupt controller
0x0000003C-0x0000003D	Programmable interrupt controller
0x00000040-0x00000043	System timer
0x00000044-0x0000005F	Motherboard resources
0x0000004E-0x0000004F	Motherboard resources
0x00000050-0x00000053	System timer
0x00000061-0x00000061	Motherboard resources
0x00000062-0x00000063	Motherboard resources
0x00000063-0x00000063	Motherboard resources
0x00000065-0x0000006F	Motherboard resources
0x00000065-0x0000006F	Motherboard resources
0x00000067-0x00000067	Motherboard resources
0x00000070-0x00000077	System CMOS/real time clock
0x00000070-0x00000077	Motherboard resources
0x00000072-0x0000007F	Motherboard resources
0x00000080-0x00000080	Motherboard resources
0x00000080-0x00000080	Motherboard resources
0x00000081-0x00000091	Direct memory access controller
0x00000084-0x00000086	Motherboard resources

I/O MAP	ASSIGNMENT
0x00000088-0x00000088	Motherboard resources
0x0000008C-0x0000008E	Motherboard resources
0x00000090-0x0000009F	Motherboard resources
0x00000092-0x00000092	Motherboard resources
0x00000093-0x0000009F	Direct memory access controller
0x000000A0-0x000000A1	Programmable interrupt controller
0x000000A2-0x000000BF	Motherboard resources
0x000000A4-0x000000A5	Programmable interrupt controller
0x000000A8-0x000000A9	Programmable interrupt controller
0x000000AC-0x000000AD	Programmable interrupt controller
0x000000B0-0x000000B1	Programmable interrupt controller
0x000000B2-0x000000B3	Motherboard resources
0x000000B4-0x000000B5	Programmable interrupt controller
0x000000B8-0x000000B9	Programmable interrupt controller
0x000000BC-0x000000BD	Programmable interrupt controller
0x000000C0-0x000000DF	Direct memory access controller
0x000000E0-0x000000EF	Motherboard resources
0x000000F0-0x000000F0	Numeric data processor
0x00000290-0x0000029F	Motherboard resources
0x000002A0-0x000002AF	Motherboard resources
0x000002E8-0x000002EF	Communications Port (COM4)
0x000002F8-0x000002FF	Communications Port (COM2)
0x000003B0-0x000003BB	Intel® Graphics Media Accelerator 3600 Series
0x000003C0-0x000003DF	Intel® Graphics Media Accelerator 3600 Series
0x000003E8-0x000003EF	Communications Port (COM3)
0x000003F8-0x000003FF	Communications Port (COM1)
0x00000400-0x0000047F	Motherboard resources
0x00000400-0x0000047F	Motherboard resources
0x000004D0-0x000004D1	Programmable interrupt controller
0x000004D0-0x000004D1	Motherboard resources
0x00000500-0x0000057F	Motherboard resources
0x00000500-0x0000057F	Motherboard resources

I/O MAP	ASSIGNMENT
0x00000600-0x0000061F	Motherboard resources
0x00000680-0x0000069F	Motherboard resources
0x000006A0-0x000006AF	Motherboard resources
0x000006B0-0x000006EF	Motherboard resources
0x00000D00-0x0000FFFF	PCI bus
0x00001000-0x0000100F	Motherboard resources
0x0000D000-0x0000D0FF	Realtek PCIe GBE Family Controller #2
0x0000D000-0x0000D0FF	Intel® N10/ICH7 Family PCI Express Root Port - 27D2
0x0000E000-0x0000E0FF	Realtek PCIe GBE Family Controller
0x0000E000-0x0000E0FF	Intel® N10/ICH7 Family PCI Express Root Port - 27D0
0x0000F000-0x0000F01F	Intel® N10/ICH7 Family SMBus Controller - 27DA
0x0000F020-0x0000F02F	Standard AHCI 1.0 Serial ATA Controller
0x0000F040-0x0000F05F	Intel® N10/ICH7 Family USB Universal Host Controller - 27CB
0x0000F060-0x0000F07F	Intel® N10/ICH7 Family USB Universal Host Controller - 27CA
0x0000F080-0x0000F09F	Intel® N10/ICH7 Family USB Universal Host Controller - 27C9
0x0000F0A0-0x0000F0BF	Intel® N10/ICH7 Family USB Universal Host Controller - 27C8
0x0000F0C0-0x0000F0C3	Standard AHCI 1.0 Serial ATA Controller
0x0000F0D0-0x0000F0D7	Standard AHCI 1.0 Serial ATA Controller
0x0000F0E0-0x0000F0E3	Standard AHCI 1.0 Serial ATA Controller
0x0000F0F0-0x0000F0F7	Standard AHCI 1.0 Serial ATA Controller
0x0000F100-0x0000F107	Intel® Graphics Media Accelerator 3600 Series
0x0000FFFF-0x0000FFFF	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources

MEMORY MAP

MEMORY MAP	ASSIGNMENT
0xFF000000-0xFFFFFFFF	Intel® 82802 Firmware Hub Device
0xFF000000-0xFFFFFFFF	Intel® 82802 Firmware Hub Device
0xDFE04000-0xDFE04FFF	Realtek PCIe GBE Family Controller
0xDFE00000-0xDFE03FFF	Realtek PCIe GBE Family Controller
0xDFE00000-0xDFE03FFF	Intel® N10/ICH7 Family PCI Express Root Port - 27D0
0xFED00000-0xFED003FF	High precision event timer
0xDFD04000-0xDFD04FFF	Realtek PCIe GBE Family Controller #2
0xDFD00000-0xDFD03FFF	Realtek PCIe GBE Family Controller #2
0xDFD00000-0xDFD03FFF	Intel® N10/ICH7 Family PCI Express Root Port - 27D2
0xDFC00000-0xDFCFFFFF	Intel® Graphics Media Accelerator 3600 Series
0x80000000-0xFEBFFFFF	PCI bus
0xDFF05000-0xDFF053FF	Intel® N10/ICH7 Family USB2 Enhanced Host Controller - 27CC
0xFED14000-0xFED19FFF	System board
0xE0000000-0xEFFFFFFF	System board
0xFED1C000-0xFED1FFFF	Motherboard resources
0xFED1C000-0xFED1FFFF	Motherboard resources
0x0000-0x3FFF	Motherboard resources
0x0000-0x3FFF	Motherboard resources
0x0000-0x3FFF	Motherboard resources
0xFED45000-0xFED8FFFF	Motherboard resources
0xFEC00000-0xFEC00FFF	Motherboard resources
0xFEE00000-0xFEE00FFF	Motherboard resources
0xFED20000-0xFED8FFFF	Motherboard resources
0xFFC00000-0xFFFFFFFF	Motherboard resources
0xDFF04000-0xDFF043FF	Standard AHCI 1.0 Serial ATA Controller
0xDFF00000-0xDFF03FFF	High Definition Audio Controller
0xA0000-0xBFFFF	Intel® Graphics Media Accelerator 3600 Series

MEMORY MAP	ASSIGNMENT
0xA0000-0xBFFFF	PCI bus
0xC0000-0xDFFFF	PCI bus
0xE0000-0xEFFFF	PCI bus
0xF0000-0xFFFFF	PCI bus
0x7F800000-0x7FFFFFFF	PCI bus

WATCHDOG TIMER CONFIGURATION

The I/O port address of the watchdog timer is 2E (hex) and 2F (hex). 2E (hex) is the address port. 2F (hex) is the data port. User must first assign the address of register by writing address value into address port 2E (hex), then write/read data to/from the assigned register through data port 2F (hex).

Configuration Sequence

To program NCT6106D configuration registers, the following configuration sequence must be followed:

(1) Enter the extended function mode

(2) Configure the configuration registers

(3) Exit the extended function mode

(1) Enter the extended function mode

To place the chip into the Extended Function Mode, two successive writes of 0x87 must be applied to Extended Function Enable Registers (EFERs, i.e. 2Eh or 4Eh).

(2) Configure the configuration registers

The chip selects the Logical Device and activates the desired Logical Devices through Extended Function Index Register (EFIR) and Extended Function Data Register (EFDR). The EFIR is located at the same address as the EFER, and the EFDR is located at address (EFIR+1). First, write the Logical Device Number (i.e. 0x07) to the EFIR and then write the number of the desired Logical Device to the EFDR. If accessing the Chip (Global) Control Registers, this step is not required. Secondly, write the address of the desired configuration register within the Logical Device to the EFIR and then write (or read) the desired configuration register through the EFDR.

(3) Exit the extended function mode

To exit the Extended Function Mode, writing 0xAA to the EFER is required. Once the chip exits the Extended Function Mode, it is in the normal running mode and is ready to enter the configuration mode.

Code example for watchdog timer

Enable and start watchdog timer, then set 30 seconds as the timeout interval.

```
------ Enter to extended function mode ------------------------------------------------------
Mov     dx,     2eh
Mov     al,     87h
Out     dx,     al
Out     dx,     al
------ Select Logical Device 8 of watchdog timer ----------------------------------------
Mov     al,     07h
Out     dx,     al
Inc     dx
Mov     al,     08h
Out     dx,     al
------ Set second as counting unit ---------------------------------------------------------
Dec     dx
Mov     al,     0f5h
Out     dx,     al
Inc     dx
In      al,     dx
And     al,     not 08h
Out     dx,     al
------ Set timeout interval as 30seconds and start counting ---------------------------------
Dec     dx
Mov     al,     0f6h
Out     dx,     al
Inc     dx
Mov     al,     30
Out     dx,     al
------ Exit the extended function mode ------------------------------------------------------
Dec     dx
Mov     al,     0aah
Out     dx,     al
```

FLASH BIOS UPDATE

I. Before system BIOS update

1. Prepare a bootable media (e.g. USB storage device) which can boot system to DOS prompt.
2. Download and save the BIOS file (e.g. 61280P01.rom) to the same folder as afudos utility.
3. Copy AMI flash utility – AFUDOS.exe (V3.04) into a bootable device

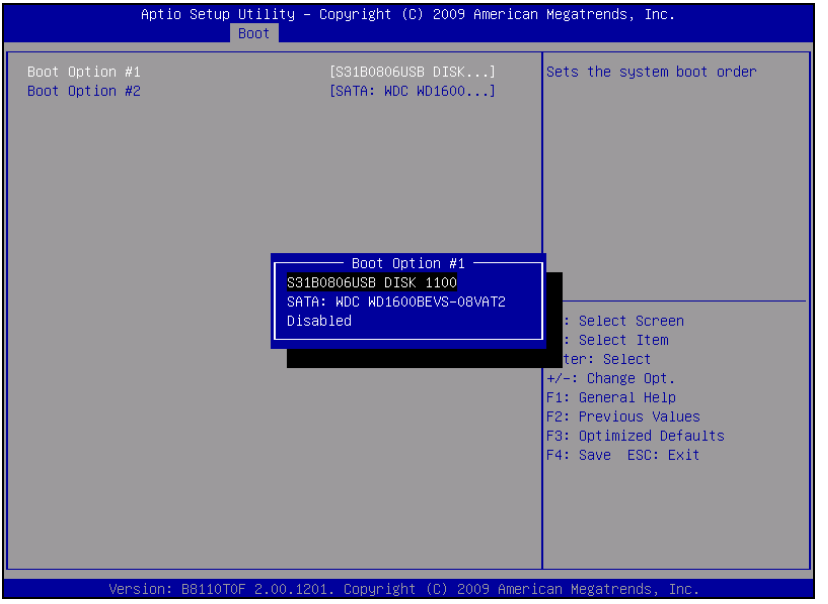
```
C:\AFUDOS>dir

 Volume in drive C has no label
 Volume serial Number is 0846-7844
 Directory of C:\AFUDOS

.              <DIR>         03-01-13  2:56p
..             <DIR>         03-01-13  2:56p
AFUDOS   EXE       167,152  11-12-12  3:12p
AFUDOS   TXT        11,357  11-16-12  1:49p
README   TXT         4,338  10-09-12  2:17p
AMI_AP~1 PDF       244,262  11-15-12  1:49p
61280P01 TXT     4,194,304  03-11-13  9:24a
         5 file(s)      4,621,413 bytes
         2 dir(s)   4,000,256,000 bytes free

C:\AFUDOS>
```

4. Make sure the target system can first boot to the bootable device.
   a. Connect the bootable USB device.
   b. Turn on the computer and press <ESC> or <Del> key during boot to enter BIOS setup menu.
   c. System will go into the BIOS setup menu.
   d. Select [Boot] menu as the picture shows below.
   e. Select [Hard Drive BBS Priorities], set the USB bootable device to be the 1st boot device.
   f. Press <F4> key to save configuration and exit the BIOS setup menu.


Aptio Setup Utility – Copyright (C) 2009 American Megatrends, Inc.
Boot
Boot Option #1
Boot Option #2
[S31B0806USB DISK...]
[SATA: WDC WD1600...]
Sets the system boot order
Boot Option #1
S31B0806USB DISK 1100
SATA: WDC WD1600BEVS-08VAT2
Disabled
: Select Screen
: Select Item
ter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save ESC: Exit
Version: B8110T0F 2.00.1201. Copyright (C) 2009 American Megatrends, Inc.

II. AFUDOS command for system BIOS update

AFUDOS.exe is aforementioned AMI firmware update utility; the command line is shown as below:

AFUDOS <ROM File Name> [option1] [option2]…

You can type AFUDOS /? to see all the definition of each control options. The recommended options for BIOS ROM update consist of following parameters:

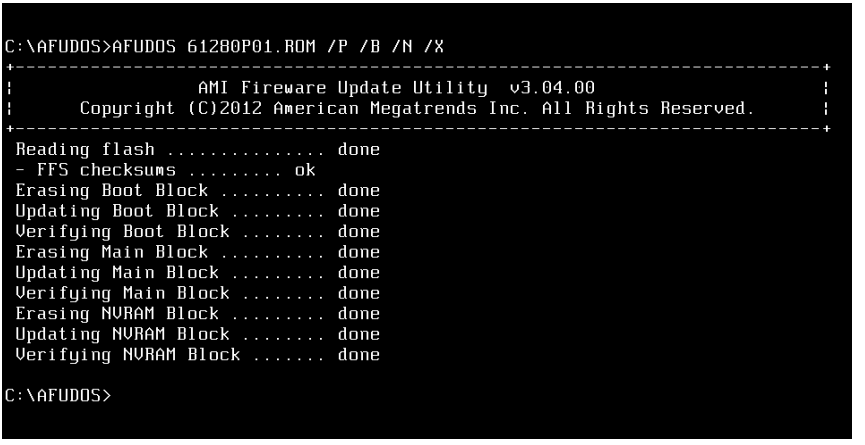
/P: program main BIOS image
/B: program Boot Block
/N: program NVRAM
/X: don't check ROM ID

III. BIOS update procedure

1. Use the bootable USB device to boot up system into the MS-DOS command prompt
2. Type in AFUDOS 612xxxxx.rom /p /b /n /x and press enter to start the flash procedure

   Note: xxxx means the BIOS revision part, ex. 0P01

3. During the update procedure, you will see the BIOS update process status and its percentage. Beware! Do not turn off system power or reset your computer if the whole procedure are not complete yet, or it may crash the BIOS ROM and make system unable to boot up next time.
4. After BIOS update procedures is complete, the messages from AFUDOS utility should be like the figure shown below.



5. You can restart the system and boot up with new BIOS now
6. Update is complete after restart
7. Verify the BIOS version on BIOS Setup screen.

   a. Turn on the computer and press <Esc> or <Del> key during boot to enter BIOS Setup.
   b. System will go into the BIOS setup menu.
   c. Select [Main] menu.
   d. Check the project version.


Aptio Setup Utility – Copyright (C) 2012 American Megatrends, Inc.
Main Advanced Chipset Boot Security Save & Exit
BIOS Information
BIOS Vendor American Megatrends
Core Version 4.6.5.3
Compliancy UEFI 2.3; PI 1.2
Project Version 61280P01
Build Date and Time 03/11/2013 09:20:24
System Language [English]
System Date [Tue 03/12/2013]
System Time [14:04:00]
Access Level Administrator
Choose the system default language
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.15.1226. Copyright (C) 2012 American Megatrends, Inc.