

USER MANUAL

BM-2505

**Mini-ITX Intel® 7th/6th Gen. Core™
i7/i5/i3/Pentium® / Celeron®
Processor**

BM-2505 M1

BM-2505

Mini-ITX Intel® 7th/6th Gen.

Core™ i7/i5/i3 Pentium® /

Celeron® Processor

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DISCLAIMER

This user's manual is meant to assist users in installing and setting up the system. The information contained in this document is subject to change without any notice.

CE NOTICE

This is a class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

FCC NOTICE

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

You are cautioned that any change or modifications to the equipment not expressly approve by the party responsible for compliance could void your authority to operate such equipment.



CAUTION: Danger of explosion may occur when the battery is incorrectly replaced. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.



WARNING: Some internal parts of the system may have high electrical voltage. We strongly recommend that only qualified engineers are allowed to service and disassemble the system. If any damages should occur on the system and are caused by unauthorized servicing, it will not be covered by the product warranty.

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Revision History

The revision history of BM-2505 User Manual is described below:

Version No.	Revision History	Page No.	Date
M1	Initial Release	-	2020/3/18

1

Introduction

This chapter provides the introduction for the BM-2505 system as well as the framework of the user manual.

The following topic is included:

- About This Manual

1.1 About This Manual

Thank you for purchasing our BM-2505 system. The BM-2505 provides faster processing speed, greater expandability and can handle more tasks than before. This manual is designed to assist you how to install and set up the whole system. It contains 5 chapters and 1 appendix. Users can configure the system according to their own needs. This user manual is intended for service personnel with strong hardware background. It is not intended for general users.

The following section outlines the structure of this user manual.

Chapter 1 Introduction

This chapter introduces you to the background of this manual.

Chapter 2 Getting Started

This chapter describes the package contents and outlines the system specifications. It also includes the physical illustrations for the BM-2505 system. Read the safety reminders carefully on how to take care of your system properly.

Chapter 3 System Configuration

This chapter outlines the locations of the motherboard components and their respective functions. You will learn how to set the jumpers and configure the system to meet your own needs.

Chapter 4 Software Utilities

This chapter contains helpful information for proper installations of the Intel Chipset Software Installation Utility, HD Graphics Driver Utility, LAN Driver Utility, Sound Driver Utility, Intel USB 3.0 Extensible Host Controller Driver Utility, Intel Management Engine Components Driver Installer, Intel Rapid Storage Technology (RST) Driver Utility and Microsoft Hotfix kb3211320 and kb3213986 Driver Installation.

Chapter 5 AMI BIOS Setup

This chapter indicates you how to change the BIOS configurations.

Appendix A Technical Summary

This appendix provides the information about BM-2505 block diagram, allocation maps for system resources, Watchdog Timer Configuration and Flash BIOS Update.

2

Getting Started

This chapter provides the information for the BM-2505 system. It describes how to set up the system quickly and outlines the system specifications.

The following topics are included:

- Package List
- BM-2505 Specification
- Safety Precautions

Experienced users can go to Chapter 3 System Configuration on page 3-1 for a quick start.

2.1 Package List

If you discover any of the items listed below are damaged or lost, please contact your local distributor immediately.

Item	Q'ty
BM-2505	1
Manual / Driver DVD	1
Quick Guide	1
Mini Jumper (2 mm)	6
SATA Cable (240 mm)	1
I/O Shield	1

2.3 BM-2505 Specifications

System	
CPU Support	➤ Intel®7th / 6th Gen. Core™ i7/i5/i3/ Pentium® / Celeron® processor (LGA1151)
Chipset	➤ Intel®H110 / Q170
Memory Support	➤ 2 x DDR4 SO-DIMM sockets (up to 32GB)
Power Supply	➤ ATX 24 + 4 Power Supply
Expansion Slots	➤ 1 x PCIe (x16) slot ➤ 1 x mini PCIe slot (half size)
SATA	➤ 2 x SATAIII ➤ RAID 0,1 for Q170 SKU
Operating System	➤ Win 10 IoT LTSC 2019 /Win 8.1 / Win 7 / Ubuntu 16.04
Dimensions	➤ 170mm x 170mm
Certificate	➤ FCC / CE
I/O Ports	
Display	➤ 2 x DVI-D ➤ 1 x DP (optional)
USB	➤ 4 x USB 3.0 ➤ 4 x USB 2.0
Audio	➤ Line In, Line Out, Mic In
LAN	➤ 2 x GbE LANs support Wake-On-LAN, PXE ➤ LAN1: Intel® I219 LM ➤ LAN2: Intel®I210 AT
Serial Port	➤ COM1 for RS-232/422/485 ➤ COM2/COM3/COM4/COM5/COM6 for RS-232 ➤ COM1/COM2 support +5V / +12V / RI selectable
Parallel Port	➤ 1 x parallel port
PS/2	➤ 1 x PS/2 (only key board signal)
Environment	
Operating Temperature	➤ 0°C ~ 60°C (32°F~ 140°F)
Storage Temperature	➤ -40°C ~ 85°C (-40°F~ 185°F)
Humidity	➤ 20%~ 90%

Note: Intel® 7th generation CPU is **NOT** compatible with Win7 / Win8.1.

2.4 Safety Precautions

Follow the instructions below to avoid your system from damages:

1. Keep your system away from static electricity on all occasions.
2. Prevent electric shock. Don't touch any components of this board when it is powered on. Always disconnect power when the system is not in use.
3. Disconnect power source when you change any hardware devices.
For instance, when you connect a jumper or install any cards, a surge of power may damage the electronic components or the whole system.

3

Hardware Configuration

This chapter contains helpful information about the external I/O Ports diagram, and jumper & connector settings, and component locations for the main board.

The following topics are included:

- Jumper & Connector Quick Reference Table
- Component Locations
- Configuration and Jumper Settings
- Connector Pin Assignments

3.1 JUMPER & CONNECTOR QUICK REFERENCE TABLE

JUMPER Description	NAME
Clear CMOS Data Selection	JCMOS1
COM1 Pin9 RI/5V/12V Selection	JP_COM1
COM2 Pin9 RI/5V/12V Selection	JP_COM2
COM4 Pin9 RI/5V/12V Selection	JP_COM4
VCCIO Voltage Selection	JP1
AT / ATX Mode Selection	JP2 & JP3
Intruder Detection Selection	JP4
BIOS Recovery Mode Selection	JP5
COM1 RS-232/422/485 Selection	JP6
SPI Override Protection Selection	JP9
COM1 RS-485 Auto Flow Selection	JP11
Keyboard / Mouse Mode Selection	JP13
Clear RTC Data Selection	JRTC1

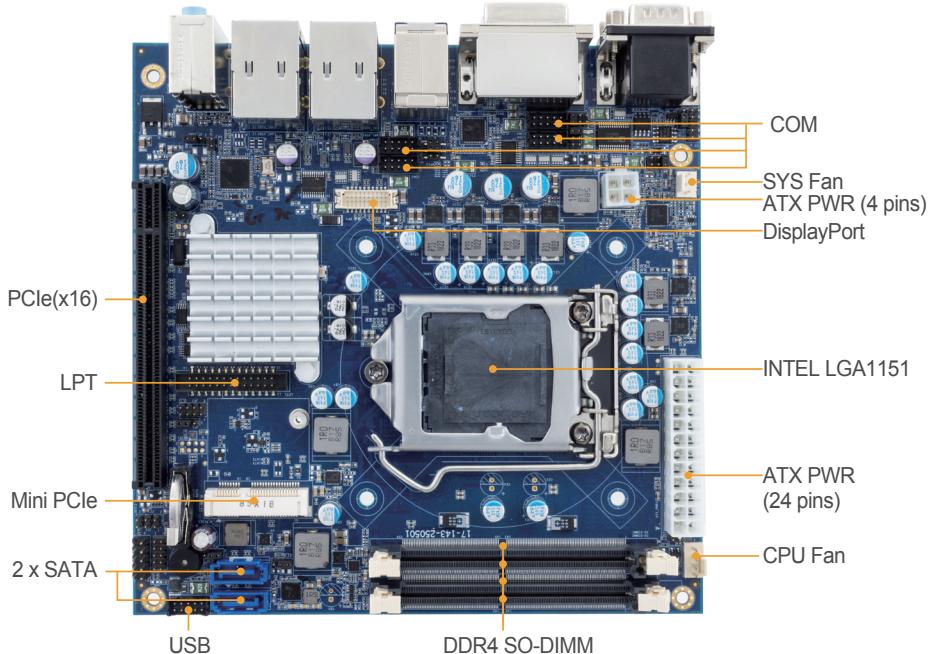
CONNECTOR Description	NAME
COM Ports	COM1, COM2
COM3, COM4 Pin Headers	COM3, COM4
(Optional) COM5, COM6 Pin Headers	COM5, COM6
2 x LAN Ports and 4 x USB 3.0 Ports	LAN1_USB1, LAN2_USB2
PS/2 Keyboard Connector & 2 x 1 USB 2.0 Connectors	JPS2_USB1
Internal USB 2.0 Connector	USB3
2 x DVI-D Connectors	DVI1
(Optional) DisplayPort Connector	DP1
HD Audio Connector	AUDIO1
(Optional) Printer Port (LPT) Connector	LPT1
Front Panel Connector	FP1
CPU Fan Connector	CPU_FAN1
System Fan Connector	SYS_FAN1
Half-Sized Mini PCI Express Slots	M_PCIE1
PCI Express Slots (PCIe (x16))	PCI_E1
SATA 3.0 Connectors	SATA1, SATA2

Chapter 3 Hardware Configuration

CONNECTOR Description	NAME
ATX Power Input Connectors	ATX_PWR1, ATX_PWR2
SPI Connector	JP7
Low Pin Count (LPC) Connector	JP12
DDR4 SO-DIMM memory socket	SO-DIMM1
DDR4 SO-DIMM memory socket	SO-DIMM2

3.2 COMPONENT LOCATIONS

3.2.1 Top View of BM-2505RA-N**



3.2.2 Jumper Settings of BM-2502RA-**N

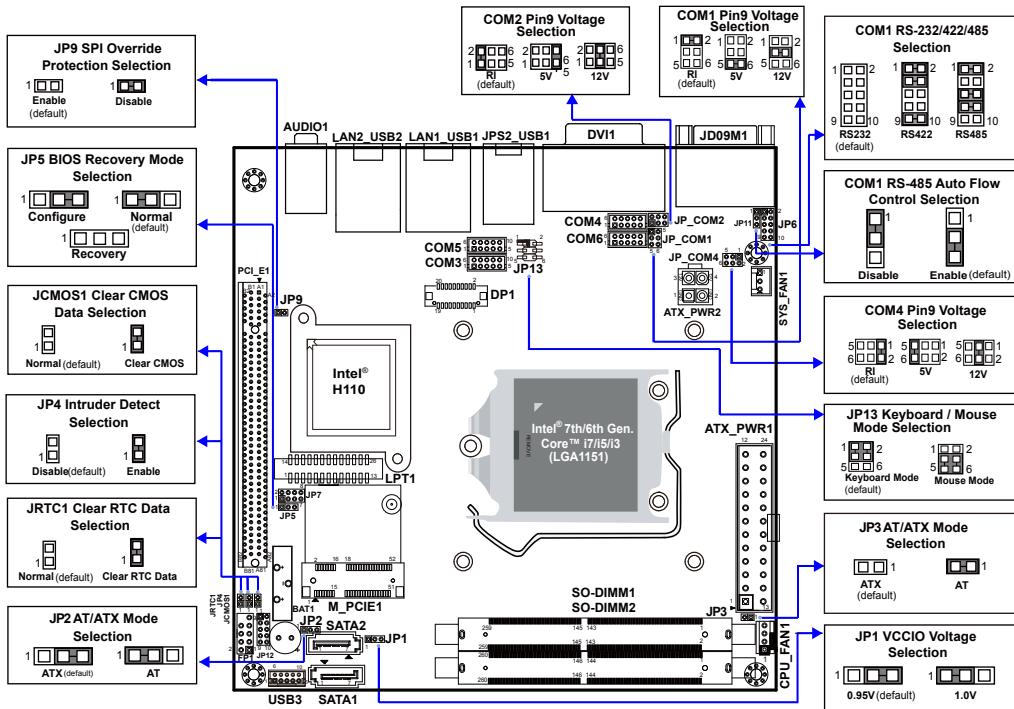


Figure 3-1. BM-2505 Main Board Component Location (Top View)

	WARNING: Always disconnect the power cord when you are working with connectors and jumpers on the main board. Make sure both the system and peripheral devices are turned OFF as sudden surge of power could damage sensitive components. Make sure BM-2505 is properly grounded.
	CAUTION: Observe precautions while handling electrostatic sensitive components. Make sure to ground yourself to prevent static charge while you are working on the connectors and jumpers. Use a grounding wrist strap and place all electronic components in any static-shielded devices.
	CAUTION: Always touch the motherboard components by the edges. Never touch components such as a processor by its pins. Take special care while you are holding electronic circuit boards by the edges only. Do not touch the main board components.

3.2.3 Bottom View of BM-2502RA-**N

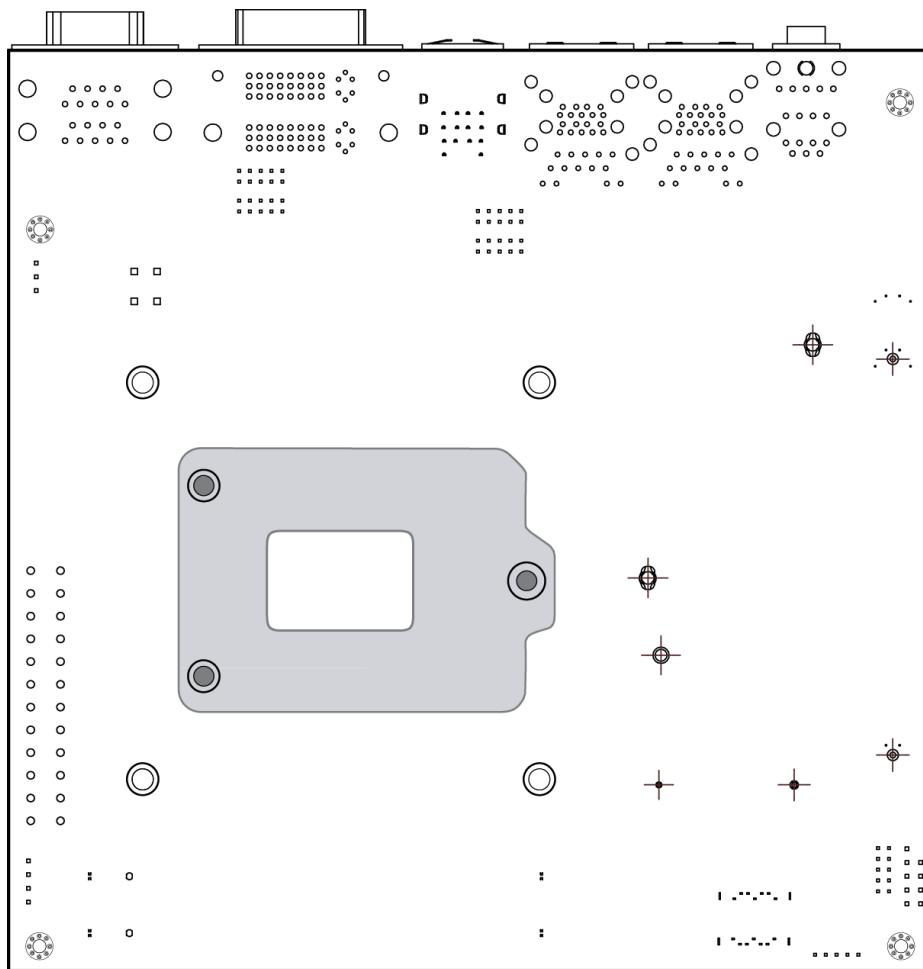
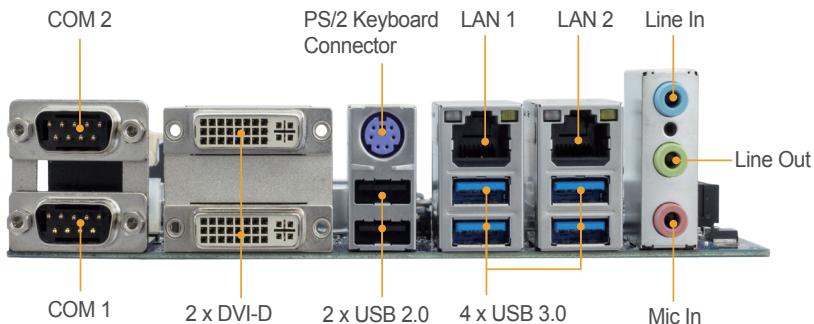


Figure 3-2. BM-2505 Main Board Component Location (Bottom View)

3.2.4 I/O View of BM-2505RA-N**

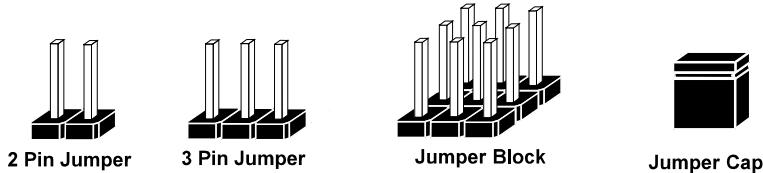


3.3 Setting Jumpers

You can configure your board by setting the jumpers. A jumper consists of two or three metal pins with a plastic base mounted on the card. By using a small plastic "cap", also known as the jumper cap (with a metal contact inside), you are able to connect the pins. So you can configure your hardware settings by "opening" or "closing" jumpers.

Jumpers can be combined into sets that are called jumper blocks. When jumpers are all in the block, you have to put them together to set up the hardware configuration. The figure below shows what this looks like.

Jumpers & Caps

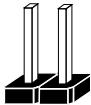


If a jumper has three pins, for example, labeled 1, 2 and 3. You can connect pins 1 and 2 to create one setting and shorting. You can also select to connect pins 2 and 3 to create another setting. The format of the jumper picture will be illustrated throughout this manual. The figure below shows different types of jumpers and jumper settings.

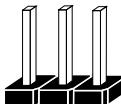
Jumper diagrams



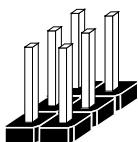
Jumper Cap looks like this



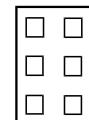
2 pin Jumper looks like this



3 pin Jumper looks like this



Jumper Block looks like this



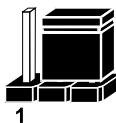
Jumper settings



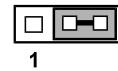
2 pin Jumper closed(enabled)
looks like this



1



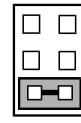
3 pin Jumper
2-3 pin closed(enabled)
looks like this



1



Jumper Block
1-2 pin closed(enabled)
looks like this



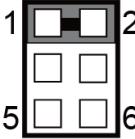
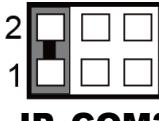
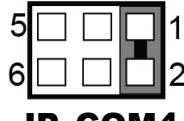
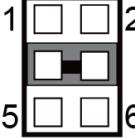
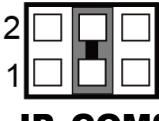
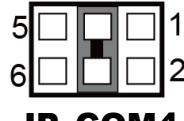
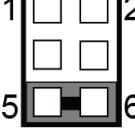
1 2

3.4 Setting Main Board Connectors and Jumpers

3.4.1 COM1, COM2, COM4 Port Pin9 Definition Selection Guide

Jumper Location: JP_COM1 & JP_COM2 & JP_COM4

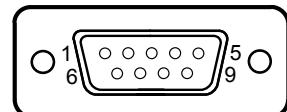
Description: COM1, COM2 and COM4 Port Pin9 RI/+5V/+12V Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION		
		JP_COM1	JP_COM2	JP_COM4
RI	1-2 <i>(Default Setting)</i>	 JP_COM1	 JP_COM2	 JP_COM4
12V	3-4	 JP_COM1	 JP_COM2	 JP_COM4
5V	5-6	 JP_COM1	 JP_COM2	 JP_COM4

3.4.2 COM Ports

COM1(RS-232/422/485) Connector Pin Assignment:

PIN	ASSIGNMENT		
	RS-232	RS-422	RS-485
1	DCD#	TX-	RS-485-
2	RX	TX+	RS-485+
3	TX	RX+	X
4	DTR#	RX-	X
5	GND	GND	GND
6	DSR#	X	X
7	RTS#	X	X
8	CTS#	X	X
9	RI#	X	X



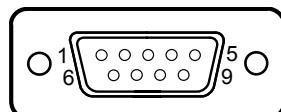
COM1

Notes:

1. COM1 is selectable as RS-232, RS-422, RS-485 by JP6.
2. Default setting is RS-232. Please see “**COM1 RS-232/422/485 Selection**” section for details.
3. COM1 Pin 9 is selectable for RI, +5V or +12V by jumper setting. Default setting is RI. Please see “**COM1, COM2, COM4 Port Pin9 Definition Selection Guide**” section for selection details.

COM2(RS-232) Connector Pin Assignment:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DCD#	6	DSR#
2	RX	7	RTS#
3	TX	8	CTS#
4	DTR#	9	RI#
5	GND	-	-



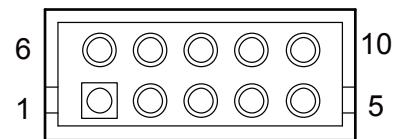
COM2

Note:

1. COM2 Pin 9 is selectable for RI, +5V or +12V by jumper setting. Default setting is RI. Please see “**COM1, COM2, COM4 Port Pin9 Definition Selection Guide**” section for selection details.

COM3(RS-232), COM4(RS-232) Connector Pin Header Pin Assignment:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DCD#	6	DSR#
2	RX	7	RTS#
3	TX	8	CTS#
4	DTR#	9	RI#
5	GND	10	NC



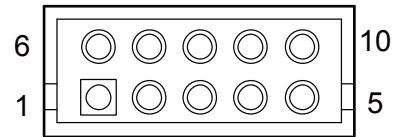
COM3/

COM4

Pin Header

COM5(RS-232), COM6 (RS-232) Connector Pin Header Pin Assignment:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DCD#	6	DSR#
2	RX	7	RTS#
3	TX	8	CTS#
4	DTR#	9	RI#
5	GND	10	NC



COM5/

COM6

Pin Header

3.4.3 COM1 RS-232/422/485 Selection

Jumper Location: JP6

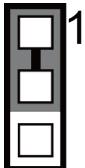
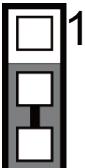
Description: COM1 RS-232/422/485 Selection

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
RS-232	<i>Open (Default Setting)</i>	 JP6
RS-422	1-2, 3-4, 9-10	 JP6
RS-485	1-2, 5-6, 7-8	 JP6

3.4.4 COM1 RS-485 Auto Flow Selection

Jumper Location: JP11

Description: COM1 RS-485 Auto Flow Selection

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
Disable	1-2	 JP11
Enable <i>(Default Setting)</i>	2-3	 JP11

3.4.5 LAN and Dual USB 3.0 Ports

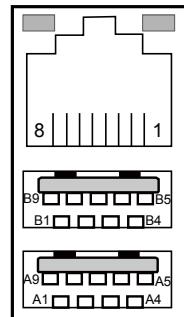
Port Name: LAN1_USB1 (rear I/O)

Description: LAN1 & Dual USB 3.0 Ports

LAN1 signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	MDI_0P	5	MDI_2N
2	MDI_0N	6	MDI_1N
3	MDI_1P	7	MDI_3P
4	MDI_2P	8	MDI_3N

Green/Orange Yellow



LAN LED Indicator:

Left Side LED

Green Color On	10/100Mbps LAN Speed Indicator
Orange Color On	Giga LAN Speed Indicator
Off	No LAN switch/hub connected

LAN1_USB1

Right Side LED

Yellow Color Blinking	LAN Message Active
Off	No LAN Message Active

USB 3.0 signals:

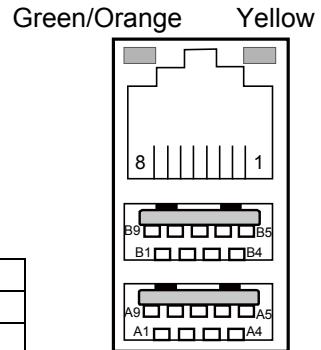
PIN	ASSIGNMENT	PIN	ASSIGNMENT
A1	VCC5	B1	VCC5
A2	USBP1N	B2	USBP2N
A3	USBP1P	B3	USBP2P
A4	GND	B4	GND
A5	RX1_DN	B5	RX2_DN
A6	RX1_DP	B6	RX2_DP
A7	GND	B7	GND
A8	TX1_DN	B8	TX2_DN
A9	TX1_DP	B9	TX2_DP

Port Name: LAN2_USB2 (rear I/O)

Description: LAN2 Port & Dual USB3.0 Ports

LAN2 signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	MDI_0P	5	MDI_2N
2	MDI_0N	6	MDI_1N
3	MDI_1P	7	MDI_3P
4	MDI_2P	8	MDI_3N



LAN LED Indicator:

Left Side LED

Green Color On	10/100Mbps LAN Speed Indicator
Orange Color On	Giga LAN Speed Indicator
Off	No LAN switch/hub connected

LAN2_USB2

Right Side LED

Yellow Color Blinking	LAN Message Active
Off	No LAN Message Active

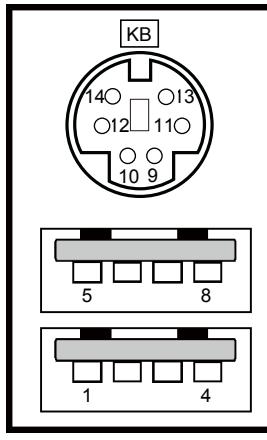
USB 3.0 signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
A1	VCC5	B1	VCC5
A2	USBP3N	B2	USBP4N
A3	USBP3P	B3	USBP4P
A4	GND	B4	GND
A5	RX3_DN	B5	RX4_DN
A6	RX3_DP	B6	RX4_DP
A7	GND	B7	GND
A8	TX3_DN	B8	TX4_DN
A9	TX3_DP	B9	TX4_DP

3.4.6 PS/2 Keyboard Connector and 2 x 1 USB 2.0 Connectors

Port Name: JPS2_USB1 (rear I/O)

Description: PS/2 Keyboard Connector and 2 x USB 2.0 Connectors



JPS2_USB1

Keyboard signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
14	NC	13	CLK
12	5V	11	GND
10	NC	9	DATA

USB 2.0 signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	VCC5	5	VCC5
2	USB_P6_DN	6	USB_P5_DN
3	USB_P6_DP	7	USB_P5_DP
4	GND	8	GND

3.4.7 Keyboard / Mouse Mode Selection

Jumper Location: JP13

Description: Keyboard / Mouse Mode Selection

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
Keyboard Mode	<i>1-3, 2-4 (Default Setting)</i>	 JP13
Mouse Mode	3-5, 4-6	 JP13

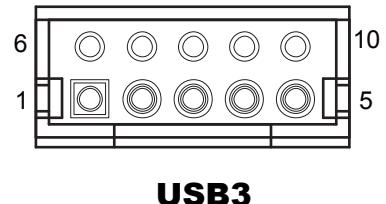
3.4.8 Internal USB 2.0 Connector

Connector Location: USB3

Description: Internal USB 2.0 Connector

USB 2.0 connector signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	USB_5_VCC5	6	USB_5_VCC5
2	USB2_P9_DN_R	7	USB2_P7_DN
3	USB2_P9_DP_R	8	USB2_P7_DP
4	GND	9	GND
5	GND	10	GND

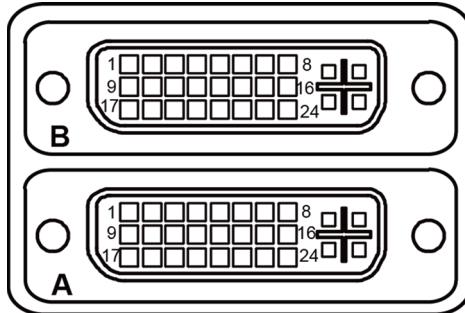


USB3

3.4.9 Dual DVI-D Ports

Port Name: DVI1 (rear I/O)

Description: 2 x DVI (Digital Visual Interface) Integrated Connectors
(Supports only DVI-D signals)



DVI1

DVI1 connector signals (A):

PIN	ASSIGNMENT	PIN	ASSIGNMENT
A1	DP_Data2-	A13	NC
A2	DP_Data2+	A14	+5V Power
A3	Ground	A15	Ground
A4	NC	A16	HOT Plug Detect
A5	NC	A17	DP_Data0-
A6	DP_Ctrl_Clock	A18	DP_Data0+
A7	DP_Ctrl_Data	A19	Ground
A8	CRT_VSYNC	A20	NC
A9	DP_Data1-	A21	NC
A10	DP_Data1+	A22	Ground
A11	Ground	A23	DP_Clock+
A12	NC	A24	DP_Clock-

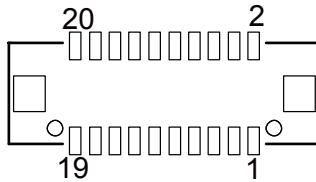
DVI1 connector signals (B):

PIN	ASSIGNMENT	PIN	ASSIGNMENT
B1	DP_Data2-	B13	NC
B2	DP_Data2+	B14	+5V Power
B3	Ground	B15	Ground
B4	NC	B16	HOT Plug Detect
B5	NC	B17	DP_Data0-
B6	DP_Ctrl_Clock	B18	DP_Data0+
B7	DP_Ctrl_Data	B19	Ground
B8	CRT_VSYNC	B20	NC
B9	DP_Data1-	B21	NC
B10	DP_Data1+	B22	Ground
B11	Ground	B23	DP_Clock+
B12	NC	B24	DP_Clock-

3.4.10 DisplayPort Connector (Optional)

Connector Location: DP1

Description: DisplayPort Connector



DP1 (optional)

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DP_C DATA0+	2	GND
3	DP_C DATA0-	4	DP_C DATA1+
5	GND	6	DP_C DATA1-
7	DP_C DATA2+	8	GND
9	DP_C DATA2-	10	DP_C DATA3+
11	GND	12	DP_C DATA3-
13	DP_C_AUX_ENJ	14	GND
15	DP_C_AUX+	16	DP_C_HPD
17	DP_C_AUX-	18	DP_VCC3_3
19	DP_VCC5	20	DP_VCC3_3

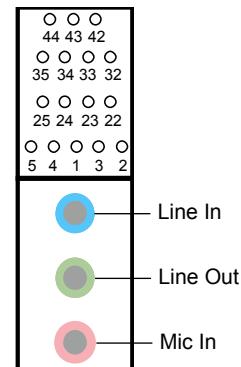
3.4.11 HD Audio Connector

Connector Location: AUDIO1

Description: HD Audio Connector for Line In/Line Out/Mic In.

Line In:

PIN	ASSIGNMENT
42	NC
43	NC
44	NC
32	HD LINE-IN-L
33	GND
34	GND
35	HD LINE-IN-R



Line Out:

PIN	ASSIGNMENT
22	LINE-OUT-L
23	GND
24	GND
25	LINE-OUT-R

AUDIO1

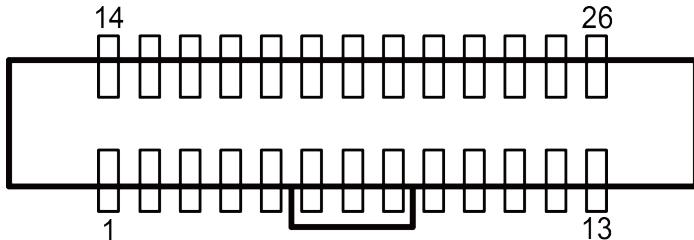
Mic In:

PIN	ASSIGNMENT
2	HD MIC1-L L
3	GND
1	GND
4	GND
5	HD MIC1-R L

3.4.12 Printer Port Connector (Optional)

Connector Location: LPT1

Description: Printer Port Connector



LPT1 (optional)

Printer Port (LPT1) signals:

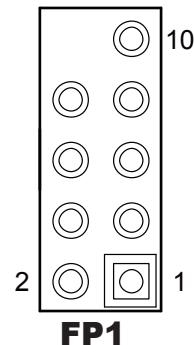
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	STRB#	14	AFD
2	D0	15	ERR#
3	D1	16	INIT
4	D2	17	SLIN
5	D3	18	GND
6	D4	19	GND
7	D5	20	GND
8	D6	21	GND
9	D7	22	GND
10	ACK#	23	GND
11	BUSY	24	GND
12	PE	25	GND
13	SLCT	26	GND

3.4.13 Front Panel Connector

Connector Location: FP1

Description: Front Panel Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	HDD+	2	PWR+
3	HDD-	4	PWR-
5	GND	6	Power Button
7	Reset Button	8	GND
9	5V	-	-



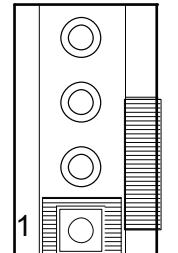
3.4.14 CPU Fan Connector

Connector Location: CPU_FAN1

Description: CPU Fan Connector

CPU Fan Connector (CPU_FAN1) signals:

PIN	ASSIGNMENT
1	GND
2	VCC12
3	TAC
4	CTL



CPU_FAN1

Notes:

1. CPU Fan speed mode can be set under BIOS.
2. Default BIOS setting is “Auto Duty-Cycle Mode”. Please see **Chapter 5** for more details.

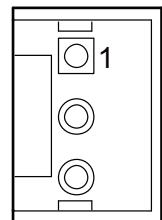
3.4.15 System Fan Connector

Connector Location: SYS_FAN1

Description: System Fan Connector

System Fan Connector (SYS_FAN1) signals:

PIN	ASSIGNMENT
1	GND
2	VCC12
3	NC



SYS_FAN1

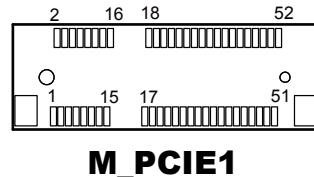
3.4.16 Half-Sized Mini PCI Express Slots

Connector Location: M_PCIE1

Description: Half-Sized Mini-PCI Express Slots

Mini-PCI Express Slot 1 (M_PCIE1) signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	WAKEJ	2	VCC 3_3 SB
3	NC	4	GND
5	NC	6	VCC1_5
7	CLKREQJ	8	NC
9	GND	10	NC
11	CLK_DN	12	NC
13	CLK_DP	14	NC
15	GND	16	NC
17	NC	18	GND
19	NC	20	NC
21	GND	22	PLTRSTJ_BUF
23	PCIE_RXN	24	VCC3_3_SB
25	PCIE_RXP	26	GND
27	GND	28	VCC1_5
29	GND	30	SMB_CLK
31	PCIE_TXN	32	SMB_DATA
33	PCIE_TXP	34	GND
35	GND	36	USBN
37	GND	38	USBP
39	VCC3_3_SB	40	GND
41	VCC3_3_SB	42	NC
43	GND	44	NC
45	NC	46	NC
47	NC	48	VCC1_5
49	NC	50	GND
51	NC	52	VCC3_3_SB



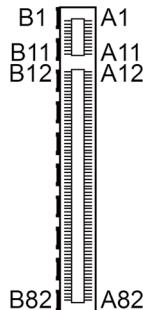
M_PCIE1

Mini PCI Express is the successor of the Mini PCI card and provides an increased data throughput. The cards have a detached network interface and are equipped with one lane. They are used in particular in embedded designs or compact box PCs.

3.4.17 PCI Express Slots

Connector Location: PCI_E1 (PCIE x16)

Description: PCI Express Slots



PCI_E1

PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT
B2	+ 12V	B1	+ 12V	A2	+ 12V	A1	PRSNT#1
B4	GND	B3	+ 12V	A4	GND	A3	+ 12V
B6	SMB DATA	B5	SMB_CLK	A6	NC	A5	NC
B8	+ 3.3V	B7	GND	A8	NC	A7	NC
B10	+ 3.3V_AUX	B9	NC	A10	+ 3.3V	A9	+ 3.3V
-	-	B11	WAKE#	-	-	A11	PERST#
B12	RSVD	B13	GND	A12	GND	A13	REFCLK+
B14	HSOP0	B15	HS0N0	A14	REFCLK-	A15	GND
B16	GND	B17	PRSNT#2	A16	HSIP0	A17	HSIN0
B18	GND	B19	HSOP1	A18	GND	A19	RSVD
B20	HS0N1	B21	GND	A20	GND	A21	HSIP1
B22	GND	B23	HSOP2	A22	HSIN1	A23	GND
B24	HS0N2	B25	GND	A24	GND	A25	HSIP2
B26	GND	B27	HSOP3	A26	HSIN2	A27	GND
B28	HS0N3	B29	GND	A28	GND	A29	HSIP3
B30	RSVD	B31	PRSNT#2	A30	HSIN3	A31	GND
B32	GND	B33	HSOP4	A32	RSVD	A33	RSVD
B34	HS0N4	B35	GND	A34	GND	A35	HSIP4
B36	GND	B37	HSOP5	A36	HSIN4	A37	GND
B38	HS0N5	B39	GND	A38	GND	A39	HSIP5
B40	GND	B41	HSOP6	A40	HSIN5	A41	GND
B42	HS0N6	B43	GND	A42	GND	A43	HSIP6
B44	GND	B45	HSOP7	A44	HSIN6	A45	GND
B46	HS0N7	B47	GND	A46	GND	A47	HSIP7
B48	PRSNT#2	B49	GND	A48	HSIN7	A49	GND
B50	HSOP8	B51	HS0N8	A50	RSVD	A51	GND
B52	GND	B53	GND	A52	HSIP8	A53	HSIN8
B54	HSOP9	B55	HS0N9	A54	GND	A55	GND
B56	GND	B57	GND	A56	HSIP9	A57	HSIN9
B58	HSOP10	B59	HS0N10	A58	GND	A59	GND
B60	GND	B61	GND	A60	HSIP10	A61	HSIN10

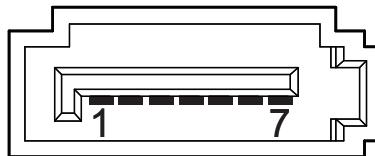
Chapter 3 Hardware Configuration

PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT
B62	HSOP11	B63	HSON11	A62	GND	A63	GND
B64	GND	B65	GND	A64	HSIP11	A65	HSIN11
B66	HSOP12	B67	HSON12	A66	GND	A67	GND
B68	GND	B69	GND	A68	HSIP12	A69	HSIN12
B70	HSOP13	B71	HSON13	A70	GND	A71	GND
B72	GND	B73	GND	A72	HSIP13	A73	HSIN13
B74	HSOP14	B75	HSIN14	A74	GND	A75	GND
B76	GND	B77	GND	A76	HSIP14	A77	HSIN14
B78	HSIP15	B79	HSIN15	A78	GND	A79	GND
B80	GND	B81	PRSNT#2	A80	HSIP15	A81	HSIN15
B82	RSVD	-	-	A82	GND	-	-

3.4.18 SATA 3.0 Connectors

Connector Location: SATA1

Description: Serial ATA (SATA) 6GB/s Connector



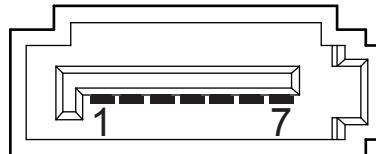
SATA1

Serial ATA 6GB/s Connector (SATA1) signals:

PIN	ASSIGNMENT
1	GND
2	TXPC
3	TXNC
4	GND
5	RXNC
6	RXPC
7	GND

Connector Location: SATA2

Description: Serial ATA (SATA) 6GB/s Connector



SATA2

Serial ATA 6GB/s Connector (SATA2) signals:

PIN	ASSIGNMENT
1	GND
2	TXPC
3	TXNC
4	GND
5	RXNC
6	RXPC
7	GND

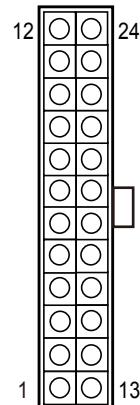
3.4.19 ATX Power Connectors

Connector Location: ATX_PWR1

Description: ATX Power Connector

ATX_PWR1 ATX Power Connector signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	+3.3V	13	+3.3V
2	+3.3V	14	12V
3	GND	15	GND
4	+5V	16	PSON
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	PWROK	20	-5V
9	5VSB	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	+3.3V	24	GND



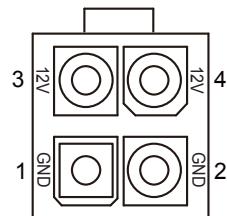
ATX_PWR1

Connector Location: ATX_PWR2

Description: DC Power Input Connector

ATX_PWR2 DC Power Input Connector signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	GND	3	+12V
2	GND	4	+12V



ATX_PWR2

3.4.20 AT / ATX Mode Selection

Jumper Location: JP2

Description: AT / ATX Mode Selection

SELECTION	JUMPTER SETTING	JUMPER ILLUSTRATION
AT	1-2	 JP2
ATX	<i>2-3 (Default Setting)</i>	 JP2

Jumper Location: JP3

Description: AT / ATX Mode Selection

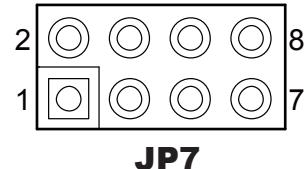
SELECTION	JUMPTER SETTING	JUMPER ILLUSTRATION
AT	Close	 JP3
ATX	<i>Open (Default Setting)</i>	 JP3

3.4.21 SPI Connector

Connector Location: JP7

Description: SPI (Serial Peripheral Interface Bus) Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	3.3V	2	GND
3	CSJ	4	CLK
5	MISO	6	MOSI
7	NC	8	NC



3.4.22 SPI Override Protection Selection

Jumper Location: JP9

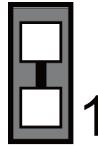
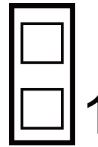
Description: SPI Override Protection Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
Enable	<i>Open (Default Setting)</i>	
Disable	Close	

3.4.23 Intruder Detection Selection

Jumper Location: JP4

Description: Intruder Detection Selection

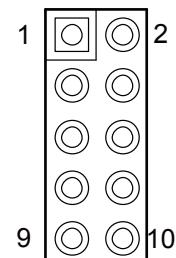
SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
Enable	Close	 JP4
Disable	<i>Open (Default Setting)</i>	 JP4

3.4.24 Low Pin Count (LPC) Connector

Connector Location: JP12

Description: Low Pin Count (LPC) Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	CLK	2	GND
3	FRAME#	4	GND
5	PLTRST#	6	LPC_AD0
7	LPC_AD3	8	LPC_AD2
9	3.3V	10	LPC_AD1

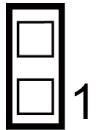


JP12

3.4.25 Clear RTC Data Selection

Jumper Location: JRTC1

Description: Clear RTC Data Selection

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
Normal	<i>Open (Default Setting)</i>	 JRTC1
Clear RTC Data	Close	 JRTC1

3.4.26 VCCIO Voltage Selection

Jumper Location: JP1

Description: VCCIO Voltage Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
0.95V	2-3 (Default Setting)	1  JP1
1.0V	1-2	1  JP1

3.4.27 BIOS Recovery Mode Selection

Jumper Location: JP5

Description: BIOS Recovery Mode Selection

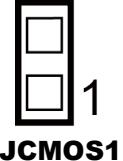
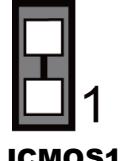
SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
Normal	<i>1-2 (Default Setting)</i>	 JP5
Configure	2-3	 JP5
Recovery	Open	 JP5

3.4.28 Clear CMOS Data Selection

Jumper Location: JCMOS1

Description: Clear CMOS Data Selection

- Step 1. Remove the main power of the PC.
- Step 2. Close **JCMOS1** (pins 1-2) for 6 seconds by a cap.
- Step 3. Remove the cap which is just used on **JCMOS1** (1-2), so that **JCMOS1** returns to “OPEN”.
- Step 4. Power on the PC and the PC will then auto-reboot for once in order to set SoC’s register.
- Step 5. Done!

SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
Normal	<i>Open (Default Setting)</i>	
Clear CMOS Data	Close	

Note: Please make sure the main power is off before you clear CMOS.

4

Software Utilities

This chapter provides the detailed information that guides users to install driver utilities for the system. The following topics are included:

- Installing Intel® Chipset Software Installation Utility
- Installing HD Graphics Driver Utility
- Installing LAN Driver Utility
- Installing Sound Driver Utility
- Installing USB 3.0 eXtensible Host Controller Utility
- Installing Intel® Management Engine Components Driver Installer
- Installing Intel® Rapid Storage Technology (RST) Driver Utility
- Installing Microsoft Hotfix kb3211320 and kb3213986 Driver Installation

4.1 Introduction

Enclosed with the BM-2505 Series package is our driver utilities contained in a DVD-ROM disk. Refer to the following table for driver locations:

Filename (Assume that DVD- ROM drive is D :)	Purpose
D:\DRIVER\Platform\Chipset\ Chipset_10.1.1.44 D:\DRIVER\Platform\Chipset\ WIN 10-2019\Windows 10 Enterprise 2019 LTSC_64bit	Intel® Chipset Device Software installer
D:\DRIVER\Platform\Graphics\ WIN7_8.1_10\GFX_win64_15. 45.20.64.4727 D:\DRIVER\Platform\Graphics\ WIN7_8.1_10\GFX_win32_15. 45.20.4727 D:\DRIVER\Platform\Graphics\ WIN10-2019\Windows 10 Enterprise 2019 LTSC_64bit	Intel(R) HD Graphics installer
D:\DRIVER\Platform\LAN\ WIN7\LAN_22_10 D:\DRIVER\Platform\LAN\ WIN10 D:\DRIVER\Platform\LAN\ WIN 10-2019\Windows 10 Enterprise 2019 LTSC_64bit	Intel(R) Network Connections Software
D:\DRIVER\Platform\Sound\ Realtek Audio for ALC888	Realtek High Definition Audio System Software
D:\DRIVER\Platform\USB3\ Windows 7 32-bit.64-bit	Intel(R) USB 3.0 eXtensible Host Controller

Filename (Assume that DVD- ROM drive is D :)	Purpose
D:\DRIVER\Platform\KMDF\ Windows 7 32-bit D:\DRIVER\Platform\KMDF\ Windows 7 64-bit	<i>Kernel-Mode Driver Framework</i>
D:\DRIVER\Platform\ME\ ME_Consumer_11.7.0.1068 (H110) D:\DRIVER\Platform\ME\ ME_Corporate_11.7.0.1068 (Q170)	Intel(R) Management Engine Components Driver Installer
D:\DRIVER\Platform\ME\ Microsoft .NET Framework4.5	Kernel-Mode Driver Framework
D:\DRIVER\Platform\RST\ Windows 7(32-bit) D:\Platform\RST\ Windows 7(64-bit) D:\DRIVER\Platform\RST\ Windows 8.1(64-bit) D:\DRIVER\Platform\RST\ Windows 10(64-bit)	Intel(R) Rapid Storage Technology
D:\ DRIVER\Platform\Hotfix	Microsoft Hotfix kb3211320 and kb3213986

Note: Install the driver utilities immediately after the OS installation is completed.

4.2 Installing Intel® Chipset Software Installation Utility

The Intel® Chipset Software Installation Utility installs the Windows *.INF files to the target system. These files outline to the operating system how to configure the Intel chipset components in order to ensure that the following functions work properly:

- Core PCI and ISAPNP Services
- PCIe Support
- SATA Storage Support
- USB Support
- Identification of Intel® Chipset Components in the Device Manager

The utility pack is to be installed only for Windows® 10 series, and it should be installed immediately after the OS installation is finished. Please follow the steps below:

- 1** Connect the USB DVD-ROM device to BM-2505 and insert the driver disk.
- 2** Enter the **Chipset** folder where the Chipset driver is located.
- 3** Select Windows 7 (32/64-bit) / Windows 8.1 (64-bit) / Windows 10 (64-bit) for your OS platform.
- 4** Click **SetupChipset.exe** file for Chipset driver installation.
- 5** Follow the on-screen instructions to install the driver.
- 6** Once the installation is completed, shut down the system and restart BM-2505 for the changes to take effect.

4.3 Installing Graphics Driver Utility

The graphics interface embedded in BM-2505 can support a wide range of display types. You can have dual displays via DVI-D ports and make the system work simultaneously.

To install the Graphics driver utility, follow the steps below:

- 1** Connect the USB DVD-ROM device to BM-2505 and insert the driver disk.
- 2** Enter the **Graphics** folder where the driver is located.
- 3** Select Windows 7 (32/64-bit) / Windows 8.1 (64-bit) or Windows 10 (64-bit) for your OS platform.
- 4** (1) Click **Setup.exe** file for Windows 7 (32/64-bit) / Windows 8.1 (64-bit) Graphics driver installation or
(2) Click **igxpin.exe** file for Windows 10 (64-bit) Graphics driver installation.
- 5** Follow the on-screen instructions to complete the installation.
- 6** Once the installation is completed, shut down the system and restart BM-2505 for the changes to take effect.

4.4 Installing LAN Driver Utility

Enhanced with LAN function, BM-2505 supports various network adapters. To install the LAN Driver, follow the steps below:

- 1** Connect the USB DVD-ROM device to BM-2505 and insert the driver disk.
- 2** Enter the **LAN** folder where the driver is located.
- 3** Select Windows 7 (32/64-bit) / Windows 8.1 (64-bit) / Windows 10 (64-bit) for your OS platform.
- 4** (1) Click **Autorun.exe** file for **Windows 7** Graphics driver installation or
(2) Click **PROWin32.exe & PROWinx64.exe** files for **Windows 10** Graphics driver installation or
(3) Click **e1d68x64.exe** files for **Windows 10-2019** (64-bit) Graphics driver installation.
- 5** Follow the on-screen instructions to complete the installation.
- 6** Once the installation is completed, shut down the system and restart BM-2505 for the changes to take effect.

For more details on the installation procedure, refer to the README.txt file that you can find on LAN Driver Utility.

4.5 Installing Sound Driver Utility

The sound function enhanced in this system is fully compatible with Windows® 7/8.1/10 series.

To install the Sound Driver, follow the steps below:

- 1** Connect the USB DVD-ROM device to BM-2505 and insert the driver disk.
- 2** Open the **Sound** folder where the driver is located.
- 3** Click **Audio_0007-Win7_Win8_Win81_Win10_R281.exe** file for Sound driver installation.
- 4** Follow the on-screen instructions to complete the installation.
- 5** Once the installation is completed, shut down the system and restart BM-2505 for the changes to take effect.

4.6 Intel® USB 3.0 eXtensible Host Controller Utility

Intel® USB 3.0 eXtensible Host Controller Driver supports the following Intel® Chipsets/Processors:

- Intel® 8 Series/C220 series Chipset Family
- Intel® 4th Generation Core™ Processors
- Intel® C610 series Chipset Family
- Intel® 9 Series Chipset Family
- Intel® Pentium® Processor or Intel® Celeron® Processor N- & J- Series
- Intel® 5th generation Intel® Core™ Processors
- Intel® Core™ M Processor
- Intel® 6th generation Intel® Core™ processors
- Intel® 100 Series Chipset Family

To install the utility, follow the steps below:

- 1** Connect the USB DVD-ROM device to BM-2505 and insert the driver disk.
- 2** Open the **USB3** folder where the driver is located.
- 3** Select Windows 7 (32/64-bit) for your OS platform.
- 4** Click **Setup.exe** file for driver installation.
- 5** Follow the on-screen instructions to complete the installation.
- 6** Once the installation is completed, shut down the system and restart BM-2505 for the changes to take effect.

4.7 Intel® Management Engine Components Driver Installer Installation

For Windows 7 only. Pre-install Microsoft's Kernel-Mode Driver Framework (KMDF) version 1.11 before you install the Intel® Management Engine Components Driver Installer (ME) in order to avoid errors in Device Manager.

Installation Instructions for Kernel-Mode Driver Framework (KMDF)

Follow the steps below to install the KMDF driver utility:

- 1** Insert the driver disk into a DVD-ROM device.
- 2** (For Windows 7 only) Enter the **KMDF** folder where the installation driver file is located.
- 3** Select Windows 7 (32/64bit) for your OS platform.
- 4** (1) Click **kmdf-1.11-Win-6.1-x86.exe** file for **Windows 7 (32bit)** driver installation.
(2) Click **kmdf-1.11-Win-6.1-x64.exe** file for **Windows 7 (64bit)** driver installation.

Installation Instructions for Intel® Management Engine Components Driver Installer

- 1** Connect the USB DVD-ROM device to BM-2505 and insert the driver disk.
- 2** Enter the **ME** folder where the driver is located.
- 3** Select the driver folder for H110 / Q170 SKU.
- 4** Click **SetupME.exe** file for ME driver installation.
- 5** Follow the on-screen instructions to complete the installation.
- 6** Once the installation is completed, shut down the system and restart BM-2505 for the changes to take effect.

4.8 Installing Intel Rapid Storage Technology Driver Utility

The Intel® Rapid Storage Technology (Intel® RST) driver supports RAID 0, 1 in Q170 SKU for 2 SATAIII HDD/SSD. To install the RAID/RST driver utility, follow the steps below:

- 1** Connect the USB DVD-ROM device to BM-2505 and insert the driver disk.
- 2** Enter the **RST** folder where the driver is located.
- 3** Select Windows 7 (32/64-bit) / Windows 8.1 (64-bit) / Windows 10 (64-bit) for your OS platform.
- 4** Click **SetupRST.exe** driver installation file for driver installation.
- 5** Follow the on-screen instructions to complete the installation.
- 6** Once the installation is completed, shut down the system and restart BM-2505 for the changes to take effect.

Note: The RAID driver utility is not supported for H110 SKU.

Intel® RapidStorage Technology Option ROM

The Intel® Rapid Storage Technology option ROM provides the following:

- Pre-operating system user interface for RAID volume management
- Ability to create, delete and reset RAID volumes
- RAID recovery

Accessing Intel® Rapid Storage Technology Option ROM User Interface

To enter the Intel® Rapid Storage Technology option ROM user interface, press **Ctrl-I** when prompted during the Power-On Self-Test (POST).

Option ROM prompt:

```
Intel(R) Rapid Storage Technology - Option ROM - 10.5.0.1034
Copyright(C) 2003-11 Intel Corporation. All Rights Reserved.

RAID Volumes:
None defined.

Physical Devices:
Port Device Model      Serial #          Size Type/Status(Vol ID)
2   WDC WD1600AAJS-7  WD-WMAP9D045721    149.0GB Non-RAID Disk
3   WDC WD1600AAJS-7  WD-WMAP9D046479    149.0GB Non-RAID Disk
Press <CTRL-I> to enter Configuration Utility...
```

In the user interface, the hard drive(s) and hard drive information listed for your system will differ from the example in the figure below:

Option ROM user interface:



4.9 Installing Microsoft Hotfix kb3211320 and kb3213986 Driver Installation

The Microsoft Hotfix kb3211320 and kb3213986 Driver that needs to be installed depends on the system's specific hardware and firmware features. The installer, compatible with Windows 10, detects the system's capabilities and installs the relevant drivers and applications.

To install the utility, simply follow the steps below:

- 1** Connect the USB DVD-ROM device to BM-2505 and insert the driver disk.
- 2** Enter the **Hotfix** folder where the driver is located.
- 3** Click **windows10.0-kb3211320x64** and **windows10.0-kb3213986x64** files for critical security update.
- 4** Follow the on-screen instructions to complete the installation.
- 5** Once the installation is completed, shut down the system and restart BM-2505 for the changes to take effect.

5

BIOS SETUP

This chapter guides users how to configure the basic system configurations via the BIOS Setup Utilities. The information of the system configuration is saved in BIOS NVRAM so that the Setup information is retained when the system is powered off. The BIOS Setup Utilities consist of the following menu items:

- Main Menu
- Advanced Menu
- Chipset Menu
- Security Menu
- Boot Menu
- Save & Exit Menu

5.1 Introduction

The board BM-2505 uses an AMI (American Megatrends Incorporated) Aptio BIOS that is stored in the Serial Peripheral Interface Flash Memory (SPI Flash) and can be updated. The SPI Flash contains the built-in BIOS setup program, Power-On Self-Test (POST), PCI auto-configuration utility, LAN EEPROM information, and Plug and Play support.

Aptio is AMI's BIOS firmware based on the UEFI (Unified Extensible Firmware Interface) specifications and the Intel Platform Innovation Framework for EFI. The UEFI specification defines an interface between the operating system and platform firmware. The interface consists of data tables that contain platform-related information, boot service calls, and runtime service calls that are available to the operating system and its loader. These elements have combined to provide a standard environment for booting the operating system and running pre-boot applications.

The diagram below shows the Extensible Firmware Interface's location in the software stack.

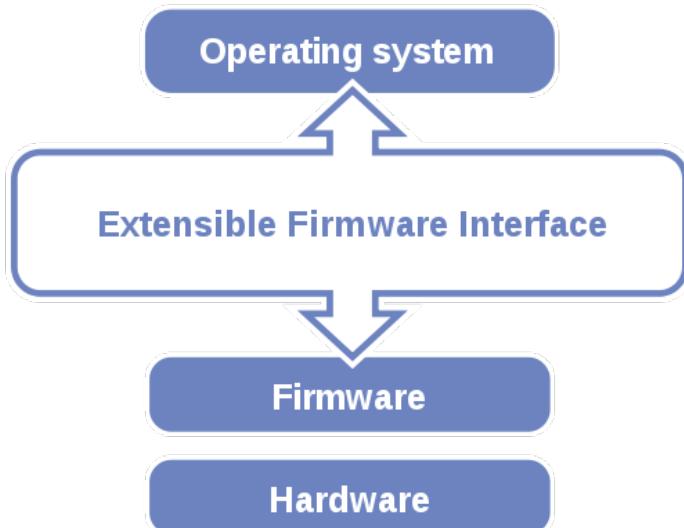


Figure 5-1. Extensible Firmware Interface Diagram

EFI BIOS provides an user interface that allows you to modify hardware configuration, e.g. change the system date and time, enable/disable a system component, determine bootable device priority, set up personal password, etc., which is convenient for engineers to perform modifications and customize the computer system and allows technicians to troubleshoot the occurred errors when the hardware is faulty.

The BIOS setup menu allows users to view and modify the BIOS settings for the computer. After the system is powered on, users can access the BIOS setup menu by pressing or <Esc> immediately while the POST message is running before the operating system is loading.

All the menu settings are described in details in this chapter.

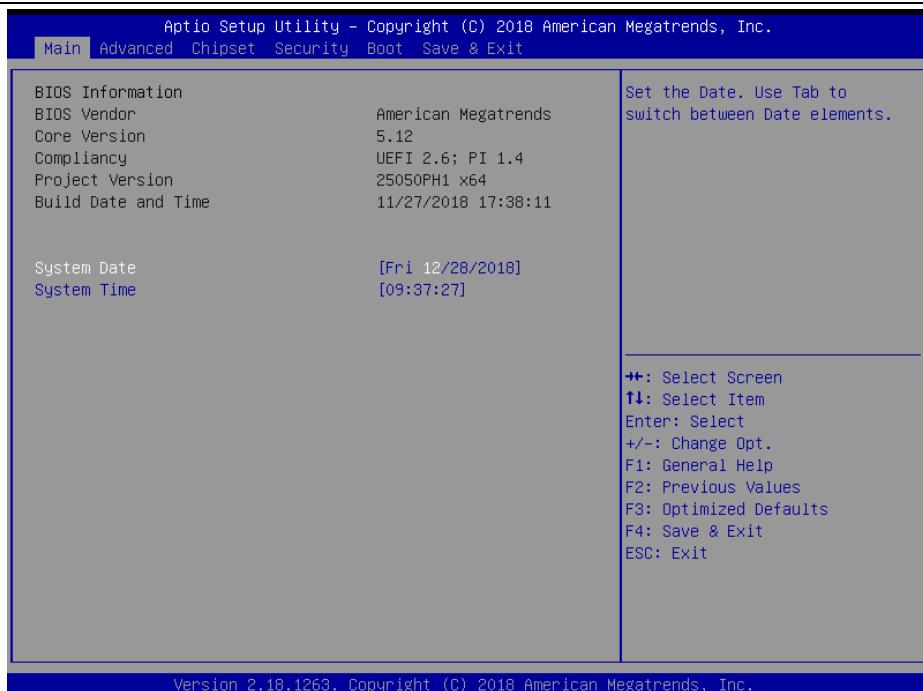
5.2 Accessing Setup Utility

After the system is powered on, BIOS will enter the Power-On Self-Test (POST) routines and the POST message will be displayed:



Figure 5-2. POST Screen with AMI Logo

Press **** or **<Esc>** to access the Setup Utility program and the **Main** menu of the Aptio Setup Utility will appear on the screen as shown:



BIOS Setup Menu Initialization Screen

You may move the cursor by $<\uparrow>$ and $<\downarrow>$ keys to highlight the individual menu items. As you highlight each item, a brief description of the highlighted selection will appear on the right side of the screen.

The language of the BIOS setup menu interface and help messages are shown in US English. You may use $<\uparrow>$ or $<\downarrow>$ key to select among the items and press $<\text{Enter}>$ to confirm and enter the sub-menu. The following table provides the list of the navigation keys that you can use while operating the BIOS setup menu.

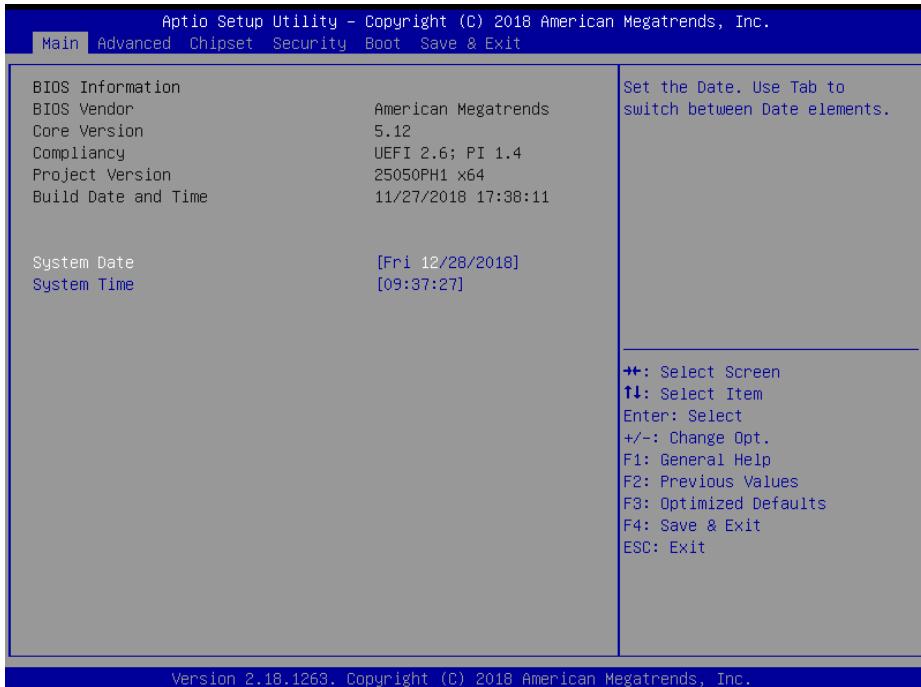
BIOS Setup Navigation Key	Description
$<\leftrightarrow>$ and $<\rightarrow>$	Select a different menu screen (move the cursor from the selected menu to the left or right).
$<\uparrow>$ and $<\downarrow>$	Select a different item (move the cursor from the selected item upwards or downwards)

BIOS Setup Navigation Key	Description
<Enter>	Execute the command or select the sub-menu.
<F2>	Load the previous configuration values.
<F3>	Load the default configuration values.
<F4>	Save the current values and exit the BIOS setup menu.
<Esc>	Close the sub-menu. Trigger the confirmation to exit BIOS setup menu.

5.3 Main

Menu Path *Main*

The **Main** menu allows you to view the BIOS Information, change the system date and time, and view the user access privilege level. Use tab to switch between date elements. Use **<↑>** or **<↓>** arrow keys to highlight the item and enter the value you want in each item. This screen also displays the BIOS version (project) and BIOS Build Date and Time.



Main Screen

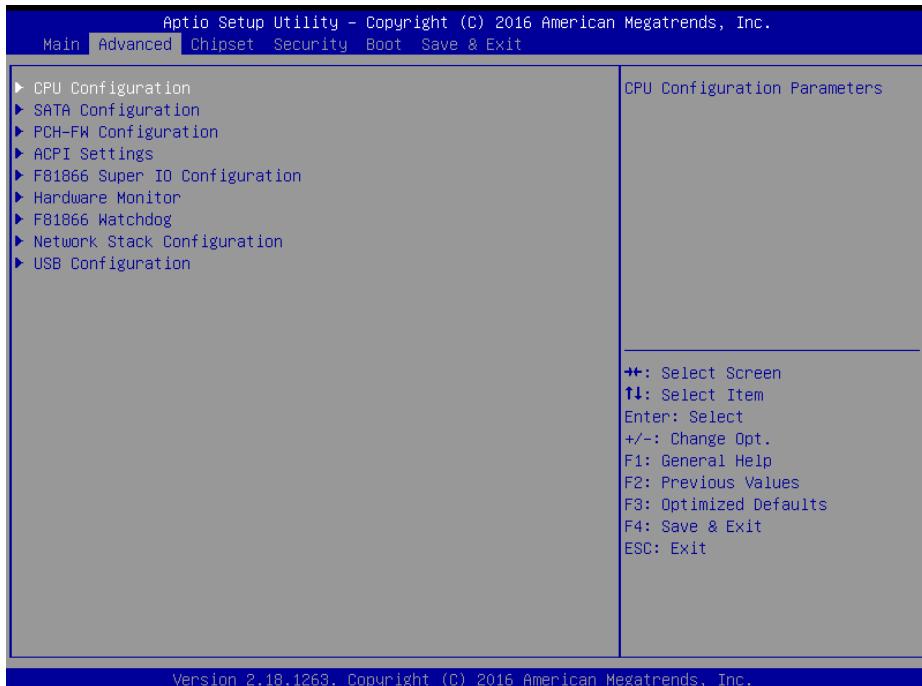
BIOS Setting	Options	Description/Purpose
BIOS Vendor	No changeable options	Displays the BIOS vendor.
Core Version	No changeable options	Displays the current BIOS core version.
Compliance	No changeable options	Displays the current UEFI version.
Project Version	No changeable options	Displays the version of the BIOS currently installed on the platform.
Build Date and Time	No changeable options	Displays the date of the current BIOS version.
System Date	Month, day, year	Sets the system date. The format is [Day Month/ Date/ Year]. Users can directly enter values or use <+> or <-> arrow keys to increase/decrease it. The “Day” is automatically changed.

BIOS Setting	Options	Description/Purpose
System Time	Hour, minute, second	Sets the system time. The format is [Hour: Minute: Second]. Users can directly enter values or use <+> or <-> arrow keys to increase/decrease it.

5.4 Advanced

Menu Path *Advanced*

This menu provides advanced configurations such as CPU Configuration, SATA Configuration, PCH-FW Configuration, ACPI Settings, F81866 Super IO Configuration, Hardware Monitor, F81866 Watchdog, Network Stack Configuration and USB Configuration.



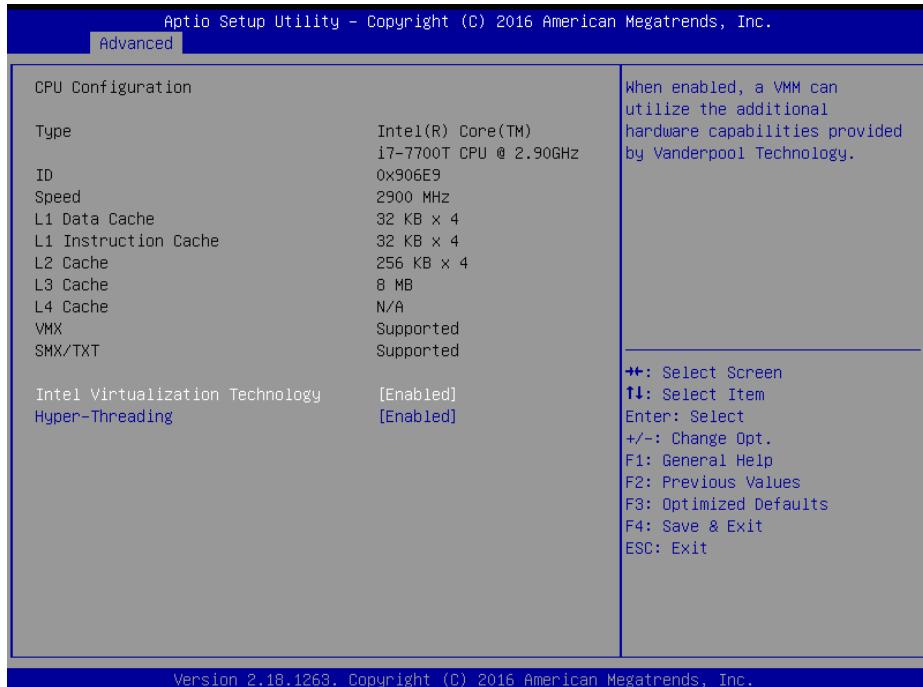
Advanced Menu Screen

BIOS Setting	Options	Description/Purpose
CPU Configuration	Sub-Menu	CPU Configuration Parameters.
SATA Configuration	Sub-Menu	SATA Device Options Settings.
PCH-FW Configuration	Sub-Menu	Management Engine Technology Parameters.
ACPI Settings	Sub-Menu	System ACPI Parameters.
F81866 Super IO Configuration	Sub-Menu	System Super I/O Chip Parameters.
Hardware Monitor	Sub-Menu	Monitor hardware status.
F81866 Watchdog	Sub-Menu	F81866 Watchdog Parameters.
Network Stack Configuration	Sub-Menu	Network Stack Settings.
USB Configuration	Sub-Menu	USB Configuration Parameters.

5.4.1 Advanced – CPU Configuration

Menu Path *Advanced > CPU Configuration*

The **CPU Configuration** provides advanced CPU settings and some information about CPU.



CPU Configuration Screen

BIOS Setting	Options	Description/Purpose
CPU Configuration	No changeable options	Displays CPU configuration.
Type	No changeable options	Displays CPU Core.
ID	No changeable options	Displays CPU ID number.
Speed	No changeable options	Displays CPU speed.
L1 Data Cache	No changeable options	Displays L1 Data Cache size.
L1 Instruction Cache	No changeable options	Displays L1 Instruction Cache size.
L2 Cache	No changeable options	Displays L2 Cache size.
L3 Cache	No changeable options	Displays L3 Cache size.
L4 Cache	No changeable options	Displays L4 Cache size.
VMX	No changeable options	Supports Intel VMX Technology.
SMX/TXT	No changeable options	Reports if Intel Secure Mode Extensions Technology (SMX) /Trusted Execution Technology (TXT) is supported by the processor.

BIOS Setting	Options	Description/Purpose
Intel Virtualization Technology	- Disabled - Enabled	When enabled, VMM can utilize the additional hardware capabilities provided by Vanderpool Technology. Previously codenamed "Vanderpool", VT-x represents Intel's technology for virtualization on the x86 platform.
Hyper-Threading	- Disabled - Enabled	When disabled, only one thread per enabled core is enabled. Hyper Threading is Intel's term for its simultaneous multithreading implementation in their CPUs. Enabling this function will improve parallelization of computation performed on PC microprocessor. For each processor core that is physically present, the operating system addresses two virtual processors, and shares the workload between them when possible.

5.4.2 Advanced – SATA Configuration

Menu Path *Advanced > SATA Configuration*

The **SATA Configuration** allows users to enable / disable the SATA controller as well as the operational mode after the SATA controller is enabled. The following screen indicates the functions available when the SATA hard drive is set to work in AHCI mode.



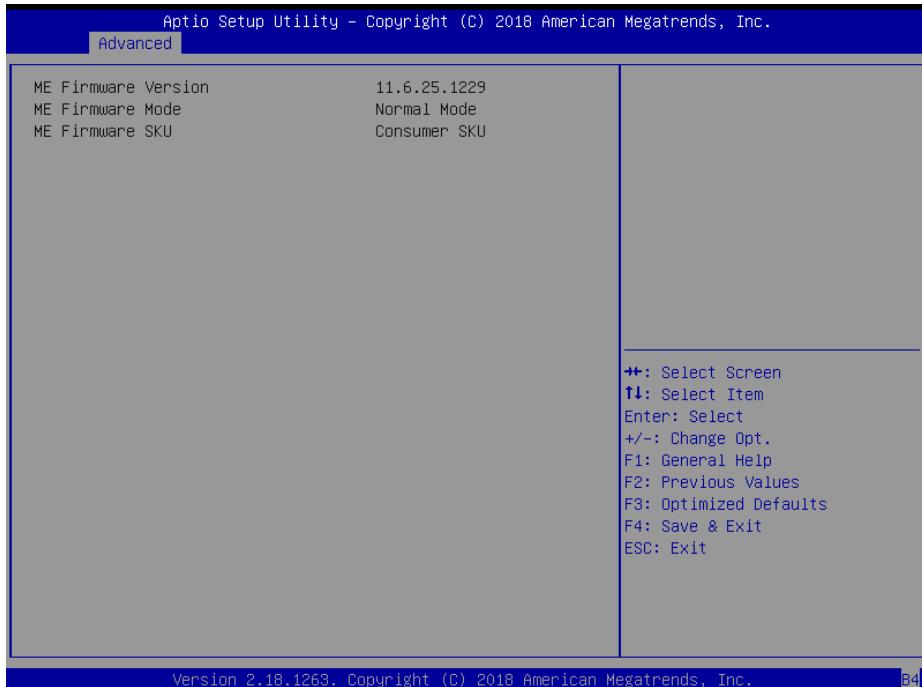
SATA Configuration Screen

BIOS Setting	Options	Description/Purpose
SATA Controller(s)	- Disabled - Enabled	Enables or Disables the on-chip SATA Device. Default: Enabled.
SATA Mode Selection	- AHCI - RAID	Determines how SATA controller(s) operate.
Serial ATA Port 1 – 2	No changeable options	Display the SATA device's name.
Software Preserve	No changeable options	Indicates whether the connected SATA device supports Software Setting Preservation (SSP).
Port 1 - 2	- Disabled - Enabled	Enables or Disables SATA Port Device.
Hot Plug	- Disabled - Enabled	Enables or Disables SATA Port Device Hot Plug function to designate a SATA port device as hot-pluggable.

5.4.3 Advanced – PCH-FW Configuration

Menu Path Advanced > PCH-FW Configuration

The **PCH-FW** allows users to view the information about ME (Management Engine) firmware information, such as ME firmware version, firmware mode and firmware SKU.



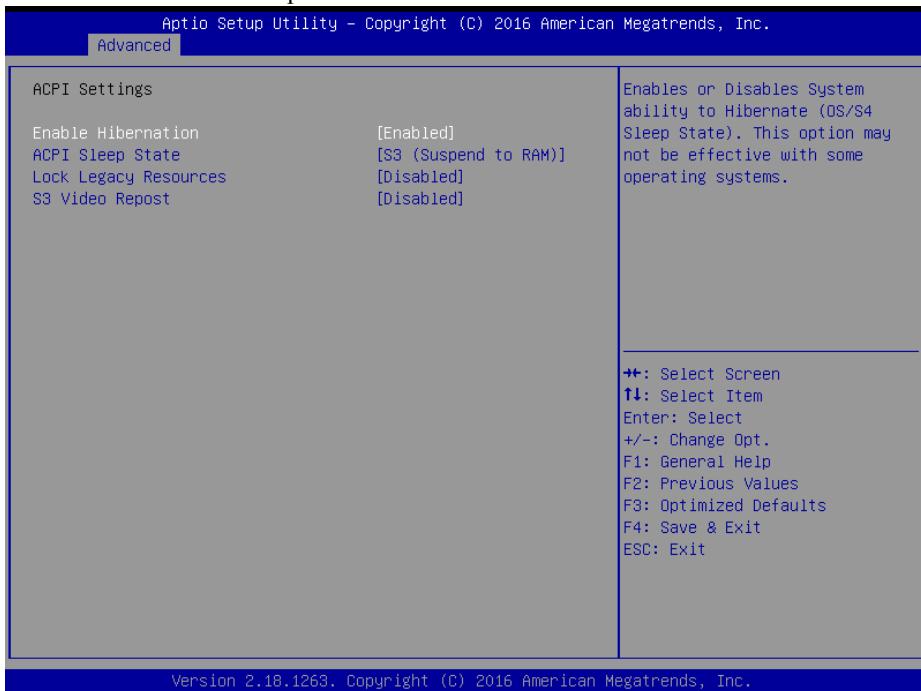
PCH-FW Configuration Screen

BIOS Setting	Options	Description/Purpose
ME Firmware Version	No changeable options	Displays the ME Firmware Version.
ME Firmware Mode	No changeable options	Displays the ME Firmware Mode.
ME Firmware SKU	No changeable options	Displays the ME Firmware SKU.

5.4.4 Advanced – ACPI Settings

Menu Path *Advanced > ACPI Settings*

The **ACPI Settings** allows users to configure relevant ACPI (Advanced Configuration and Power Management Interface) settings such as Enable/Disable Hibernation and ACPI sleep state.

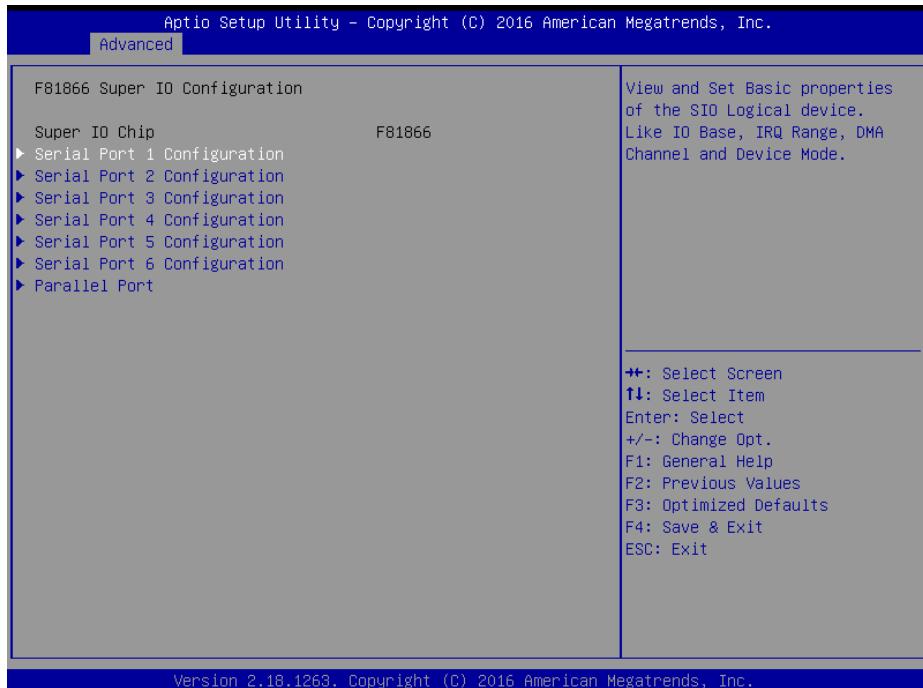


ACPI Settings Screen

BIOS Setting	Options	Description/Purpose
Enable Hibernation	- Disabled - Enabled	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some OS.
ACPI Sleep State	- Suspend Disabled - S3 (Suspend to RAM)	Selects the ACPI sleep state the system will enter when the SUSPEND button is pressed.
Lock Legacy Resources	- Disabled - Enabled	Enables or Disables Lock of Legacy Resources.
S3 Video Repost	- Disabled - Enabled	Enables or Disables S3 Video Repost.

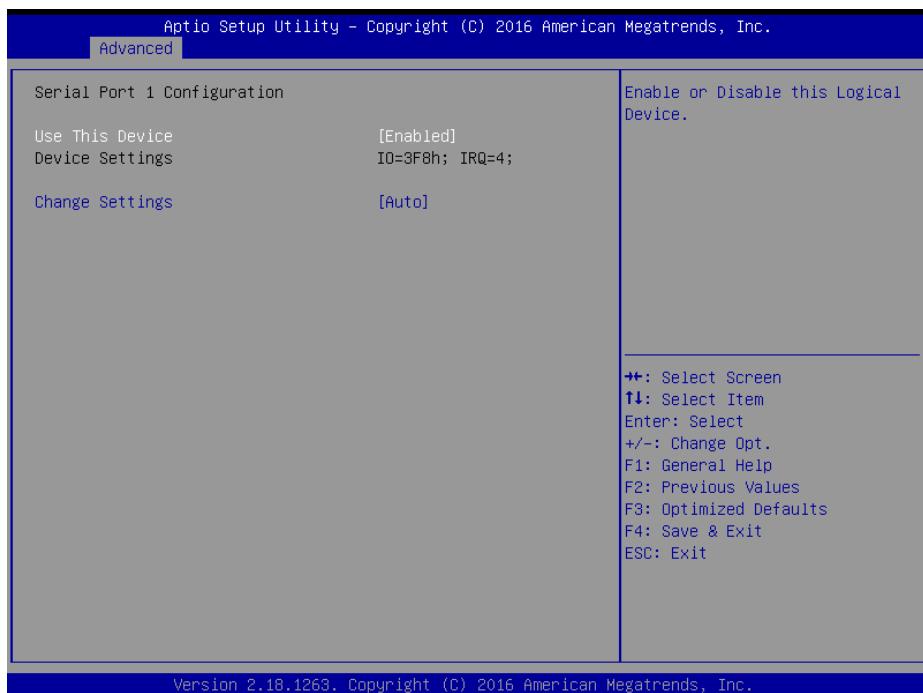
5.4.5 Advanced – F81866 Super IO Configuration

Menu Path

Advanced > F81866 Super IO Configuration**F81866 Super IO Configuration Screen**

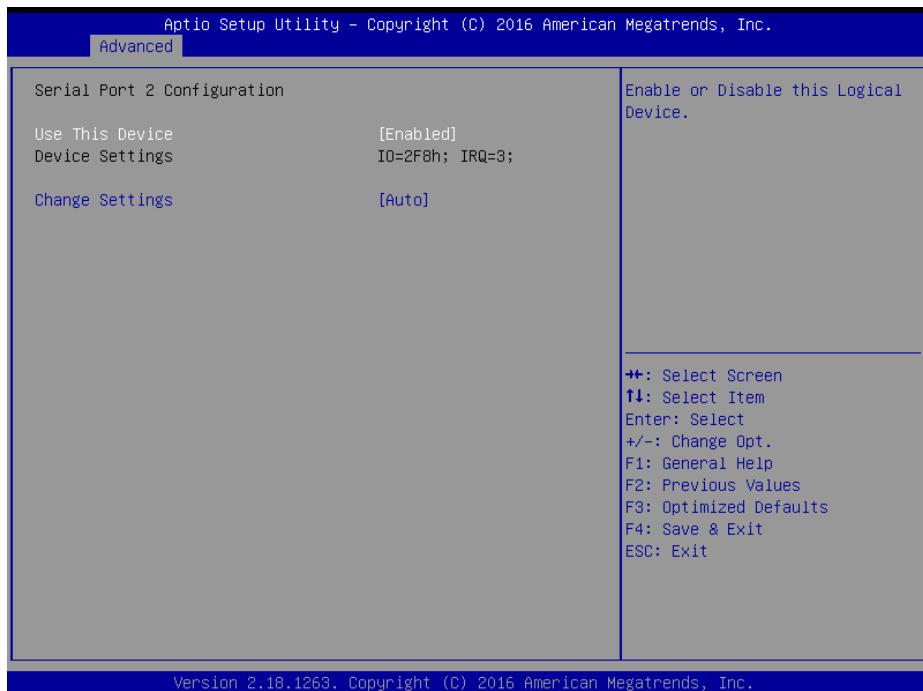
BIOS Setting	Options	Description/Purpose
Serial Port 1 Configuration	Sub-Menu	Sets the parameters of Serial Port 1 (COMA).
Serial Port 2 Configuration	Sub-Menu	Sets the parameters of Serial Port 2 (COMB).
Serial Port 3 Configuration	Sub-Menu	Sets the parameters of Serial Port 3 (COMC).
Serial Port 4 Configuration	Sub-Menu	Sets the parameters of Serial Port 4 (COMD).
Serial Port 5 Configuration	Sub-Menu	Sets the parameters of Serial Port 5 (COME).
Serial Port 6 Configuration	Sub-Menu	Sets the parameters of Serial Port 6 (COMF).
Parallel Port	Sub-Menu	Sets the parameters of Parallel Port (LPT).

Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 1 Configuration*

**Serial Port 1 Configuration Screen**

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables Serial Port 1.
Device Settings	No changeable options	Displays the current settings of Serial Port 1.
Change Settings	- Auto - IO=3F8h; IRQ=4 - IO=3F8h; IRQ=3,4,5,6,7,10,11 - IO=2F8h; IRQ=3,4,5,6,7,10,11 - IO=3E8h; IRQ=3,4,5,6,7,10,11 - IO=2E8h; IRQ=3,4,5,6,7,10,11	Allows you to select specific I/O address and IRQ for Serial Port 1.

Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 2 Configuration*

**Serial Port 2 Configuration Screen**

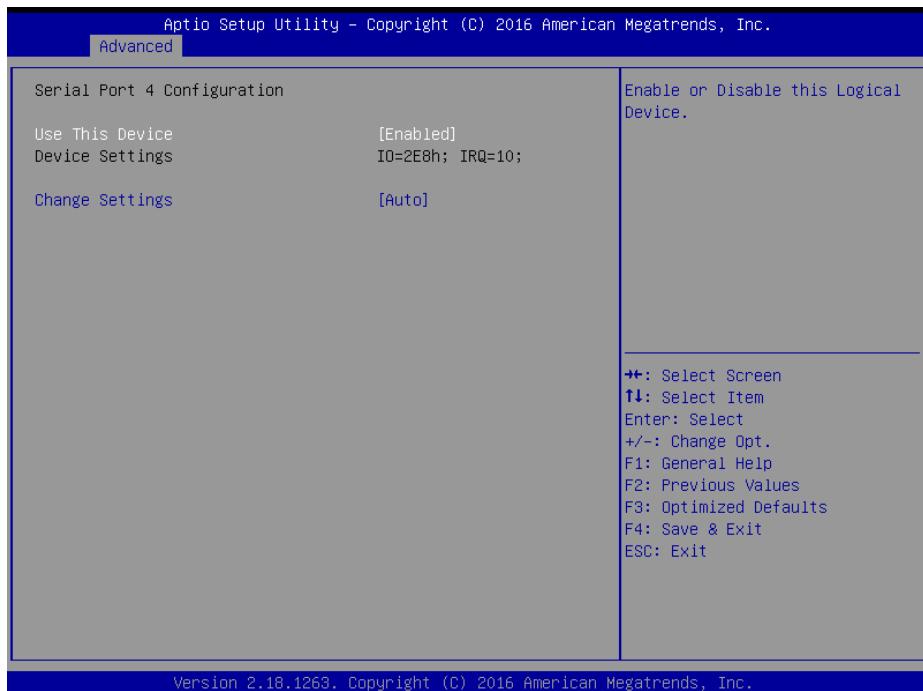
BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables Serial Port 2.
Device Settings	No changeable options	Displays the current settings of Serial Port 2.
Change Settings	- Auto - IO=2F8h; IRQ=3 - IO=3F8h; IRQ=3,4,5,6,7,10,11 - IO=2F8h; IRQ=3,4,5,6,7,10,11 - IO=3E8h; IRQ=3,4,5,6,7,10,11 - IO=2E8h; IRQ=3,4,5,6,7,10,11	Allows you to select specific I/O address and IRQ for Serial Port 2.

Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 3 Configuration*

**Serial Port 3 Configuration Screen**

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables Serial Port 3.
Device Settings	No changeable options	Displays the current settings of Serial Port 3.
Change Settings	- Auto - IO=3E8h; IRQ=7 - IO=3E8h; IRQ=3,4,5,6,7,10,11 - IO=2E8h; IRQ=3,4,5,6,7,10,11 - IO=2F0h; IRQ=3,4,5,6,7,10,11 - IO=2E0h; IRQ=3,4,5,6,7,10,11	Allows you to select specific I/O address and IRQ for Serial Port 3.

Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 4 Configuration*

**Serial Port 4 Configuration Screen**

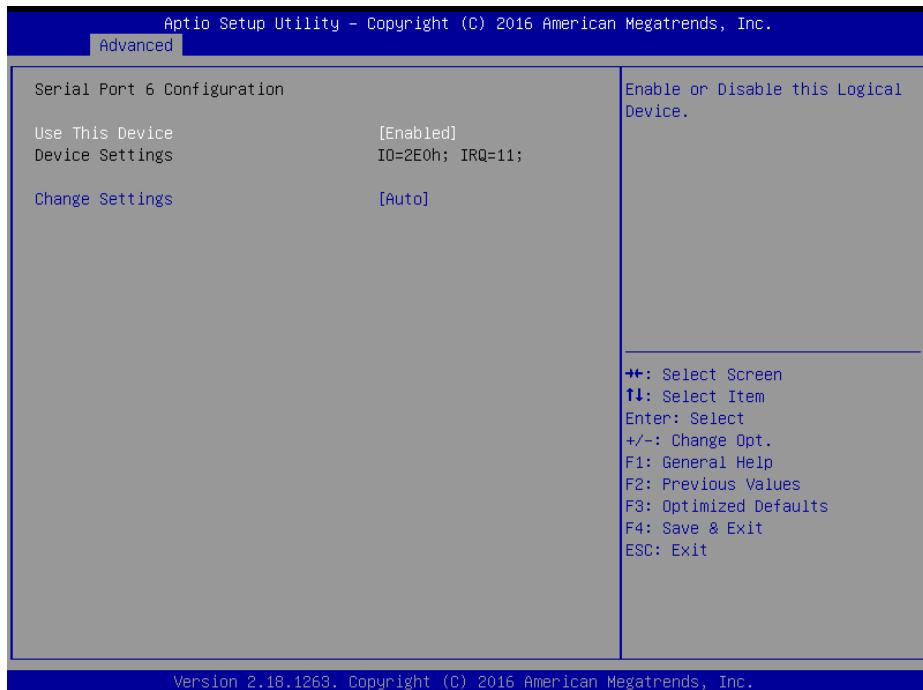
BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables Serial Port 4.
Device Settings	No changeable options	Displays the current settings of Serial Port 4.
Change Settings	- Auto - IO=2E8h; IRQ=10 - IO=3E8h; IRQ=3,4,5,6,7,10,11 - IO=2E8h; IRQ=3,4,5,6,7,10,11 - IO=2F0h; IRQ=3,4,5,6,7,10,11 - IO=2E0h; IRQ=3,4,5,6,7,10,11	Allows you to select specific I/O address and IRQ for Serial Port 4.

Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 5 Configuration*

**Serial Port 5 Configuration Screen**

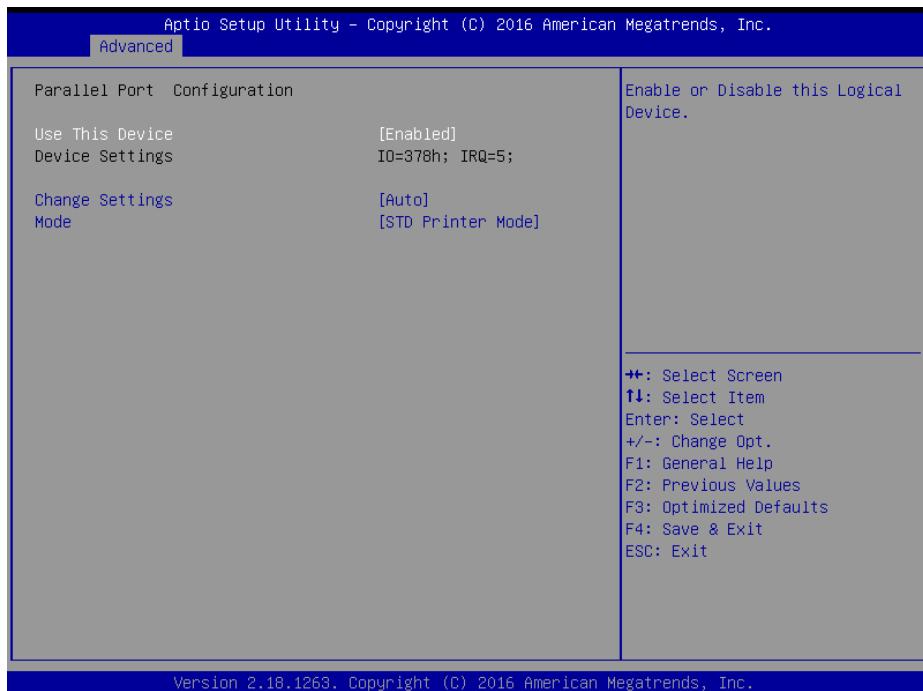
BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables Serial Port 5.
Device Settings	No changeable options	Displays the current settings of Serial Port 5.
Change Settings	- Auto - IO=2F0h; IRQ=6 - IO=3E8h; IRQ=3,4,5,6,7,10,11 - IO=2E8h; IRQ=3,4,5,6,7,10,11 - IO=2F0h; IRQ=3,4,5,6,7,10,11 - IO=2E0h; IRQ=3,4,5,6,7,10,11	Allows you to select specific I/O address and IRQ for Serial Port 5.

Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 6 Configuration*

**Serial Port 6 Configuration Screen**

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables Serial Port 6.
Device Settings	No changeable options	Displays the current settings of Serial Port 6.
Change Settings	- Auto - IO=2E0h; IRQ=11 - IO=3E8h; IRQ=3,4,5,6,7,10,11 - IO=2E8h; IRQ=3,4,5,6,7,10,11 - IO=2F0h; IRQ=3,4,5,6,7,10,11 - IO=2E0h; IRQ=3,4,5,6,7,10,11	Allows you to select specific I/O address and IRQ for Serial Port 6.

Menu Path *Advanced > F81866 Super IO Configuration > Parallel Port Configuration*



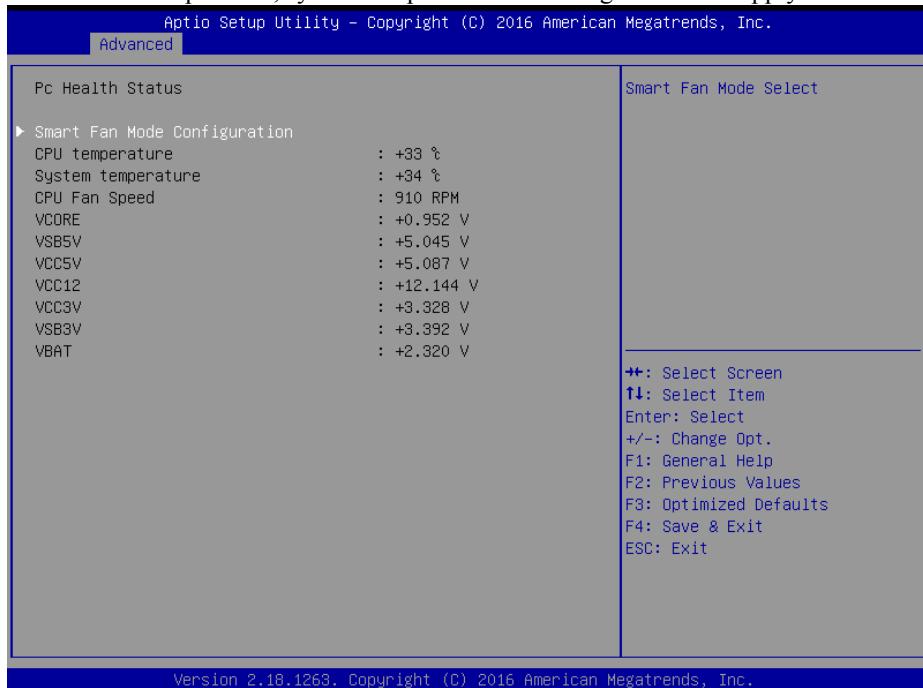
Parallel Port Configuration Screen

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables parallel port.
Device Settings	No changeable options	Displays the current settings of Parallel Port.
Change Settings	- Auto - IO=378h; IRQ=5 - IO=378h; IRQ=5,6,7,9,10,11,12 - IO=278h; IRQ=5,6,7,9,10,11,12 - IO=3BCh; IRQ=5,6,7,9,10,11,12	Allows you to select specific I/O address and IRQ for Parallel Port.
Mode	- STD Printer Mode - SPP Mode - EPP-1.9 and SPP Mode - EPP-1.7 and SPP Mode - ECP Mode - ECP and EPP 1.9 Mode - ECP and EPP 1.7 Mode	Allows you to change Parallel Port mode. Some of the Mode required a DMA resource. After Mode is changed, reset the system to reflect actual device settings.

5.4.6 Advanced – Hardware Monitor

Menu Path *Advanced > Hardware Monitor*

The **Hardware Monitor** allows users to monitor the health and status of the system such as CPU temperature, system temperature and voltage levels in supply.



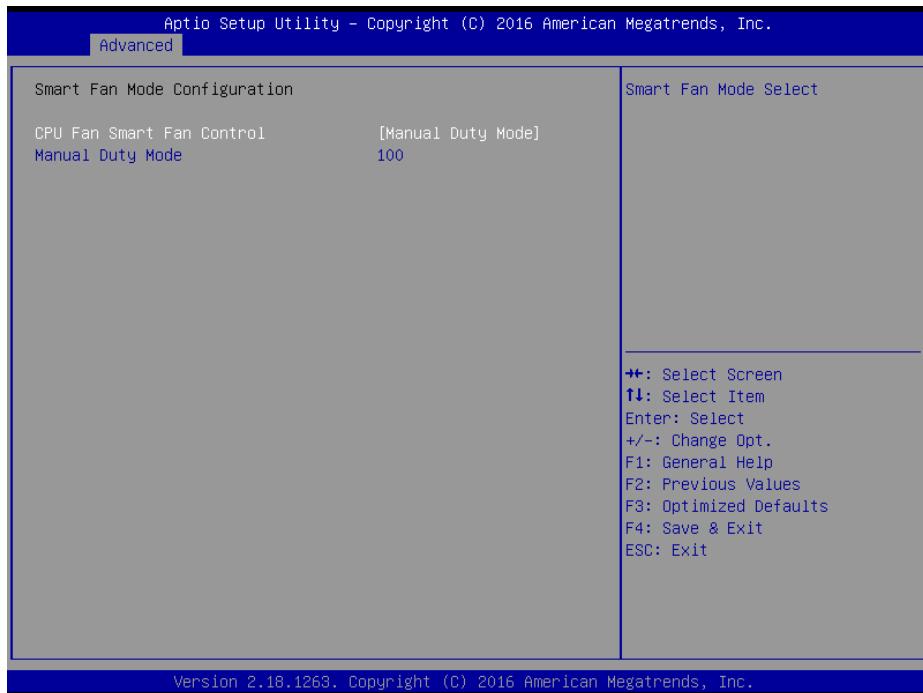
Hardware Monitor Screen

BIOS Setting	Options	Description/Purpose
Smart Fan Mode Configuration	Sub-Menu	Smart Fan Mode Selection
CPU Temperature	No changeable options	Displays the processor's temperature.
System Temperature	No changeable options	Displays the system's temperature.
CPU Fan Speed	No changeable options	Displays CPU Fan speed
VCORE	No changeable options	Detects and displays the VCORE CPU voltage.
VSB5V	No changeable options	Detects and displays voltage level of the VSB5V in supply.
VCC5V	No changeable options	Detects and displays voltage level of the VCC5V in supply.
VCC12	No changeable options	Detects and displays voltage level of the VCC12 in supply.
VCC3V	No changeable options	Detects and displays voltage level of the VCC3V in supply.

BIOS Setting	Options	Description/Purpose
VSB3V	No changeable options	Detects and displays voltage level of the VSB3V in supply.
VBAT	No changeable options	Detects and displays the battery voltage.

5.4.6.1 Advanced – Hardware Monitor – Smart Fan Mode Configuration

Menu Path *Advanced > Hardware Monitor > Smart Fan Mode Configuration*



Smart Fan Mode Configuration Screen

BIOS Setting	Options	Description/Purpose
CPU Fan Smart Fan Control	- Manual Duty Mode - Auto Duty-Cycle Mode	Smart Fan Mode selection for CPU Fan.
Manual Duty Mode	Numeric (from 1 to 100)	Manual mode fan control. Users can write expected duty cycle from 1-100 (PWM fan type).

5.4.7 Advanced – F81866 Watchdog Configuration

Menu Path *Advanced > F81866 Watchdog*

If the system hangs or fails to respond, enable the F81866 Watchdog function to trigger a system reset via the 255-level watchdog timer.



F81866 Watchdog Configuration Screen

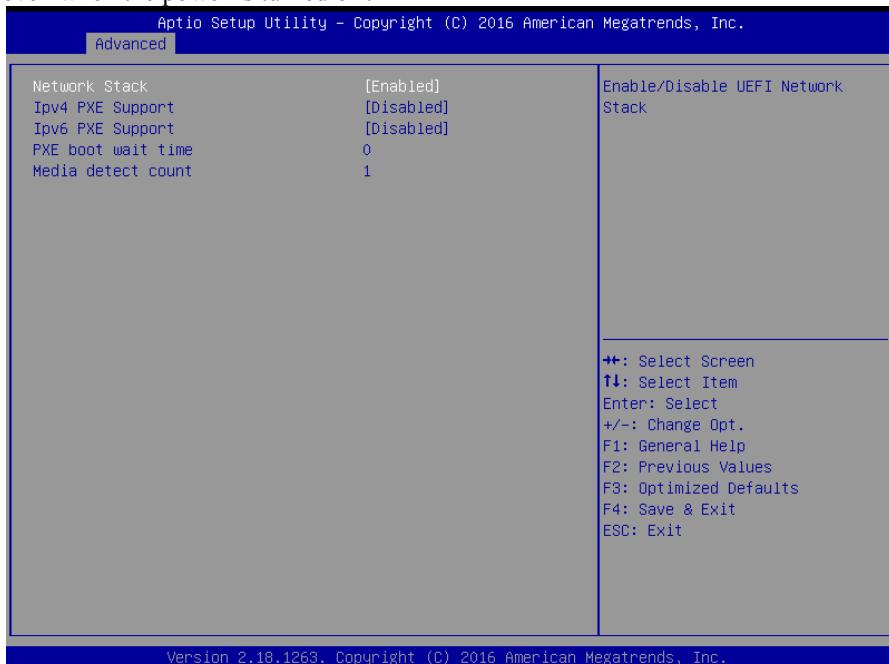
BIOS Setting	Options	Description/Purpose
Enable Watchdog	- Enabled - Disabled	Enables/Disables F81866 Watchdog timer settings.
Watchdog timer unit	- 1s - 60s	Selects 1s (second) or 60s (minute) as the time unit of Watchdog timer.
Count for Timer (Seconds)	Numeric (from 1 to 255)	Sets the timeout for Watchdog timer. (Max. value: 255 seconds or minutes)

5.4.8 Advanced – Network Stack Configuration

Menu Path *Advanced > Network Stack Configuration*

The **Network Stack Configuration** allows users to enable/disable UEFI Network Stack, IPv4/IPv6 PXE (Pre-Boot eXecution Environment) support and configure PXE boot wait time and detect the media presence.

PXE allows a workstation to boot from a server on a network prior to booting the operating system on the local hard drive. A PXE-enabled workstation connects its NIC to the LAN via a jumper, which keeps the workstation connected to the network even when the power is turned off.



Network Stack Configuration Screen

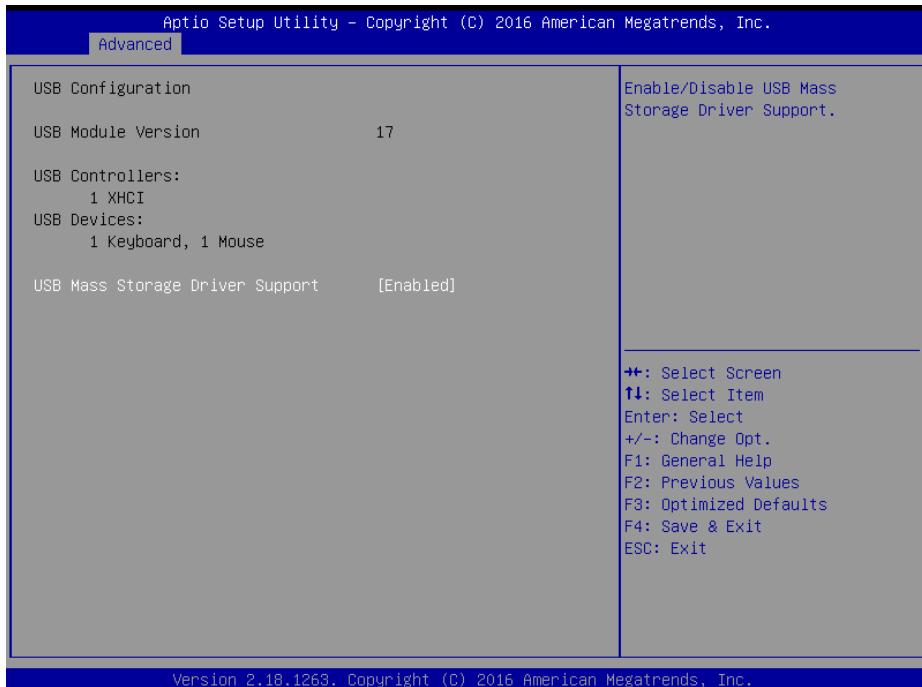
BIOS Setting	Options	Description/Purpose
Network Stack	- Disabled - Enabled	Enables or Disables UEFI Network Stack.
Ipv4 PXE Support	- Disabled - Enabled	Enables IPv4 PXE Boot Support. If disabled, IPv4 PXE boot option will not be created.
Ipv6 PXE Support	- Disabled - Enabled	Enables IPv6 PXE Boot Support. If disabled, IPv6 PXE boot option will not be created.
PXE boot wait time	Numeric (from 0 to 5)	Number of seconds to wait for PXE boot to abort after the Esc key is pressed.

BIOS Setting	Options	Description/Purpose
Media detect count	Numeric (from 1 to 50)	Number of times that the media presence will be checked.

5.4.9 Advanced – USB Configuration

Menu Path *Advanced > USB Configuration*

The **USB Configuration** allows users to enable/disable USB mass storage driver support.



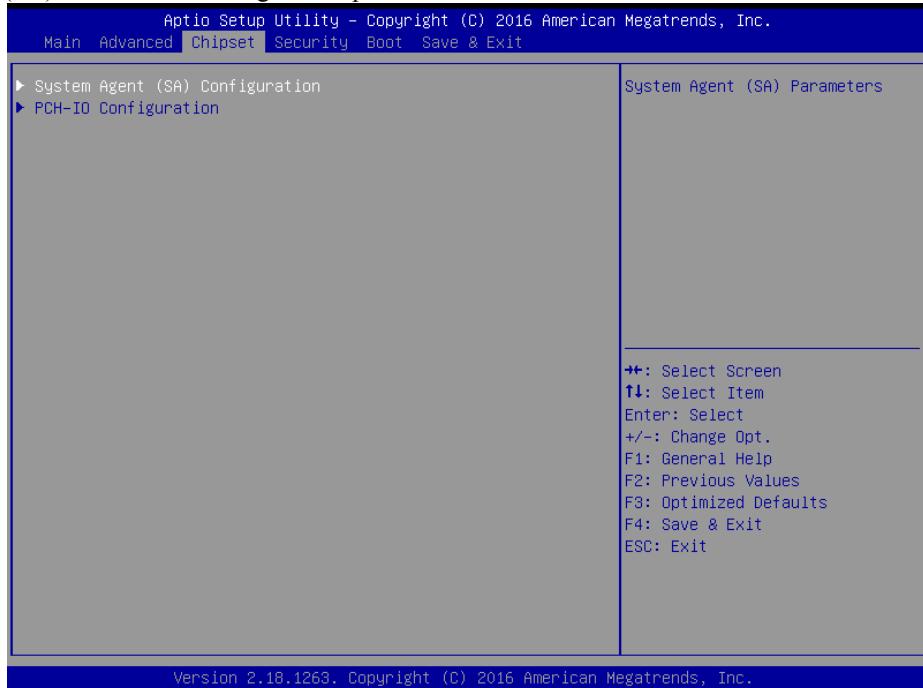
USB Configuration Screen

BIOS Setting	Options	Description/Purpose
USB Mass Storage Driver Support	- Enabled - Disabled	Enables or Disables USB mass storage driver support.

5.5 Chipset

Menu Path *Chipset*

This menu allows users to configure advanced Chipset settings such as System Agent (SA) and PCH-IO configuration parameters.

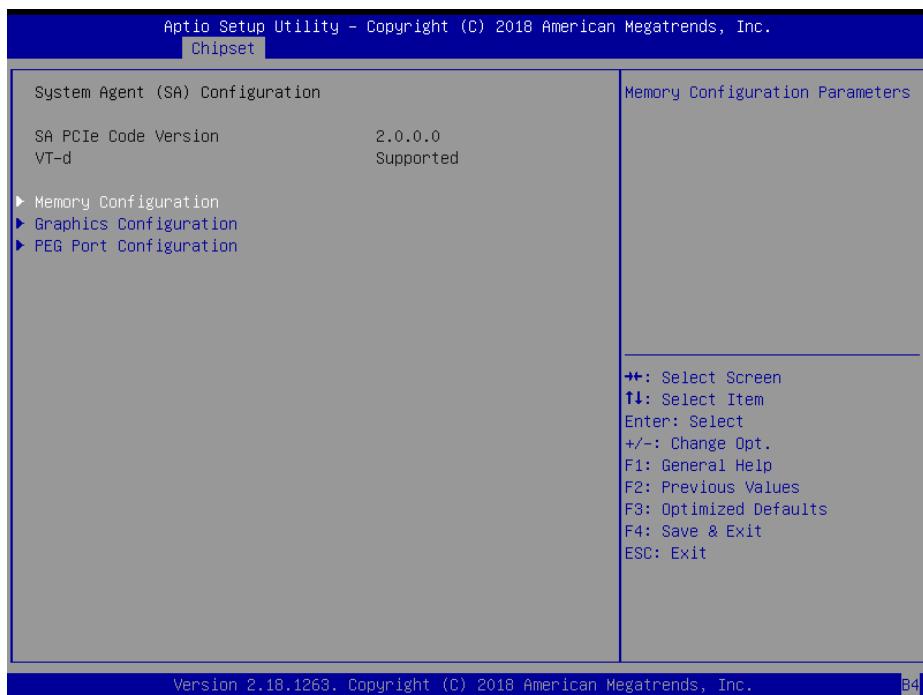
**Chipset Screen**

BIOS Setting	Options	Description/Purpose
System Agent (SA) Configuration	Sub-Menu	System Agent (SA) parameters.
PCH-IO Configuration	Sub-Menu	PCH parameters.

5.5.1 Chipset – System Agent (SA) Configuration

Menu Path *Chipset > System Agent (SA) Configuration*

The **System Agent Configuration** allows users to display DRAM information on the platform as well as configure graphics settings.



System Agent (SA) Configuration Screen

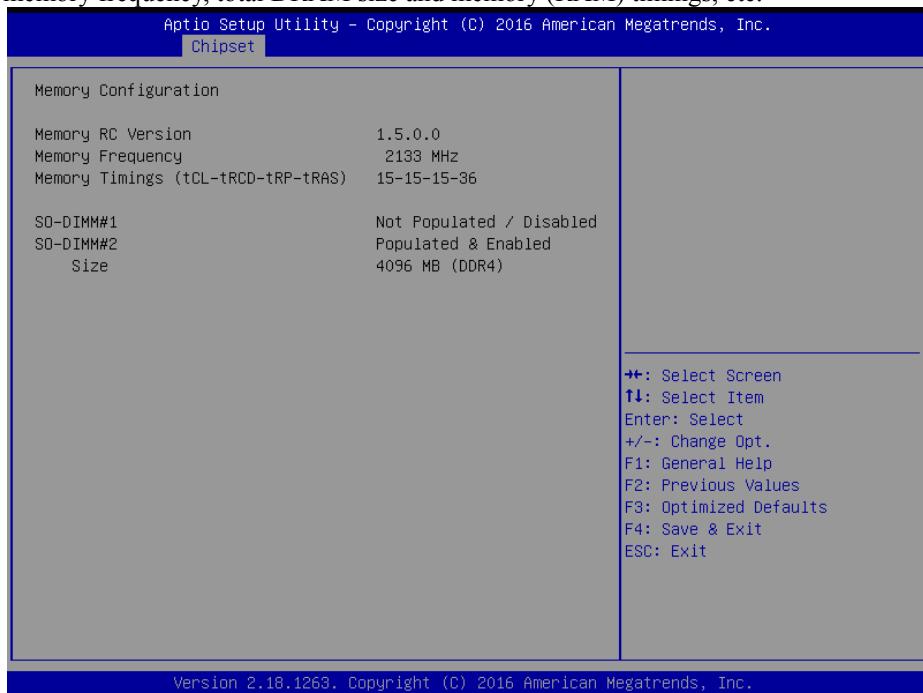
BIOS Setting	Options	Description/Purpose
SA PCIe Code Version	No changeable options	Displays the SA PCIe Code Version.
VT-d	No changeable options	Indicates whether Intel's VT-d (Virtualization Technology for Directed I/O) capability is supported. VT-d extends Intel's Virtualization Technology (VT) roadmap by providing hardware assists for virtualization solution, and helps end users improve security and reliability of the systems and also improves performance of I/O devices in virtualized environment.
Memory Configuration	Sub-Menu	Displays the DRAM information on the platform.

BIOS Setting	Options	Description/Purpose
Graphics Configuration	Sub-Menu	Configures Graphics Settings.
PEG Port Configuration	Sub-Menu	PEG (PCI Express Graphics) Port Configuration.

5.5.1.1 Chipset – SA Configuration – Memory Configuration

Menu Path *Chipset > System Agent (SA) Configuration > Memory Configuration*

The **Memory Configuration** allows users to check for the information about the memory frequency, total DRAM size and memory (RAM) timings, etc.



Memory Configuration Screen

BIOS Setting	Options	Description/Purpose
Memory RC Version	No changeable options	Displays the Memory RC Version.
Memory Frequency	No changeable options	Displays the Frequency of Memory.
Memory Timings (tCL-tRCD-tRP-tRAS)	No changeable options	Displays the Memory (RAM) timings and latency. • CAS Latency (tCL) - This is the most important memory timing. CAS stands

BIOS Setting	Options	Description/Purpose
		<p>for Column Address Strobe. If a row has already been selected, it tells us how many clock cycles we'll have to wait for a result (after sending a column address to the RAM controller).</p> <ul style="list-style-type: none"> • Row Address (RAS) to Column Address (CAS) Delay (tRCD) - Once we send the memory controller a row address, we'll have to wait this many cycles before accessing one of the row's columns. So, if a row hasn't been selected, this means we'll have to wait tRCD + tCL cycles to get our result from the RAM. • Row Precharge Time (tRP) - If we already have a row selected, we'll have to wait this number of cycles before selecting a different row. This means it will take tRP + tRCD + tCL cycles to access the data in a different row. • Row Active Time (tRAS) - This is the minimum number of cycles that a row has to be active for to ensure we'll have enough time to access the information that's in it. This usually needs to be greater than or equal to the sum of the previous three latencies ($tRAS = tCL + tRCD + tRP$).
SO-DIMM#1	No changeable options	Displays if SO-DIMM#1 socket is populated/enabled or not.
SO-DIMM#2	No changeable options	Displays if SO-DIMM#2 socket is populated/enabled or not.
Size	No changeable options	Displays the total memory size.

5.5.1.2 Chipset – System Agent (SA) Configuration – Graphics Configuration

Menu Path *Chipset > System Agent (SA) Configuration > Graphics Configuration*



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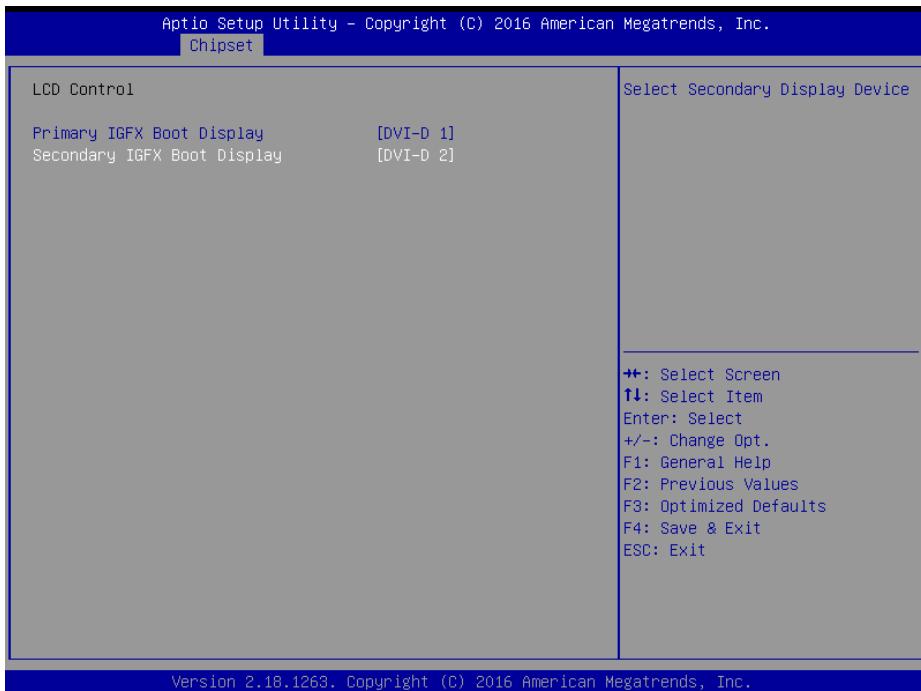
B4

Graphics Configuration Screen

BIOS Setting	Options	Description/Purpose
Primary Display	- Auto - IGFX - PEG	Selects which of IGFX/PEG Graphics device should be Primary Display.
Internal Graphics	- Auto - Disabled - Enabled	Keeps IGFX enabled based on the setup options.
LCD Control	Sub-Menu	LCD Control sub-menu.

Menu Path *Chipset > System Agent (SA) Configuration > Graphics Configuration > LCD Control*

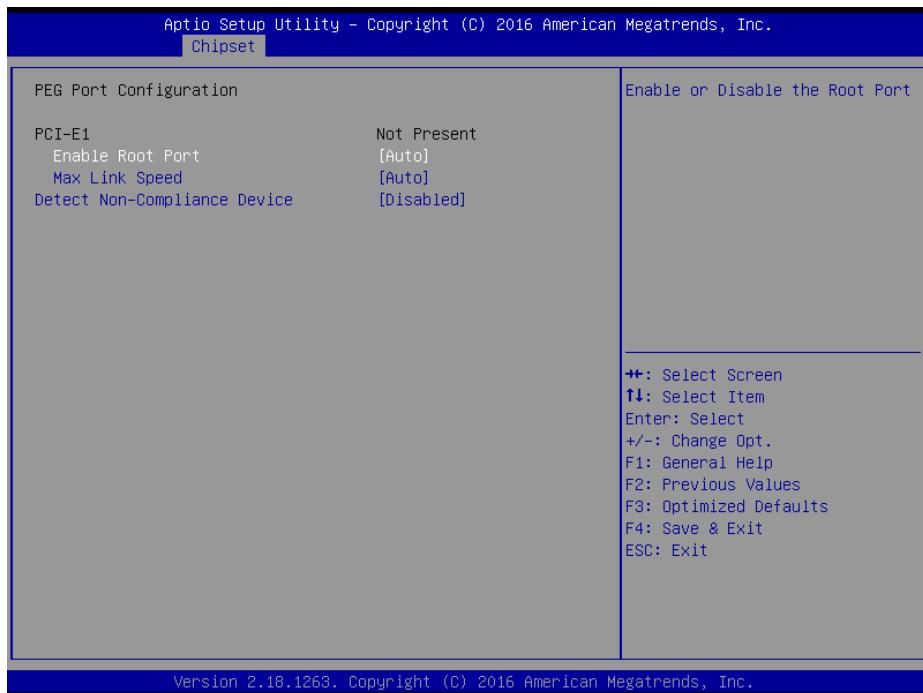
The **LCD Control** allows users to select the primary and secondary display device.

**LCD Control Screen**

BIOS Setting	Options	Description/Purpose
Primary IGFX Boot Display	<ul style="list-style-type: none"> - VBIOS default - DVI-D 1 - DVI-D 2 - DisplayPort 	Selects Primary Display Device.
Secondary IGFX Boot Display	<ul style="list-style-type: none"> - Disabled - DVI-D 1 - DVI-D 2 - DisplayPort 	Selects Secondary Display Device.

5.5.1.3 Chipset –System Agent (SA) Configuration – PEG Port Configuration

Menu Path *Chipset > System Agent (SA) Configuration > PEG Port Configuration*



PEG Port Configuration Screen

BIOS Setting	Options	Description/Purpose
PCI-E1	No changeable options	Displays PCI-E1 Link and speed information.
Enable Root Port	- Disabled - Enabled - Auto	Enables or Disables the Root Port.
Max Link Speed	- Auto - Gen1 - Gen2 - Gen3	Configures PCI-E1 maximum speed.
Detect Non-Compliance Device	- Disabled - Enabled	Detects Non-Compliance PCI Express Device in PEG.

5.5.2 Chipset – PCH-IO Configuration

Menu Path *Chipset > PCH-IO Configuration*

The **PCH-IO** Configuration allows users to set PCI Express configuration parameters, enable/disable PCH LAN Controller and Wake-On-LAN function and determine the power on/off state that the system will go into following a power failure (G3 state).



PCH-IO Configuration Screen

BIOS Setting	Options	Description/Purpose
Name	No changeable options	Displays the Intel PCH Name.
PCH SKU	No changeable options	Displays the Intel PCH SKU.
Stepping	No changeable options	Displays the Intel PCH Stepping.
PCI Express Configuration	Sub-Menu	Configures PCI Express Configuration settings.
PCH LAN Controller	- Disabled - Enabled	Enables or Disables onboard NIC.
Wake on LAN Enable	- Disabled - Enabled	Enables or Disables integrated LAN to wake up the system. Default: Enabled.
State After G3	- Power On - Power Off	Specifies the Power On/Off state that the system will go into when the power is re-applied following a power failure (G3 state).

5.5.2.1 Chipset – PCH-IO Configuration – PCI Express Configuration

Menu Path *Chipset > PCH-IO Configuration > PCI Express Configuration*

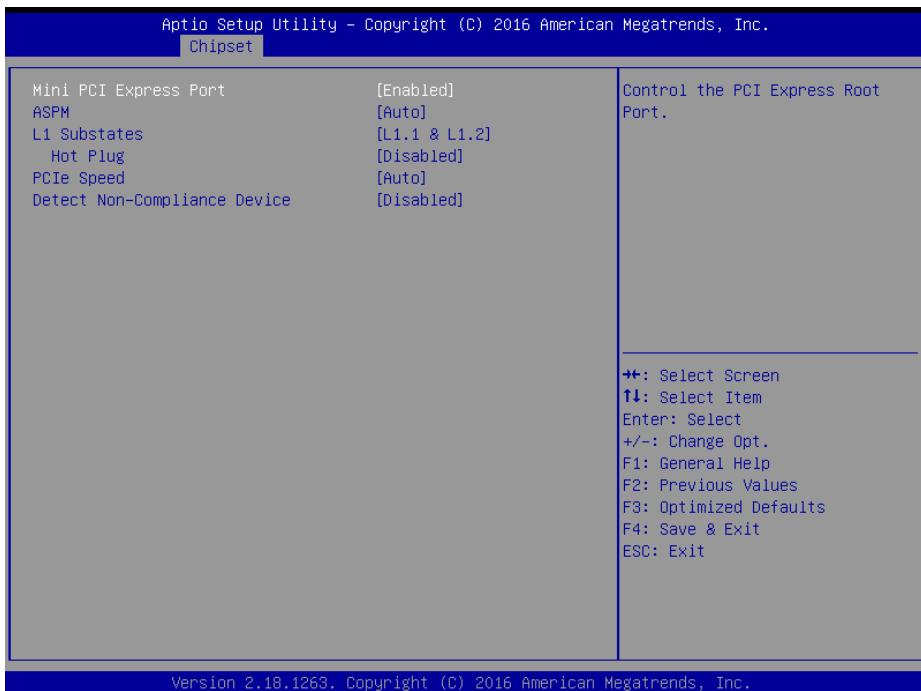
The **PCI Express Configuration** allows users to configure Mini PCI Express Port settings.



PCI Express Configuration Screen

BIOS Setting	Options	Description/Purpose
PCIE Port assigned to LAN	No changeable options	Displays the LAN assigned PCIE Port.
Mini PCI Express Port	Sub-Menu	Configures Mini PCI Express Port settings.

Menu Path *Chipset > PCH-IO Configuration > PCI Express Configuration > Mini PCI Express Port Configuration*

**Mini PCI Express Port Configuration Screen**

BIOS Setting	Options	Description/Purpose
Mini PCI Express Port	- Disabled - Enabled	Controls the PCI Express Root Port.
ASPM	- Disabled - L0s - L1 - L0sL1 - Auto	Sets the PCI Express ASPM (Active-State Power Management) Level. The option allows users to set the lower power mode that activates when the bus is not being used.
L1 Substates	- Disabled - L1.1 - L1.2 - L1.1 & L1.2	PCI Express L1 Substates settings.
Hot Plug	- Disabled - Enabled	Enables or Disables PCI Express Hot Plug.
PCIe Speed	- Auto - Gen1 - Gen2 - Gen3	Selects PCI Express Port Speed.

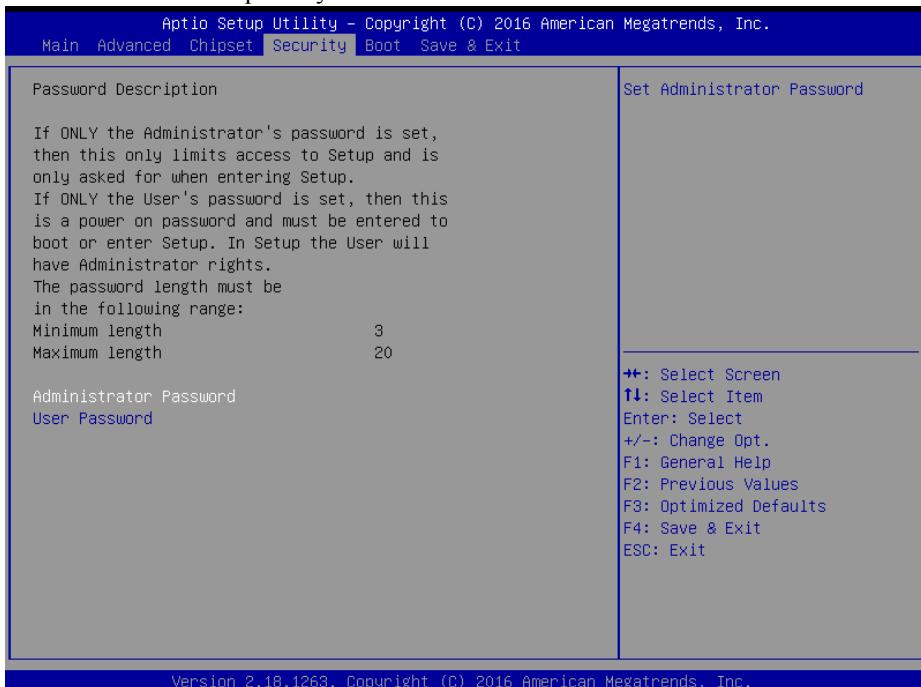
BIOS Setting	Options	Description/Purpose
Detect Non-Compliance Device	- Disabled - Enabled	Detects Non-Compliance PCI Express Device. If enabled, it will take more time during POST.

5.6 Security

Menu Path *Security*

From the **Security** menu, you are allowed to create, change or clear the administrator password. You will be asked to enter the configured administrator password before you can access the Setup Utility.

By setting an administrator password, you will prevent other users from changing your BIOS settings. You can configure an Administrator password and then configure a user password. An administrator has much more privileges over the settings in the Setup utility than a user. Heed that a user password does not provide access to most of the features in the Setup utility.



Security Screen

BIOS Setting	Options	Description/Purpose
Administrator Password	Password can be 3-20 alphanumeric characters.	Specifies the administrator password.
User Password	Password can be 3-20 alphanumeric characters.	Specifies the user password.

Create an Administrator or User Password

1. Select the **Administrator Password / User Password** option from the Security menu and press <Enter>, and the password dialog entry box appears.
2. Enter the password you want to create. A password can be 3-20 alphanumeric characters. After you have configured the password, press <Enter> to confirm.
3. Type the new password again and press <Enter>.

Change an Administrator or User Password

1. Select the **Administrator Password / User Password** option from the Security menu and press <Enter>, and the password dialog entry box appears.
2. Select the Administrator Password or User Password that you want to change. A password can be 3-20 alphanumeric characters. After you have changed the password, press <Enter> to confirm.
3. Type the changed password again and press <Enter>.

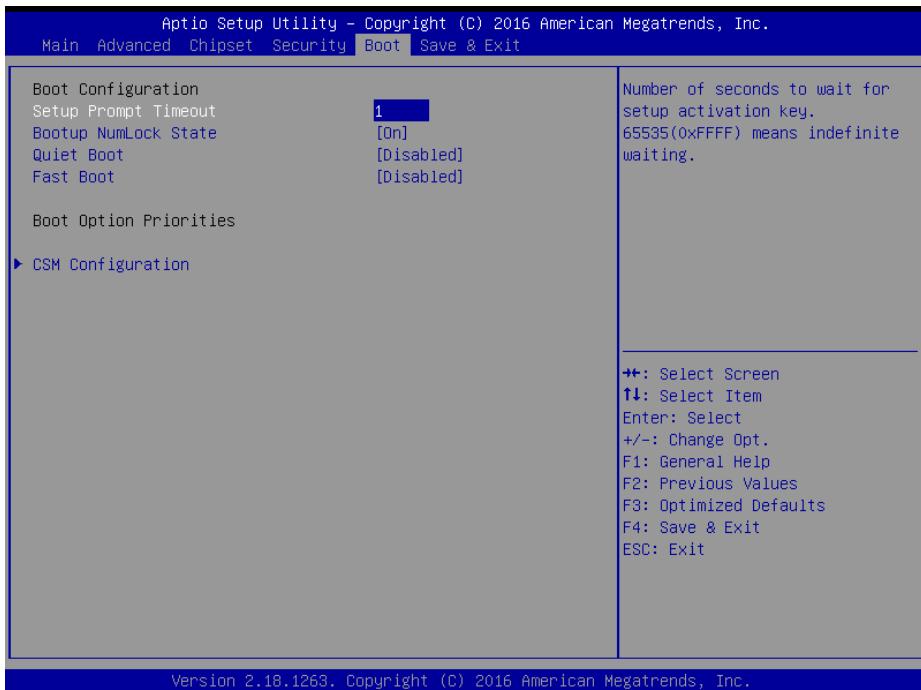
Remove an Administrator or User Password

1. Select the **Administrator Password / User Password** option from the Security menu and press <Enter>, and the password dialog entry box appears.
2. Select the configured Administrator Password or User Password that you want to delete. Leave the dialog box blank and press <Enter>.
3. Press <Enter> again when the password confirmation box appears.

5.7 Boot

Menu Path Boot

This menu provides control items for system boot configuration such as setting setup prompt timeout, enabling/disabling quiet boot and fast boot, changing the boot order from the available bootable device(s), and setting CSM (Compatibility Support Module) configuration parameters to support legacy BIOS operation systems, various bootable devices and add-on devices for achieving better compatibility.



Boot Screen

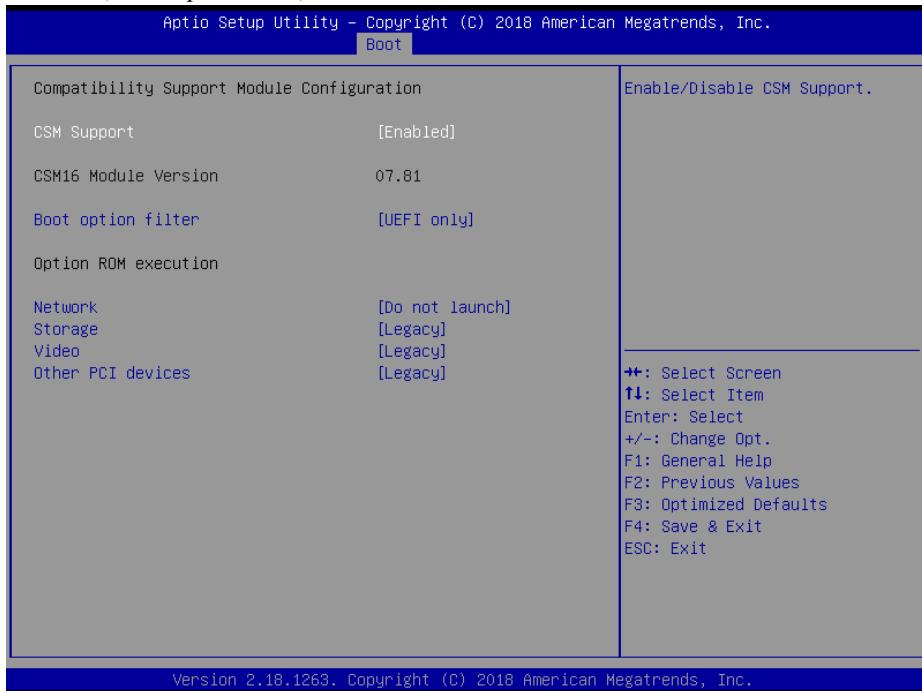
BIOS Setting	Options	Description/Purpose
Setup Prompt Timeout	Numeric (from 1 to 65535)	Number of seconds to wait for setup activation key.
Bootup NumLock State	- On - Off	Selects the NumLock state after the system is powered on. <ul style="list-style-type: none"> On: Enables the NumLock function automatically after the system is powered on. Off: Disables the NumLock function after the system is powered on.

BIOS Setting	Options	Description/Purpose
Quiet Boot	- Disabled - Enabled	Enables or Disables Quiet Boot options. When this option is set to “Disabled”, BIOS will display normal POST messages.
Fast Boot	- Disabled - Enabled	Enables or Disables Fast Boot options.
Boot Option #1~#n	- [Drive(s)] - Disabled	Allows users to change the boot order from the available device(s). Note that in the menu displayed, you will only see the device with the highest priority for a specific boot device type.
CSM Configuration	Sub-Menu	CSM configuration: Enable/Disable, Option ROM execution settings, etc.

5.7.1 Boot – CSM Configuration

Menu Path *Boot > CSM Configuration*

The **CSM Configuration** provides advanced CSM (Compatibility Support Module) configurations such as Enable/Disable CSM Support, configure Option ROM execution, boot option filter, etc.



CSM Configuration Screen

BIOS Setting	Options	Description/Purpose
CSM Support	- Disabled - Enabled	Enables or Disables CSM Support.
CSM16 Module Version	No changeable options	Displays the CSM 16 Module version.
Boot option filter	- UEFI and Legacy - UEFI only	This option controls Legacy/UEFI ROMs priority.
Network	- Do not launch - UEFI - Legacy	Controls the execution of UEFI and Legacy PXE OpROM.

BIOS Setting	Options	Description/Purpose
Storage	- Do not launch - UEFI - Legacy	Controls the execution of UEFI and Legacy Storage OpROM.
Video	- Do not launch - UEFI - Legacy	Controls the execution of UEFI and Legacy Video OpROM.
Other PCI devices	- Do not launch - UEFI - Legacy	Determines OpROM execution policy for devices other than Network, Storage or Video.

5.8 Save & Exit

Menu Path	<i>Save & Exit</i>
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The **Save & Exit** allows users to save or discard changed BIOS settings as well as load factory default settings.

Save Changed BIOS Settings

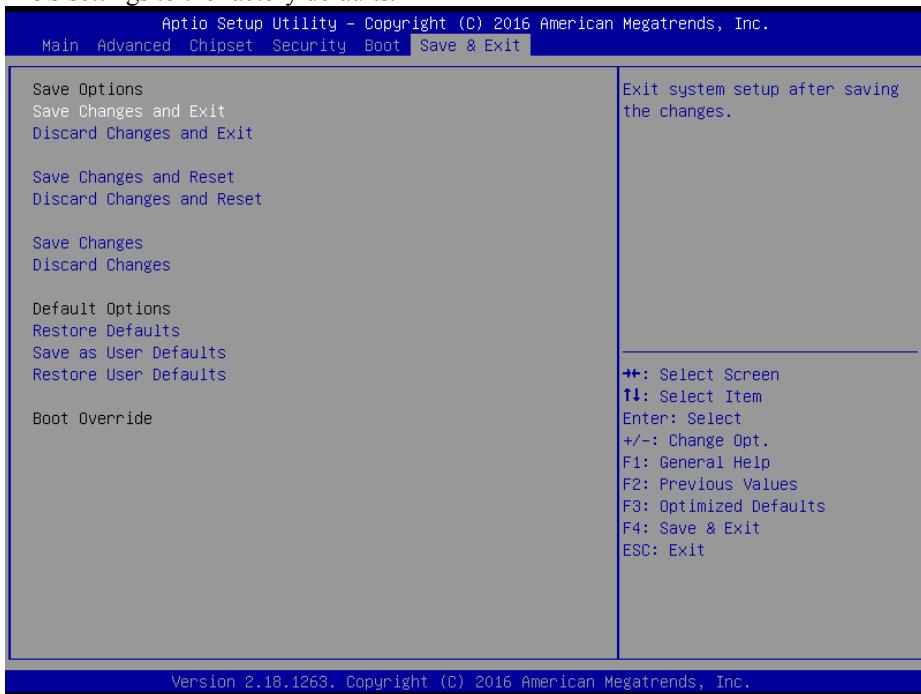
To save and validate the changed BIOS settings, select **Save Changes** from the **Save & Exit** menu, or you can select **Save Changes and Exit** (or press **F4**) to validate the changes and then exit the system. Select **Save Changes and Reset** to validate the changed BIOS settings and then restart the system

Discard Changed BIOS Settings

To cancel the BIOS settings you have previously configured, select **Discard Changes and Exit** from this menu, or simply press **Esc** to exit the BIOS setup. You can also select **Discard Changes and Reset** to discard any changes you have made and restore the factory BIOS defaults.

Load User Defaults

You may simply press **F3** at any time to load the **Optimized Values** which resets all BIOS settings to the factory defaults.



Save & Exit Screen

BIOS Setting	Options	Description/Purpose
Save Changes and Exit	No changeable options	Exits the system and saves the changes in NVRAM.
Discard Changes and Exit	No changeable options	Exits the system without saving any changes configured in BIOS settings.
Save Changes and Reset	No changeable options	Saves the changes in NVRAM and resets the system.
Discard Changes and Reset	No changeable options	Resets the system without saving any changes configured in BIOS settings.
Save Changes	No changeable options	Saves the changes done so far to any of the setup options.
Discard Changes	No changeable options	Discards the changes done so far to any of the setup options.
Restore Defaults	No changeable options	Loads the optimized defaults for BIOS settings.
Save as User Defaults	No changeable options	Saves the changes done so far as User Defaults.
Restore User Defaults	No changeable options	Restores the User Defaults to all the BIOS settings.
Boot Override	- [Drive(s)]	Forces to boot from selected [drive(s)].

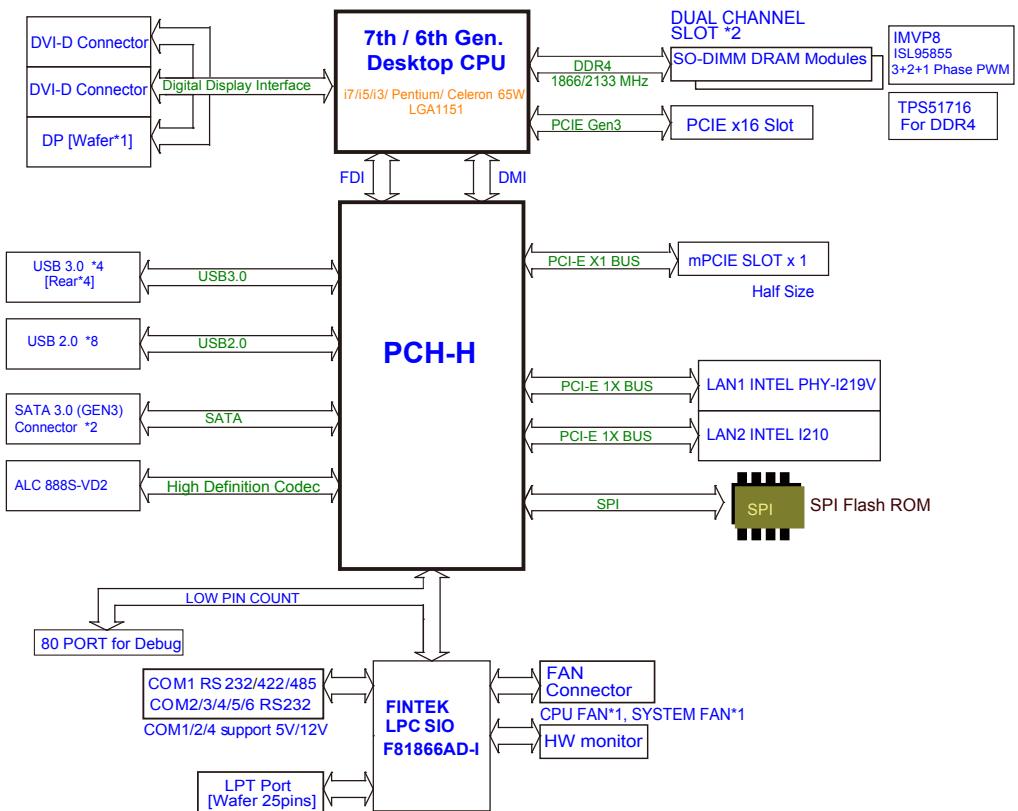
Appendix A Technical Summary

This appendix will give you a brief introduction of the allocation maps for the system resources.

The following topics are included:

- BM-2505 Block Diagram
- Interrupt Map
- I/O Map
- DMA Channels Map
- Memory Map
- Configuring WatchDog Timer
- Flash BIOS Update

BM-2505 Block Diagram



Interrupt Map

IRQ	Assignment
IRQ 0	System timer
IRQ 1	Standard PS/2 Keyboard
IRQ 3	Communications Port (COM2)
IRQ 4	Communications Port (COM1)
IRQ 6	Communications Port (COM5)
IRQ 7	Communications Port (COM3)
IRQ 8	System CMOS/real time clock
IRQ 10	Communications Port (COM4)
IRQ 11	Communications Port (COM6)
IRQ 12	PS/2 Compatible Mouse
IRQ 13	Numeric data processor
IRQ 14	Motherboard resources
IRQ 16	High Definition Audio Controller
IRQ 54	Microsoft ACPI-Compliant System
IRQ 55	Microsoft ACPI-Compliant System
IRQ 56	Microsoft ACPI-Compliant System
IRQ 57	Microsoft ACPI-Compliant System
IRQ 58	Microsoft ACPI-Compliant System
IRQ 59	Microsoft ACPI-Compliant System
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IRQ	Assignment
IRQ 79	Microsoft ACPI-Compliant System
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IRQ	Assignment
IRQ 118	Microsoft ACPI-Compliant System
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IRQ	Assignment
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IRQ	Assignment
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IRQ 300	Microsoft ACPI-Compliant System
IRQ 301	Microsoft ACPI-Compliant System
IRQ 302	Microsoft ACPI-Compliant System
IRQ 303	Microsoft ACPI-Compliant System
IRQ 304	Microsoft ACPI-Compliant System
IRQ 305	Microsoft ACPI-Compliant System
IRQ 306	Microsoft ACPI-Compliant System
IRQ 307	Microsoft ACPI-Compliant System
IRQ 308	Microsoft ACPI-Compliant System
IRQ 309	Microsoft ACPI-Compliant System
IRQ 310	Microsoft ACPI-Compliant System
IRQ 311	Microsoft ACPI-Compliant System
IRQ 312	Microsoft ACPI-Compliant System
IRQ 313	Microsoft ACPI-Compliant System
IRQ 314	Microsoft ACPI-Compliant System
IRQ 315	Microsoft ACPI-Compliant System
IRQ 316	Microsoft ACPI-Compliant System
IRQ 317	Microsoft ACPI-Compliant System
IRQ 318	Microsoft ACPI-Compliant System
IRQ 319	Microsoft ACPI-Compliant System
IRQ 320	Microsoft ACPI-Compliant System
IRQ 321	Microsoft ACPI-Compliant System
IRQ 322	Microsoft ACPI-Compliant System
IRQ 323	Microsoft ACPI-Compliant System
IRQ 324	Microsoft ACPI-Compliant System

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IRQ	Assignment
IRQ 325	Microsoft ACPI-Compliant System
IRQ 326	Microsoft ACPI-Compliant System
IRQ 327	Microsoft ACPI-Compliant System
IRQ 328	Microsoft ACPI-Compliant System
IRQ 329	Microsoft ACPI-Compliant System
IRQ 330	Microsoft ACPI-Compliant System
IRQ 331	Microsoft ACPI-Compliant System
IRQ 332	Microsoft ACPI-Compliant System
IRQ 333	Microsoft ACPI-Compliant System
IRQ 334	Microsoft ACPI-Compliant System
IRQ 335	Microsoft ACPI-Compliant System
IRQ 336	Microsoft ACPI-Compliant System
IRQ 337	Microsoft ACPI-Compliant System
IRQ 338	Microsoft ACPI-Compliant System
IRQ 339	Microsoft ACPI-Compliant System
IRQ 340	Microsoft ACPI-Compliant System
IRQ 341	Microsoft ACPI-Compliant System
IRQ 342	Microsoft ACPI-Compliant System
IRQ 343	Microsoft ACPI-Compliant System
IRQ 344	Microsoft ACPI-Compliant System
IRQ 345	Microsoft ACPI-Compliant System
IRQ 346	Microsoft ACPI-Compliant System
IRQ 347	Microsoft ACPI-Compliant System
IRQ 348	Microsoft ACPI-Compliant System
IRQ 349	Microsoft ACPI-Compliant System
IRQ 350	Microsoft ACPI-Compliant System
IRQ 351	Microsoft ACPI-Compliant System
IRQ 352	Microsoft ACPI-Compliant System
IRQ 353	Microsoft ACPI-Compliant System
IRQ 354	Microsoft ACPI-Compliant System
IRQ 355	Microsoft ACPI-Compliant System
IRQ 356	Microsoft ACPI-Compliant System
IRQ 357	Microsoft ACPI-Compliant System
IRQ 358	Microsoft ACPI-Compliant System
IRQ 359	Microsoft ACPI-Compliant System
IRQ 360	Microsoft ACPI-Compliant System
IRQ 361	Microsoft ACPI-Compliant System
IRQ 362	Microsoft ACPI-Compliant System
IRQ 363	Microsoft ACPI-Compliant System

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IRQ	Assignment
IRQ 364	Microsoft ACPI-Compliant System
IRQ 365	Microsoft ACPI-Compliant System
IRQ 366	Microsoft ACPI-Compliant System
IRQ 367	Microsoft ACPI-Compliant System
IRQ 368	Microsoft ACPI-Compliant System
IRQ 369	Microsoft ACPI-Compliant System
IRQ 370	Microsoft ACPI-Compliant System
IRQ 371	Microsoft ACPI-Compliant System
IRQ 372	Microsoft ACPI-Compliant System
IRQ 373	Microsoft ACPI-Compliant System
IRQ 374	Microsoft ACPI-Compliant System
IRQ 375	Microsoft ACPI-Compliant System
IRQ 376	Microsoft ACPI-Compliant System
IRQ 377	Microsoft ACPI-Compliant System
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IRQ 379	Microsoft ACPI-Compliant System
IRQ 380	Microsoft ACPI-Compliant System
IRQ 381	Microsoft ACPI-Compliant System
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IRQ 384	Microsoft ACPI-Compliant System
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IRQ 388	Microsoft ACPI-Compliant System
IRQ 389	Microsoft ACPI-Compliant System
IRQ 390	Microsoft ACPI-Compliant System
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IRQ 392	Microsoft ACPI-Compliant System
IRQ 393	Microsoft ACPI-Compliant System
IRQ 394	Microsoft ACPI-Compliant System
IRQ 395	Microsoft ACPI-Compliant System
IRQ 396	Microsoft ACPI-Compliant System
IRQ 397	Microsoft ACPI-Compliant System
IRQ 398	Microsoft ACPI-Compliant System
IRQ 399	Microsoft ACPI-Compliant System
IRQ 400	Microsoft ACPI-Compliant System
IRQ 401	Microsoft ACPI-Compliant System
IRQ 402	Microsoft ACPI-Compliant System

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IRQ	Assignment
IRQ 403	Microsoft ACPI-Compliant System
IRQ 404	Microsoft ACPI-Compliant System
IRQ 405	Microsoft ACPI-Compliant System
IRQ 406	Microsoft ACPI-Compliant System
IRQ 407	Microsoft ACPI-Compliant System
IRQ 408	Microsoft ACPI-Compliant System
IRQ 409	Microsoft ACPI-Compliant System
IRQ 410	Microsoft ACPI-Compliant System
IRQ 411	Microsoft ACPI-Compliant System
IRQ 412	Microsoft ACPI-Compliant System
IRQ 413	Microsoft ACPI-Compliant System
IRQ 414	Microsoft ACPI-Compliant System
IRQ 415	Microsoft ACPI-Compliant System
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IRQ 417	Microsoft ACPI-Compliant System
IRQ 418	Microsoft ACPI-Compliant System
IRQ 419	Microsoft ACPI-Compliant System
IRQ 420	Microsoft ACPI-Compliant System
IRQ 421	Microsoft ACPI-Compliant System
IRQ 422	Microsoft ACPI-Compliant System
IRQ 423	Microsoft ACPI-Compliant System
IRQ 424	Microsoft ACPI-Compliant System
IRQ 425	Microsoft ACPI-Compliant System
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IRQ 427	Microsoft ACPI-Compliant System
IRQ 428	Microsoft ACPI-Compliant System
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IRQ 431	Microsoft ACPI-Compliant System
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IRQ 433	Microsoft ACPI-Compliant System
IRQ 434	Microsoft ACPI-Compliant System
IRQ 435	Microsoft ACPI-Compliant System
IRQ 436	Microsoft ACPI-Compliant System
IRQ 437	Microsoft ACPI-Compliant System
IRQ 438	Microsoft ACPI-Compliant System
IRQ 439	Microsoft ACPI-Compliant System
IRQ 440	Microsoft ACPI-Compliant System
IRQ 441	Microsoft ACPI-Compliant System

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IRQ	Assignment
IRQ 442	Microsoft ACPI-Compliant System
IRQ 443	Microsoft ACPI-Compliant System
IRQ 444	Microsoft ACPI-Compliant System
IRQ 445	Microsoft ACPI-Compliant System
IRQ 446	Microsoft ACPI-Compliant System
IRQ 447	Microsoft ACPI-Compliant System
IRQ 448	Microsoft ACPI-Compliant System
IRQ 449	Microsoft ACPI-Compliant System
IRQ 450	Microsoft ACPI-Compliant System
IRQ 451	Microsoft ACPI-Compliant System
IRQ 452	Microsoft ACPI-Compliant System
IRQ 453	Microsoft ACPI-Compliant System
IRQ 454	Microsoft ACPI-Compliant System
IRQ 455	Microsoft ACPI-Compliant System
IRQ 456	Microsoft ACPI-Compliant System
IRQ 457	Microsoft ACPI-Compliant System
IRQ 458	Microsoft ACPI-Compliant System
IRQ 459	Microsoft ACPI-Compliant System
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IRQ 461	Microsoft ACPI-Compliant System
IRQ 462	Microsoft ACPI-Compliant System
IRQ 463	Microsoft ACPI-Compliant System
IRQ 464	Microsoft ACPI-Compliant System
IRQ 465	Microsoft ACPI-Compliant System
IRQ 466	Microsoft ACPI-Compliant System
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IRQ 468	Microsoft ACPI-Compliant System
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IRQ 470	Microsoft ACPI-Compliant System
IRQ 471	Microsoft ACPI-Compliant System
IRQ 472	Microsoft ACPI-Compliant System
IRQ 473	Microsoft ACPI-Compliant System
IRQ 474	Microsoft ACPI-Compliant System
IRQ 475	Microsoft ACPI-Compliant System
IRQ 476	Microsoft ACPI-Compliant System
IRQ 477	Microsoft ACPI-Compliant System
IRQ 478	Microsoft ACPI-Compliant System
IRQ 479	Microsoft ACPI-Compliant System
IRQ 480	Microsoft ACPI-Compliant System

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IRQ	Assignment
IRQ 481	Microsoft ACPI-Compliant System
IRQ 482	Microsoft ACPI-Compliant System
IRQ 483	Microsoft ACPI-Compliant System
IRQ 484	Microsoft ACPI-Compliant System
IRQ 485	Microsoft ACPI-Compliant System
IRQ 486	Microsoft ACPI-Compliant System
IRQ 487	Microsoft ACPI-Compliant System
IRQ 488	Microsoft ACPI-Compliant System
IRQ 489	Microsoft ACPI-Compliant System
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IRQ 497	Microsoft ACPI-Compliant System
IRQ 498	Microsoft ACPI-Compliant System
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IRQ 500	Microsoft ACPI-Compliant System
IRQ 501	Microsoft ACPI-Compliant System
IRQ 502	Microsoft ACPI-Compliant System
IRQ 503	Microsoft ACPI-Compliant System
IRQ 504	Microsoft ACPI-Compliant System
IRQ 505	Microsoft ACPI-Compliant System
IRQ 506	Microsoft ACPI-Compliant System
IRQ 507	Microsoft ACPI-Compliant System
IRQ 508	Microsoft ACPI-Compliant System
IRQ 509	Microsoft ACPI-Compliant System
IRQ 510	Microsoft ACPI-Compliant System
IRQ 511	Microsoft ACPI-Compliant System
IRQ 4294967283	Intel(R) Management Engine Interface
IRQ 4294967284	Intel(R) I210 Gigabit Network Connection
IRQ 4294967285	Intel(R) I210 Gigabit Network Connection
IRQ 4294967286	Intel(R) I210 Gigabit Network Connection
IRQ 4294967287	Intel(R) I210 Gigabit Network Connection
IRQ 4294967288	Intel(R) I210 Gigabit Network Connection
IRQ 4294967289	Intel(R) I210 Gigabit Network Connection
IRQ 4294967290	Intel(R) USB 3.0 eXtensible Host Controller - 1.0

Appendix A Technical Summary

IRQ	Assignment
	(Microsoft)
IRQ 4294967291	Intel(R) HD Graphics 630
IRQ 4294967292	Intel(R) Ethernet Connection (2) I219-V
IRQ 4294967293	Standard SATA AHCI Controller
IRQ 4294967294	Intel(R) 100 Series/C230 Series Chipset Family PCI Express Root Port #6 - A115

I/O MAP

I/O Map	Assignment
0x00000000-0x00000CF7	PCI Express Root Complex
0x00000020-0x00000021	Programmable interrupt controller
0x00000024-0x00000025	Programmable interrupt controller
0x00000028-0x00000029	Programmable interrupt controller
0x0000002C-0x0000002D	Programmable interrupt controller
0x0000002E-0x0000002F	Motherboard resources
0x00000030-0x00000031	Programmable interrupt controller
0x00000034-0x00000035	Programmable interrupt controller
0x00000038-0x00000039	Programmable interrupt controller
0x0000003C-0x0000003D	Programmable interrupt controller
0x00000040-0x00000043	System timer
0x0000004E-0x0000004F	Motherboard resources
0x00000050-0x00000053	System timer
0x00000060-0x00000060	Standard PS/2 Keyboard
0x00000061-0x00000061	Motherboard resources
0x00000063-0x00000063	Motherboard resources
0x00000064-0x00000064	Standard PS/2 Keyboard
0x00000065-0x00000065	Motherboard resources
0x00000067-0x00000067	Motherboard resources
0x00000070-0x00000070	Motherboard resources
0x00000070-0x00000070	System CMOS/real time clock
0x00000080-0x00000080	Motherboard resources
0x00000092-0x00000092	Motherboard resources
0x000000A0-0x000000A1	Programmable interrupt controller
0x000000A4-0x000000A5	Programmable interrupt controller
0x000000A8-0x000000A9	Programmable interrupt controller
0x000000AC-0x000000AD	Programmable interrupt controller
0x000000B0-0x000000B1	Programmable interrupt controller
0x000000B2-0x000000B3	Motherboard resources
0x000000B4-0x000000B5	Programmable interrupt controller
0x000000B8-0x000000B9	Programmable interrupt controller
0x000000BC-0x000000BD	Programmable interrupt controller
0x000000F0-0x000000F0	Numeric data processor
0x000002E0-0x000002E7	Communications Port (COM6)
0x000002E8-0x000002EF	Communications Port (COM4)
0x000002F0-0x000002F7	Communications Port (COM5)
0x000002F8-0x000002FF	Communications Port (COM2)

I/O Map	Assignment
0x000000378-0x00000037F	Printer Port (LPT1)
0x0000003B0-0x0000003BB	Intel(R) HD Graphics 630
0x0000003C0-0x0000003DF	Intel(R) HD Graphics 630
0x0000003E8-0x0000003EF	Communications Port (COM3)
0x0000003F8-0x0000003FF	Communications Port (COM1)
0x0000004D0-0x0000004D1	Programmable interrupt controller
0x000000680-0x00000069F	Motherboard resources
0x000000800-0x00000087F	Motherboard resources
0x000000A00-0x000000A0F	Motherboard resources
0x000000A10-0x000000A1F	Motherboard resources
0x000000A20-0x000000A2F	Motherboard resources
0x000000D00-0x0000FFFF	PCI Express Root Complex
0x0000164E-0x0000164F	Motherboard resources
0x00001800-0x000018FE	Motherboard resources
0x00001854-0x00001857	Motherboard resources
0x0000E000-0x0000EFFF	Intel(R) 100 Series/C230 Series Chipset Family PCI Express Root Port #6 - A115
0x0000F000-0x0000F03F	Intel(R) HD Graphics 630
0x0000F040-0x0000F05F	Intel(R) 100 Series/C230 Series Chipset Family SMBus - A123
0x0000F060-0x0000F07F	Standard SATA AHCI Controller
0x0000F080-0x0000F083	Standard SATA AHCI Controller
0x0000F090-0x0000F097	Standard SATA AHCI Controller
0x0000FF00-0x0000FFE	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources

DMA Channels Map

DMA Channels Map	Assignment
Channel 3	Printer Port (LPT1)

Memory Map

Memory Map	Assignment
0xFED10000-0xFED17FFF	Motherboard resources
0xFED18000-0xFED18FFF	Motherboard resources
0xFED19000-0xFED19FFF	Motherboard resources
0xE0000000-0xFFFFFFFF	Motherboard resources
0xFED20000-0xFED3FFFF	Motherboard resources
0xFED90000-0xFED93FFF	Motherboard resources
0xFED45000-0xFED8FFFF	Motherboard resources
0xFF000000-0xFFFFFFFF	Motherboard resources
0xFF000000-0xFFFFFFFF	Legacy device
0xFEE00000-0xFEEFFFFFF	Motherboard resources
0xDFFE0000-0xDFFFFFFF	Motherboard resources
0xFDAF0000-0xFDAFFFFFF	Motherboard resources
0xFDAE0000-0xFDAEFFFF	Motherboard resources
0xFDAC0000-0xFDACFFFF	Motherboard resources
0xDE000000-0xDEFFFFFF	Intel(R) HD Graphics 630
0xC0000000-0xCFFFFFFF	Intel(R) HD Graphics 630
0xDF000000-0xDF0FFFFF	Intel(R) 100 Series/C230 Series Chipset Family PCI Express Root Port #6 - A115
0xDF000000-0xDF0FFFFF	Intel(R) I210 Gigabit Network Connection
0xFED00000-0xFED003FF	High precision event timer
0xDF148000-0xDF149FFF	Standard SATA AHCI Controller
0xDF14C000-0xDF14C0FF	Standard SATA AHCI Controller
0xDF14B000-0xDF14B7FF	Standard SATA AHCI Controller
0xFD000000-0xFDABFFFF	Motherboard resources
0xFD000000-0xFDABFFFF	PCI Express Root Complex
0xFDAD0000-0xFDADFFFF	Motherboard resources
0xFDB00000-0xFDFFFFFF	Motherboard resources
0xFE000000-0xFE01FFFF	Motherboard resources
0xFE036000-0xFE03BFFF	Motherboard resources
0xFE03D000-0xFE3FFFFFF	Motherboard resources
0xFE410000-0xFE7FFFFFF	Motherboard resources
0xDF144000-0xDF147FFF	Intel(R) 100 Series/C230 Series Chipset Family PMC - A121
0x90000000-0xDFFFFFFF	PCI Express Root Complex
0xFE40F000-0xFE40FFFF	Intel(R) Management Engine Interface

Appendix A Technical Summary

Memory Map	Assignment
0xDF130000-0xDF13FFFF	Intel(R) USB 3.0 eXtensible Host Controller - 1.0 (Microsoft)
0xDF14A000-0xDF14A0FF	Intel(R) 100 Series/C230 Series Chipset Family SMBus - A123
0xDF080000-0xDF083FFF	Intel(R) I210 Gigabit Network Connection
0xDF100000-0xDF11FFFF	Intel(R) Ethernet Connection (2) I219-V
0xDF14E000-0xDF14EFFF	Intel(R) 100 Series/C230 Series Chipset Family Thermal subsystem - A131
0xDF140000-0xDF143FFF	High Definition Audio Controller
0xDF120000-0xDF12FFFF	High Definition Audio Controller
0xA0000-0xBFFFF	Intel(R) HD Graphics 630
0xA0000-0xBFFFF	PCI Express Root Complex

Configuring WatchDog Timer

The I/O port address of the watchdog timer is 2E (hex) and 2F (hex). 2E (hex) is the address port. 2F (hex) is the data port. User must first assign the address of register by writing address value into address port 2E (hex), then write/read data to/from the assigned register through data port 2F (hex).

Configuration Sequence

To program F81866 configuration registers, the following configuration sequence must be followed:

(1) Enter the extended function mode

To place the chip into the Extended Function Mode, two successive writes of 0x87 must be applied to Extended Function Enable Registers (EFERs, i.e. 2Eh or 4Eh).

(2) Configure the configuration registers

The chip selects the Logical Device and activates the desired Logical Devices through Extended Function Index Register (EFIR) and Extended Function Data Register (EFDR). The EFIR is located at the same address as the EFER, and the EFDR is located at address (EFIR+1). First, write the Logical Device Number (i.e. 0x07) to the EFIR and then write the number of the desired Logical Device to the EFDR. If accessing the Chip (Global) Control Registers, this step is not required. Secondly, write the address of the desired configuration register within the Logical Device to the EFIR and then write (or read) the desired configuration register through the EFDR.

(3) Exit the extended function mode

To exit the Extended Function Mode, writing 0xAA to the EFER is required. Once the chip exits the Extended Function Mode, it is in the normal running mode and is ready to enter the configuration mode.

Code example for watch dog timer

Enable watchdog timer and set timeout interval to 30 seconds.

```
; ----- Enter to extended function mode -----
mov dx, 2eh
mov al, 87h
out dx, al
out dx, al

; ----- Select Logical Device 7 of watchdog timer -----
mov al, 07h
out dx, al
inc dx
mov al, 07h
out dx, al

; ----- Enable Watch dog feature -----
mov al, 030h
out dx, al
inc dx
mov al, 01h
out dx, al

; ----- Set timeout interval as 30 seconds -----
dec dx
mov al, 0F6h
out dx, al
inc dx
mov al, 1Eh
out dx, al

; ----- Enable Watch PME-----
dec dx
mov al, 0FAh
out dx, al
inc dx
in al, dx
or al, 51h
out dx, al

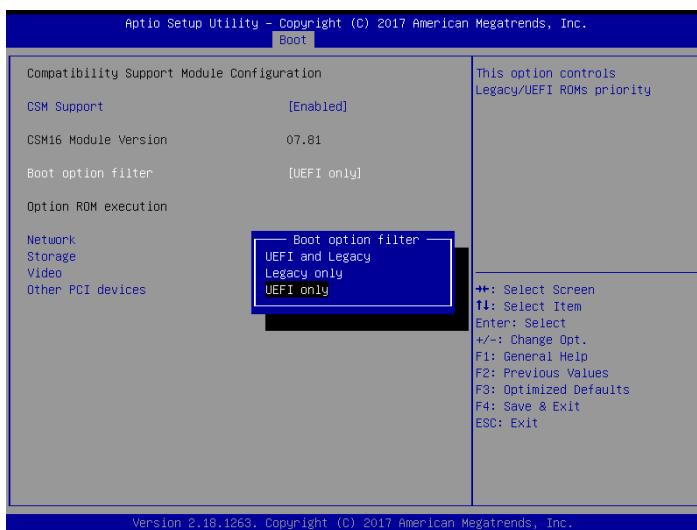
; ----- Set second as counting unit and start counting -----
dec dx
mov al, 0F5h
out dx, al
inc dx
in al, dx
and al, 0F7h
or al, 20h
out dx, al

; ----- Exit the extended function mode -----
dec dx
mov al, 0AAh
out dx, al
```

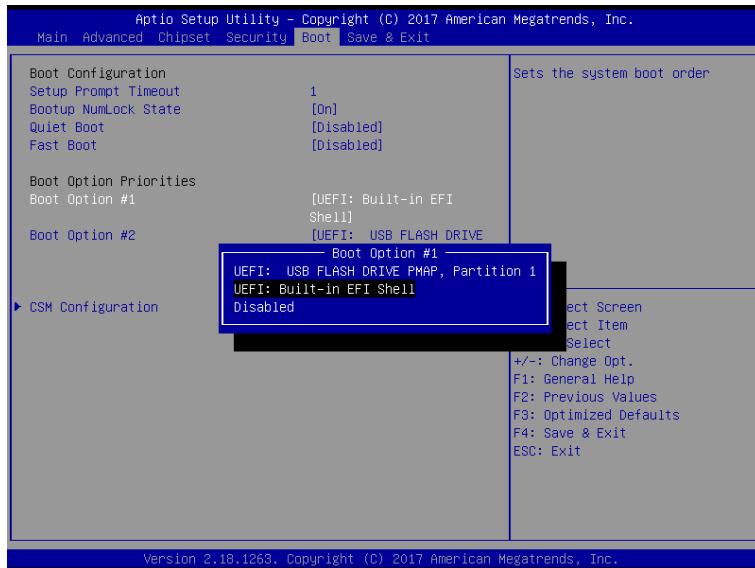
Flash BIOS Update

I. Prerequisites

- 1** Prepare a USB storage device to save the required files for BIOS update.
- 2** Download and save the BIOS file (e.g. 25050PH1.bin) to the storage device.
- 3** Copy AMI flash utility – AFUEFIx64.exe (v5.09.01) into the storage device. The utility and BIOS file should be saved to the same path.
- 4** Make sure the targeted system can first boot to the EFI shell environment.
 - (1) Connect the USB storage device.
 - (2) Turn on the computer and press <ESC> or key during boot to enter BIOS Setup.
 - (3) The system will go into the BIOS setup menu.
 - (4) Select **[Boot]** menu and enter into **[CSM Configuration]** menu.
 - (5) Set **[Boot option filter]** to **[UEFI Only]** and press <F4> to save the configuration and restart the system.



- (6) Press <ESC> or to enter BIOS setup menu again.
- (7) Select [Boot] menu and set [**UEFI: Built-in EFI Shell**] as the 1st boot device.
- (8) Press <F4> to save the configuration and restart the system to boot into EFI Shell environment.



II. AFUDOS Command for System BIOS Update

AFUEFIx64.efi is the AMI firmware update utility. The command line is shown as below:

AFUEFIx64 <ROM File Name> [option1] [option2]....

Users can type “**AFUEFIx64 /?**” to view the definition of each control option. The recommended options for BIOS ROM update include the following parameters:

/P: Program main BIOS image.

/B: Program Boot Block.

/N: Program NVRAM.

/X: Don’t check ROM ID.

III. BIOS Update Procedure

- 1** Boot into EFI Shell, change to the path where you put BIOS image and AFUEFIx64.

```
Shell> fs0:  
fs0:\> cd afuefix64
```

- 2** Type "AFUEFIx64 2505xxxx.bin /p /b /n /x" and press Enter to start the flash procedure. (xxxx means the BIOS revision part, e.g. 0PH1...)
- 3** During the update procedure, you will see the BIOS update process status and the percentage it has been updated. **Beware!** Do not turn off system power or reset your computer if the whole update procedure is not completed yet, or it may crash the BIOS ROM and the system will be unable to boot up next time.
- 4** After BIOS update procedure is completed, the following messages will be shown:

```
fs0:\afuefix64> afuefix64 25050PH1.bin /p /b /n /x
+-----+
|          AMI Firmware Update Utility v5.09.01.1317
|  Copyright (C) 2016 American Megatrends Inc. All Rights Reserved.
+-----+
Reading flash ..... done
- ME Data Size Checking - OK
- FFS checksums ..... OK.
Erasing Boot Block ..... done
Updating Boot Block ..... done
Verifying Boot Block ..... done
Erasing Main Block ..... done
Updating Main Block ..... done
Verifying Main Block ..... done
Erasing NVRAM Block ..... done
Updating NVRAM Block ..... done
Verifying NVRAM Block ..... done

fs0:\afuefix64
```

- 5 Restart the system and boot up with the new BIOS configuration.
- 6 The BIOS Update is completed after the system is restarted.
- 7 Reboot the system and verify if the BIOS version shown on the initialization screen has been updated.

